LOGICAL I/O COMMAND #6 (+/-COMMAND#)

NOTE: LOGIC FOR

YELLOW

CLEARANCE

(HEAD 51).

FROM PHASE 5

ARROW

IF YELLOW ON PHASE #5 IS ON

SCROLL DOWN

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

SET OUTPUT ASSIGNMENT #43 ON

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0840 DESIGNED: May 2022 SEALED: 5/17/2024 REVISED:

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

PROJECT REFERENCE NO.

U-6202

GREEN

FLASH

12345678910111213141516

PAGE 2: VEHICLE OVERLAP 'E' SETTINGS

VEH OVL GRN EXT: : STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0

OVERLAP PROGRAMMING COMPLETE

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....0 YFILOW CLEAR (O=PARENT.3-25.5 SEC)..0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0

PHASE:

VEH OVL PARENTS:

VEH OVL NOT VEH:

VEH OVL NOT PED:

112345678910111213141516

**←** NOTICE

FLASH

## OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow

OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red

OUTPUT 51 = Overlap A Yellow

OUTPUT 52 = Overlap A Green

Prepared in the Offices of:

ELECTRICAL AND PROGRAMMIN

SR 2048 (Gordon Rd)

SR 2117 (Harris Rd) ivision 3 New Hanover County

August 2023 REVIEWED BY: N.K. Vlanich PLAN DATE: PREPARED BY: E.E. Tiller REVIEWED BY: N.R. Simmons REVISIONS

TH CARO, OR WESSION 1 031464 ENCINEER Notasha R Simmons 5/17/202 SIGNATURE DATE

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DETAILS FOR:

Signal Upgrade - Final Design

Electrical Detail - Sheet 2 of 6

Wilmingtor INIT. DATE

SIG. INVENTORY NO. 03-0840