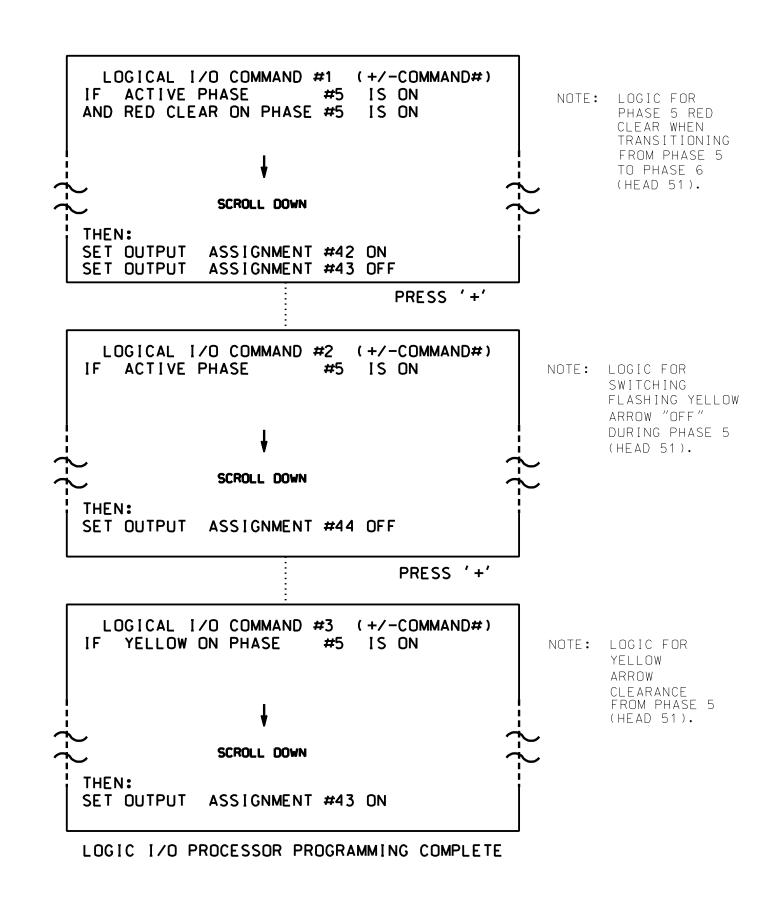
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



	PROJECT REFERENCE NO.	SHEET NO.
	U-6202	Sig. 8.2
OGRAMMING DETAIL FOR DEFAULT PHASING (program controller as shown below)		
FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS '+' TWICE		
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)00 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0.0 CUTPUT AS PHASE # (0=NONE. 1-16)0 OVERLAP PROGRAMMING COMPLETE		
BRAMMING DETAIL FOR ALTERNATE PHASING		
(program controller as shown below) FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.		
PRESS '+' TWICE		
PAGE 2: VEHICLE OVERLAP 'C' SETTINGS PHASE: : 12345678910111213141516 VEH OVL PARENTS:: X VEH OVL NOT VEH::		
VEH OVL NOT PED:: VEH OVL GRN EXT:: STARTUP COLOR: _ RED _ YELLOW _ GREEN		
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OVERLAP PROGRAMMING COMPLETE		
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DETAILS FOR: SR 2048 (Gordon Rd) Prepared in the Offices of: at	TH CAR	
I-40 WB Ramps	A CR FESSION SEAL	
Division 3 New Hanover County Wilmin PLAN DATE: August 2023 REVIEWED BY: N.K. Vlan:	ngton 031464	S
O PREPARED BY: E.E. Tiller REVIEWED BY: N.R. Simmo	On S MCINEE	MMM S
750 N.Greenfield Pkwy.Garner, NC 27529 INTI.	DATE DocuSigned by: Matasha R Simmon SIGNA TURE	
	SIG. INVENTORY NO.	03-0258

	PROJECT REFERENCE NO.	SHEET NO.
	U-6202	Sig. 8.2
RAMMING DETAIL FOR DEFAULT PHASING		
(program controller as shown below)		
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AMMING DETAIL FOR ALTERNATE PHASING		
(program controller as shown below)		
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Signal Upgrade - Final Design		
Electrical Detail Sheet 2 of 1 DOCUMENT	NOT CONSIDERED F	
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Details for: Prepared in the Offices of: SR 2048 (Gordon Rd) at	11 ¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹¹	11111111111111111111111111111111111111
Prepared in the Offices of: Mobility and I - 40 WB Ramps	PRIFESSIO	N N N
	SEAL	
PLAN DATE: August 2023 REVIEWED BY: N.K. Vlan:	ich Z	SA/
	DATE DocuSigned by:	
750 N.Greenfield Pkwy.Garner.NC 27529	DocuSigned by: """""""""""""""""""""""""""""""""""	
	SIGNATORE FOLA880F3AU443A SIG. INVENTORY NO.	03-0258

	PROJE	U-6202	SHEET NO. Sig. 8.2
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DETAILS FOR: SR 2048 (Gordon Rd) Prepared in the Offices of: at I - 40 WB Ramps		OR THE CAR	
PLAN DATE: August 2023 REVIEWED BY: N.K. Vlan PREPARED BY: E.E. Tiller REVIEWED BY: N.R. Simm		SEAL 031464 ACINES ACINES ACINES	A
9 9 750 N.Greenfield Pkwy.Garner.NC 27529		-DocuSigned by:""""""""""""""""""""""""""""""""""""	<u>5/17/202</u> 4 <u>DATE</u> 03-0258
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			PROJECT REFERENCE NO.	SHEET NO.
			U-6202	Sig. 8.2
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	FF TH PF NOTICE → PAGE 2 V V SF SF G Y R	AMMING DETAIL FOR ALTERNATE PHASING (program controller as shown below) ROM MAIN MENU PRESS '8' (OVERLAPS). HEN '1' (VEHICLE OVERLAP SETTINGS). RESS 'NEXT' TO ADVANCE TO PAGE 2. PRESS '+' TWICE PAGE 2: VEHICLE OVERLAP 'C' SETTINGS PHASE: '12345678910111213141516 EH OVL NOT VEH:: 'EH OVL NOT VEH:: 'EH OVL NOT VEH:: EEL OVLORS: _ RED _ YELLOW _ GREEN LASH COLORS: _ RED _ YELLOW _ GREEN LASH YELLOW IN CONTROLLER FLASH?Y REEN EXTENSION (0-255 SEC)0 UIFUT AS PHASE # (0=NONE, 1-16)0 OVERLAP PROGRAMMING COMPLETE		
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OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green