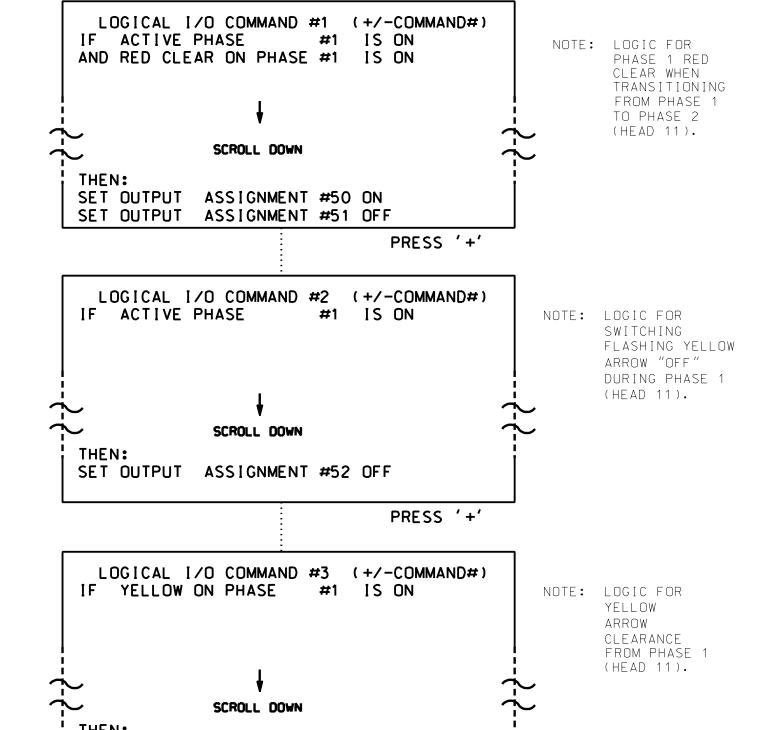
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

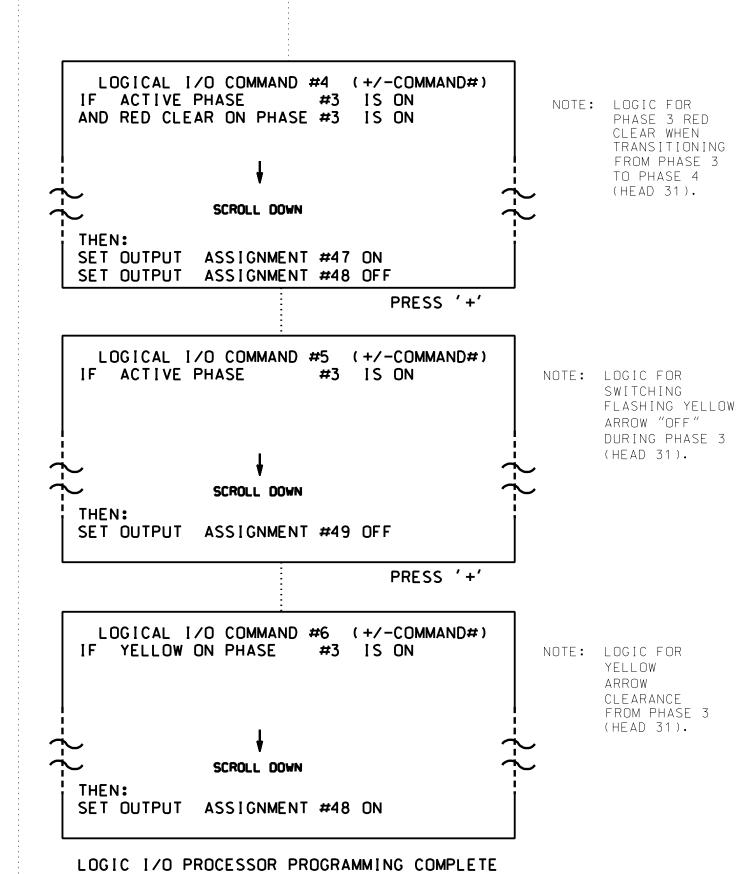
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



PRESS '+'

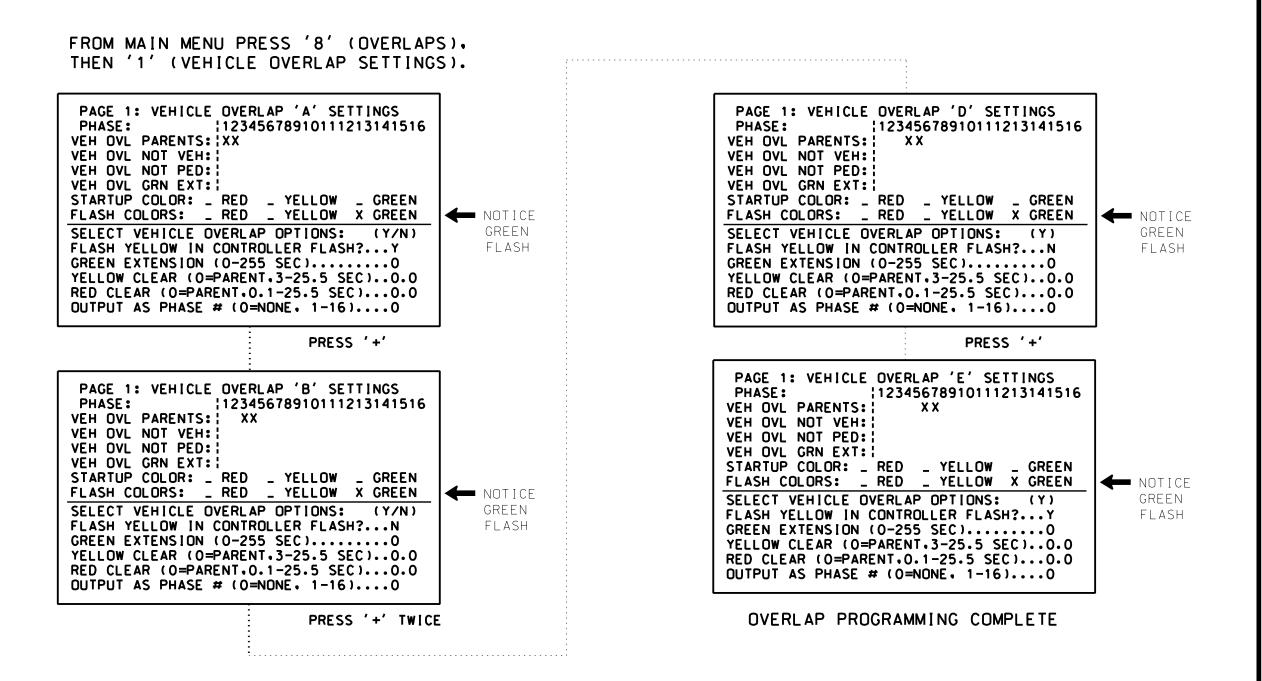
SET OUTPUT ASSIGNMENT #51 ON



OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

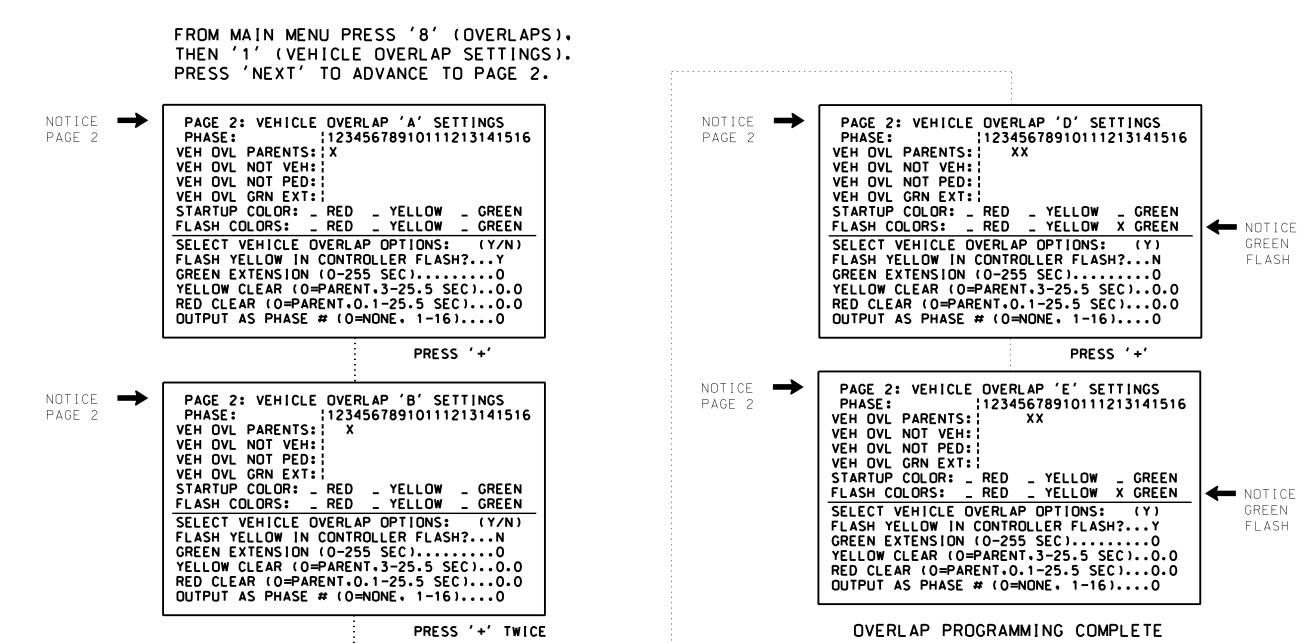
U-6202 Sig. 5.3

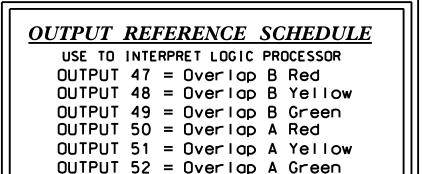
(program controller as shown below)



OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)





Signal Upgrade - Final Design Electrical Detail - Sheet 3 of 7 THE SIGNAL DESIGN: 03-0331

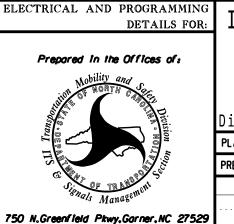
DESIGNED: May 2022

SEALED: 5/17/2024

REVISED:

THIS ELECTRICAL DETAIL IS FOR

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



I-40 EB Ramp / US 117 - NC 132 (N. College Road) at SR 2048 (Gordon Rd)

REVISIONS

SR 2048 (Gordon Rd)

Division 3 New Hanover County Wilmington

PLAN DATE: August 2023 REVIEWED BY: N.K. Vlanich

PREPARED BY: E.E. Tiller REVIEWED BY: N.R. Simmons

INIT. DATE

DocuSigned by:

Motasha R Simmons
SIGNATURE

S1G. INVENTORY NO. 03-0331

TH CARO

STESSION !!

031464

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