

Metal Pole #2

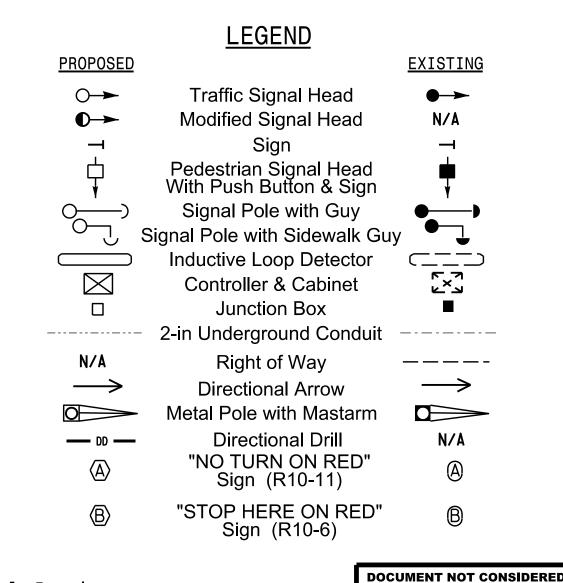
63' LT. +/-

-L- Sta. 30+07+/-

5 Phase Fully Actuated (Isolated)

<u>NOTES</u>

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or Phase 5 may be lagged.
- 4. Set all detector units to presence mode.



M **MOTT MACDONALD** MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200

RALEIGH, NC 27606

License No. F-0669

Metal Pole #3

65' RT. +/-

-L- Sta. 31+02 +/-

Signal Upgrade - Final Design

NC 73-211/NC 211 NC 73/SR 1133 (Mode Rd)

Division 8 Moore County REVIEWED BY: R. Mullinax PLAN DATE: June 2024 750 N.Greenfield Pkwy.Garner.NC 27529 PREPARED BY: LD Stouchko REVIEWED BY: INIT. DATE

FINAL UNLESS ALL SIGNATURES COMPLETED 034437

SIG. INVENTORY NO. 08-0098

SIGNATURE

_ Ped Clear * Min Green 10 2.0 2.0 2.0 2.0 5.0 5.0 20 45 30 20 45 30 3.0 3.8 3.0 Yellow Change 4.4 3.8 4.4 Red Clear 1.5 2.5 2.3 1.5 2.5 Added Initial * 1.5 1.5 Maximum Initial * 24 24 Time Before Reduction 15 15 Time To Reduce * 30 3.0 Minimum Gap 3.0 Advance Walk Χ Non Lock Detector Χ _ Vehicle Recall MIN RECALL MIN RECALL Dual Entry * These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6

2

MAXTIME TIMING CHART

PHASE

PHASING DIAGRAM

PHASING DIAGRAM DETECTION LEGEND

DETECTED MOVEMENT

UNSIGNALIZED MOVEMENT PEDESTRIAN MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

21,22,23 41,42,43 61,62,63

81,82,83

Metal Pole #1

59' LT. +/-

-L- Sta. 28+56+/-

Metal Pole #4

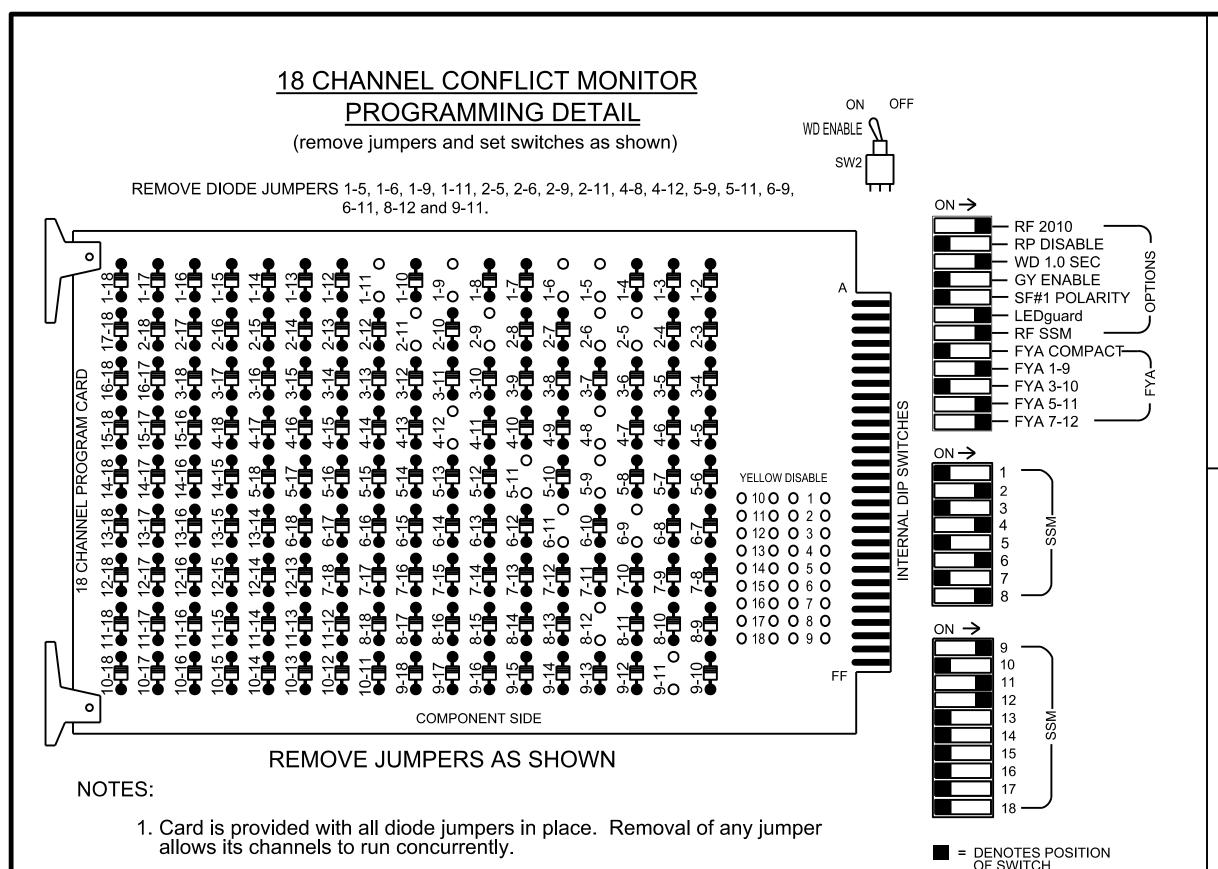
69' RT. +/-

-L- Sta. 29+71+/-

2+5

lower than what is shown. Min Green for all other phases should not be lower than 4 seconds

FEATURE



NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S5, S7, S8, S11, AUX S1,
	AUX S4, AUX S5
Phases Used	1, 2, 4, 5, 6, 8
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	*

*See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART S8 S9 S10 S11 S12 AUX S1 AUX S2 AUX S3 AUX S4 AUX S5 S6 LOAD SWITCH NO CMU CHANNEL NO. 15 10 | 17 | 8 OL1 OL2 SPARE OL3 OL4 SPARE PHASE SIGNAL HEAD NO. NU NU 41,42 NU 101 134 RED 107 ***** 135 102 YELLOW 108 103 136 109 130 GREEN A114 A101 ARROW YELLOW A115 A102 A122 ARROW FLASHING YELLOW ARROW A116 A103 GREEN ARROW 133

PROJECT REFERENCE NO.

R-5726

- NU = Not Used
- * Denotes install load resistor. See load resistor installation detail this sheet.
- ★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

FS = FLASH SENSE ST = STOP TIME

Phase 5 Yellow Field

Terminal (132)

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1	ø 2	S L C	SL	SL	Ø 4	SL	8 L	SL	8 L	SL	SL	S L	FS
FILE	1A	2A	O T	OT	O T	4A	OT	O T	O T	ÖT	O T	ÖT	O T	DC ISOLATOR
" "	NOT	ø 2	E M P	E M P	E M P	Ø 4	E M P T	E M P	E M P	E M P	E M P	E M P	E M P	ST
L	USED	2B	T Y	T Y	T Y	4B	T Y	T Y	T Y	T Y	T Y	T Y	T Y	DC ISOLATOR
	ø 5	ø6	S	S	S	ø 8	S	S	S	S	S	S	S	S
FILE U	5A	<i>.</i> 6A	Ö	O T	Ö	, 8A	ŎŢ	Ŏ	ŎŢ	T OT	Ö	Ö	, P	Ö
"J" ˌ	NOT	ø6	E M P	E M P	E M P	NOT	E M P	E M	E M P	E M P	E M	E M P	E M P	E M P
L	USED	6B	T Y	T Y	T Y	USED	T Y	T Y	T Y	T Y	T Y	T Y	T Y	T
Į.														

EX.: 1A, 2A, ETC. = LOOP NO.'S

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES

Value (ohms) Wattage

1.5K - 1.9K | 25W (min)

2.0K - 3.0K | 10W (min)

Phase 1 Yellow Field

Terminal (126)

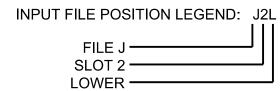
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

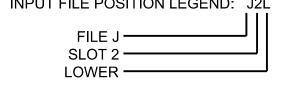
3. Ensure that the Red Enable is active at all times during normal operation.

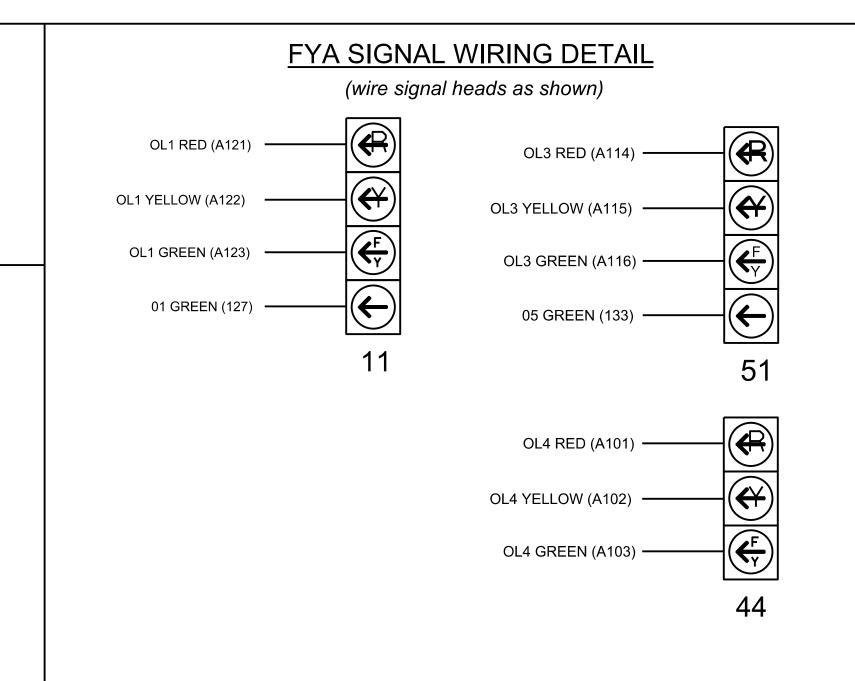
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1 1	TD2 1 2	I1·U	56	1:8	1	1	30.0		Х		Х	
1A	TB2-1,2	I I'U		•	29	6	3.0		Х		Х	Х
2A	TB2-5,6	I2U	39	1	2	2			Х	Χ	Х	
2B	TB2-7,8	I2L	4:3	5	3	2			Х	Х	Х	
4:A	TB4 - 9,10	I6U	41	3	8	4	3.0		Х		Х	
4B	TB4-11,12	I6L	45	7	9	4			Х		Х	
5A	TB3-1,2	J1U	55	1.7	1:5	5	15.0		Х		Х	
3A	103-1,2	310	33		31	2	3.0		Х		Х	X
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Х	
6B	TB3-7,8	J2L	44	6	1.7	6			Х	Χ	Х	
8:A	TB5 - 9,10	J6U	4:2	4	2:2	8	3.0		Х		Х	







THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0098 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 1 of 2

DETAILS FOR:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED NC 73-211/NC 211

Prepared for:

ELECTRICAL AND PROGRAMMIN

NC 73/SR 1133 (Mode Rd)

Moore County West En PLAN DATE: June 2024 REVIEWED BY: R. Mullinax PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

034437

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M

SIG. INVENTORY NO.

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	6	8
Modifier Phases	1	-	5	-
Modifier Overlaps	-	-	1	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTICE	1	Phase Vehicle	1		Х	Х	1
NOTICE PHASE 2	2	Phase Vehicle	2	·	Х	·	2
FLASH RED	3	Phase Vehicle	3	·	Х	Х	3
	4	Phase Vehicle	4	·	Х		4
	5	Phase Vehicle	5	·	Х	·	5
NOTICE PHASE 6	6	Phase Vehicle	6	·	Х	Х	6
FLASH RED	7	Phase Vehicle	7	·	Х	•	7
NOTIOE	8	Phase Vehicle	8	·	Х	Х	8
NOTICE OVERLAP 1	9	Overlap	1	·	Х	Х	9
FLASH RED	1:0	Overlap	2	·	Х	Х	1:0
NOTICE OVERLAP 3	1:1	Overlap	3	·	Х	٠	11
FLASH RED	1:2	Overlap	4	·	Х	·	1:2
	1:3	Phase Ped	2	•	•	·	1:3
	14	Phase Ped	4	·		٠	1:4
	1:5	Phase Ped	6	·		·	1:5
	16	Phase Ped	8	·	·	·	16
	1.7	Overlap	5	·	Х	Х	1.7
	18	Overlap	6	•	Χ		18

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

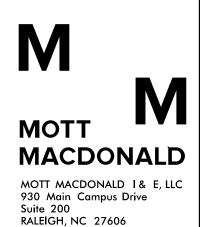
	Startup Parameters					
	Startup Clearance Hold					
Γ	6					

Unit Flash Parameters

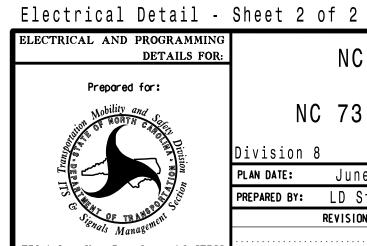
All Red Flash Exit Time

6

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0098
DESIGNED: June 2024
SEALED: 7/11/2024
REVISED:



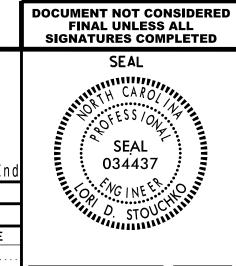
License No. F-0669



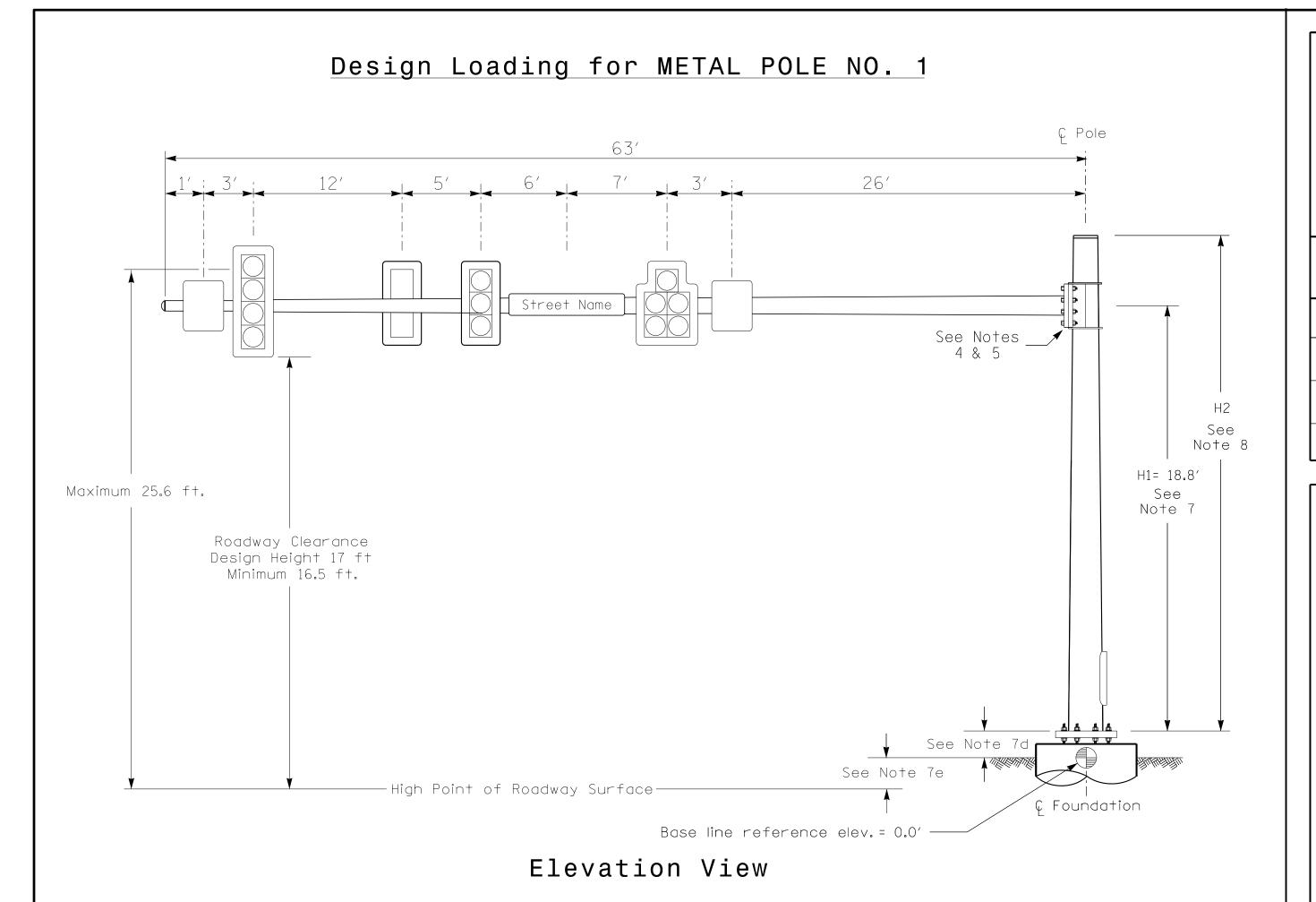
NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)

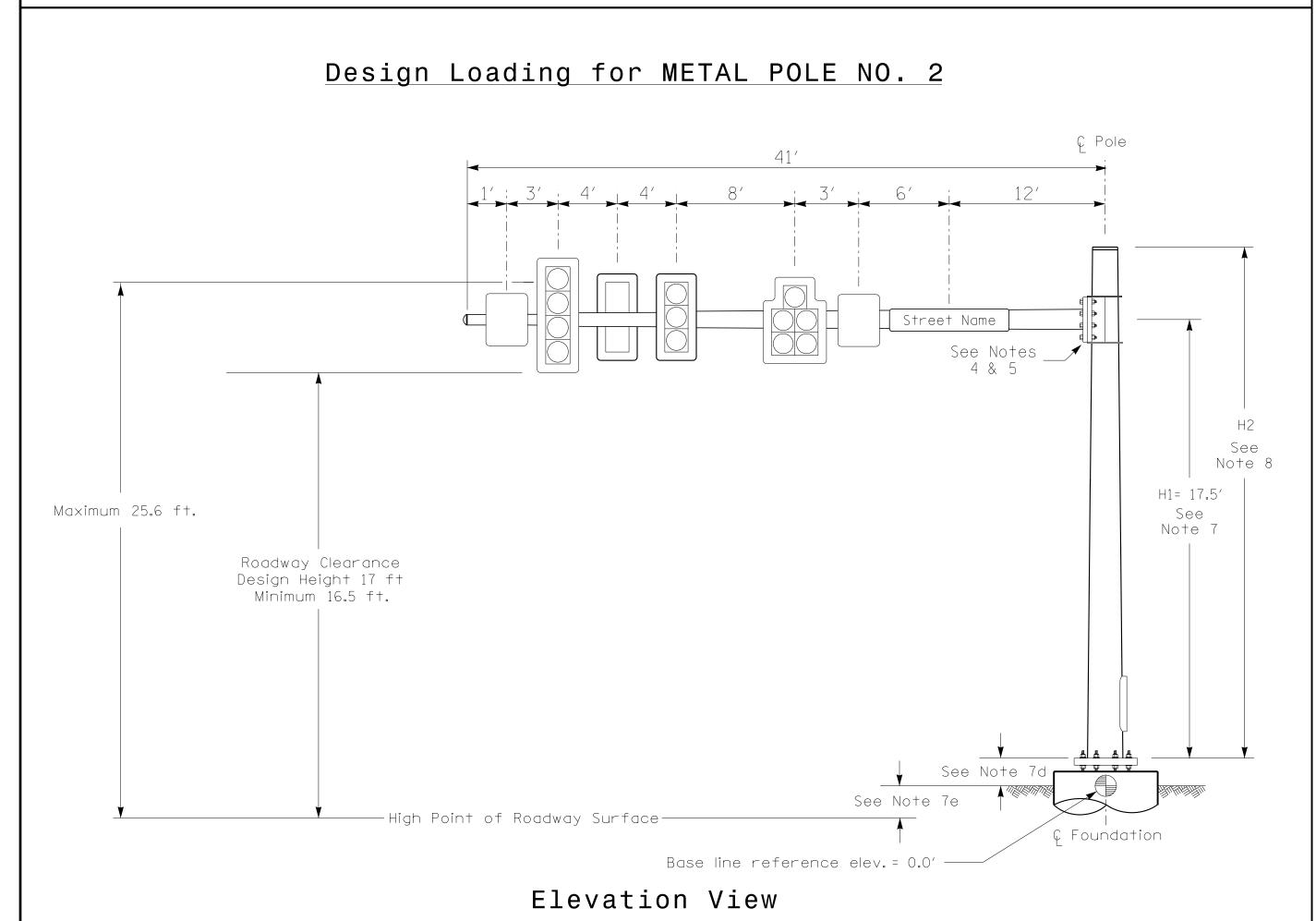
Division 8 Moore County West English Date: June 2024 Reviewed BY: R. Mullinax
PREPARED BY: LD Stouchko Reviewed BY:

REVISIONS INIT. DATE



SIG. INVENTORY NO. 08-0098



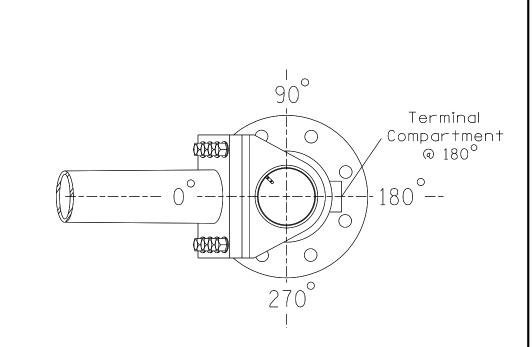


SPECIAL NOTE

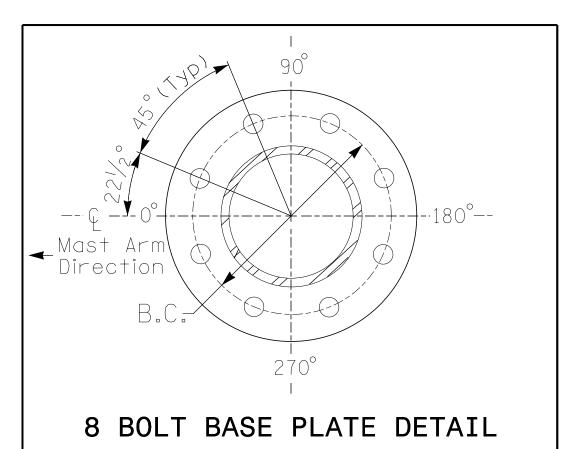
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

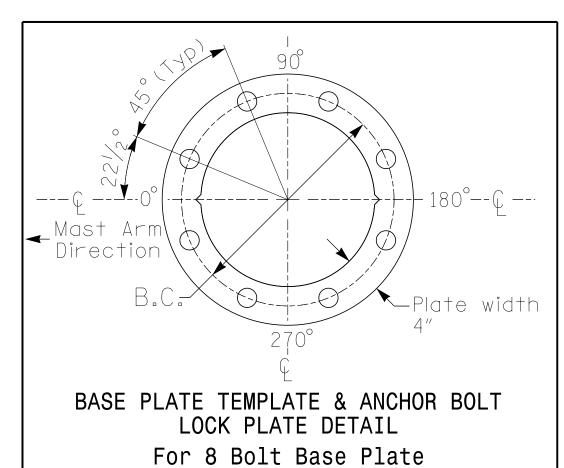
Elevation Differences for:	Pole 1	Pole 2
Baseline reference point at © Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	-2.5 ft.	+1.5 ft.
Elevation difference at Edge of travelway or face of curb	+0.9 ft.	0.3 ft.



POLE RADIAL ORIENTATION



See Note 6



METAL POLE No. 1 and No. 2

MOTT MACDO	M
MOTT MACDONAL 930 Main Campus Suite 200 RALEIGH, NC 2760 License No. F–0669	Drive

Sig.11.3

R-5726

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3 S.F.	42.0″W X 56.0″L	103 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0″W X 96.0″L	36 LBS

NOTES

DESIGN REFERENCE MATERIAL

- 1. Design the traffic signalstructure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

DESIGN REQUIREMENTS

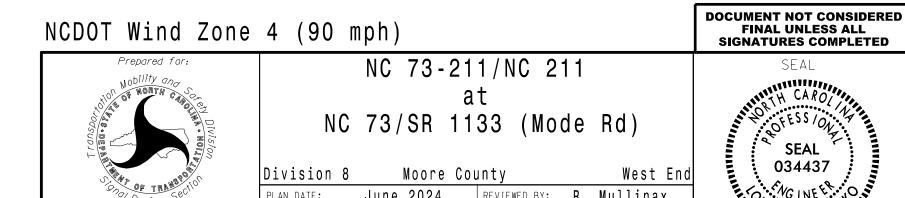
views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.

3. Design all signal supports using stress ratios that do not exceed 0.9.

4. The camber design for the mast arm deflection should provide an appearance of a low

2. Design the traffic signalstructure using the loading conditions shown in the elevation

- 1. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground leveland the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
 9. If pole location adjustments are required, the contractor must gain approval from the
- Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10.The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signalheads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



DIVISION 8 MOORE COUNTY WEST END
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

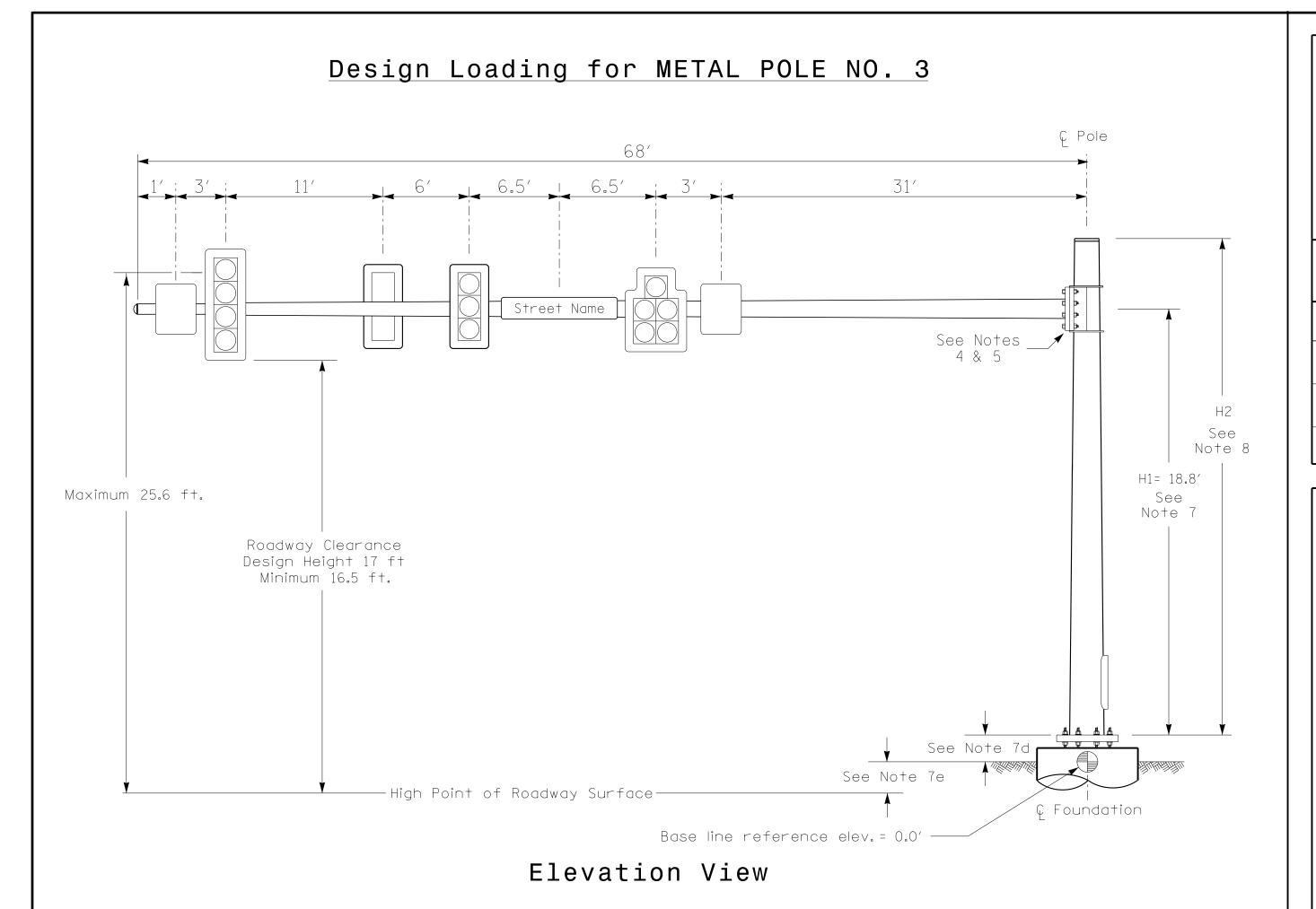
SCALE
N/A

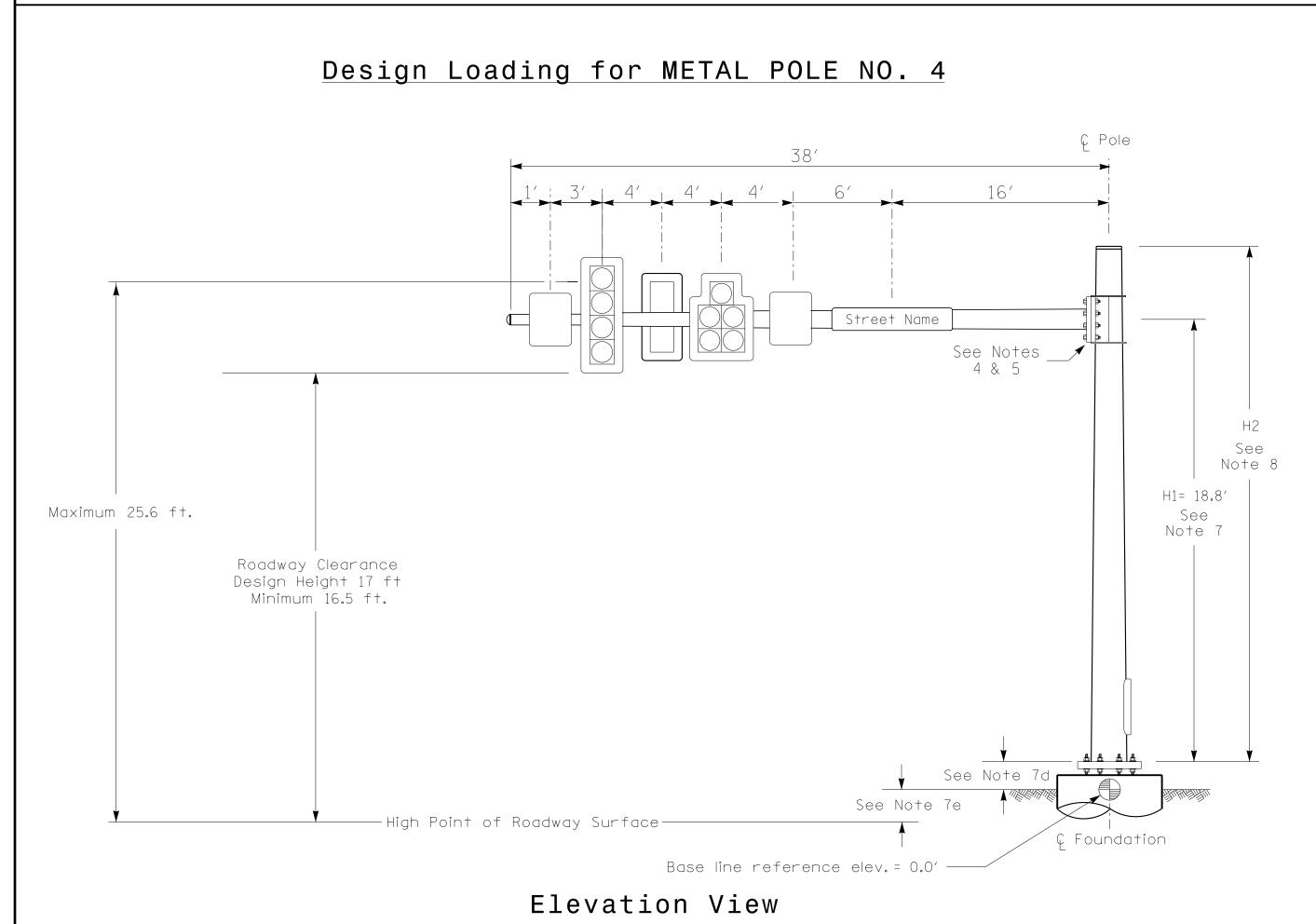
N/A

SIGNATURE
DATE

SIGNATURE
DATE

SIGNATURE
DATE



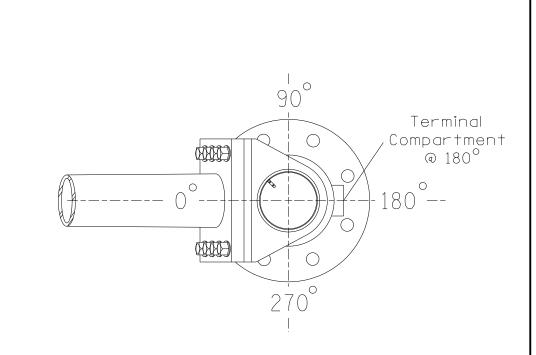


SPECIAL NOTE

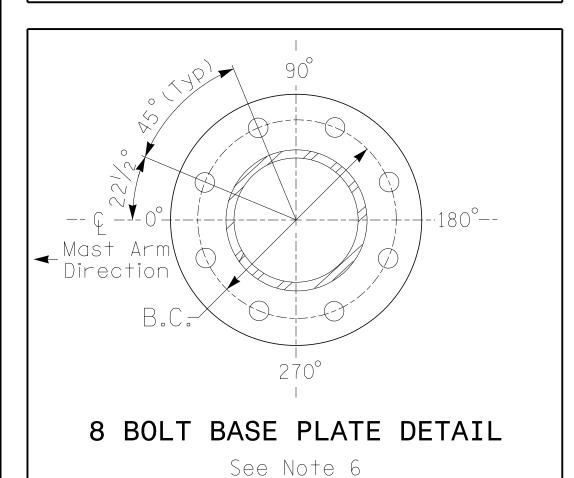
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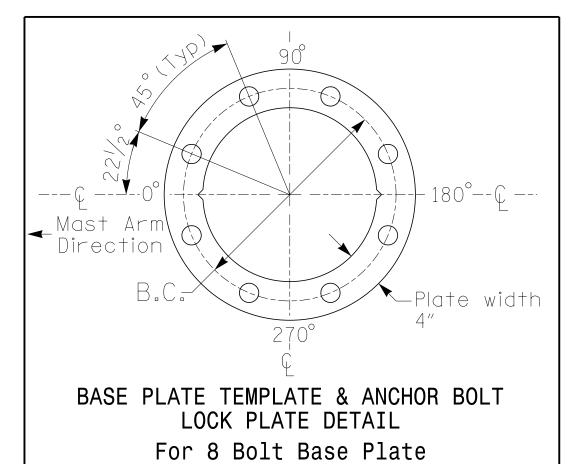
Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 3	Pole 4
Baseline reference point at © Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+0.2 ft.	+0.2 ft.
Elevation difference at Edge of travelway or face of curb	0.0 ft.	+0.5 ft.



POLE RADIAL ORIENTATION





METAL POLE No. 3 and No. 4

PROJECT REFERENCE NO.	SHEET NO.
R - 5726	Sig.11.4

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3 S.F.	42.0″W X 56.0″L	103 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
	SIGN RIGID MOUNTED	7.5 S.F.	30.0"W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0″W X 96.0″L	36 LBS

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<u>NOTES</u>

DESIGN REFERENCE MATERIAL

- 1. Design the traffic signalstructure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website:
- The NUDUL "MetalPole Standards" located at the tollowing NUDUL Website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

DESIGN REQUIREMENTS

views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.

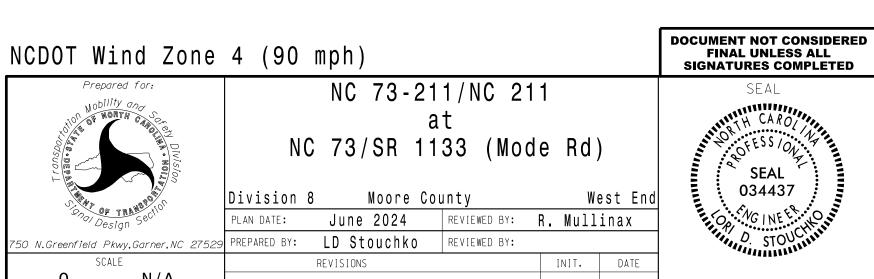
3. Design all signal supports using stress ratios that do not exceed 0.9.

2. Design the traffic signalstructure using the loading conditions shown in the elevation

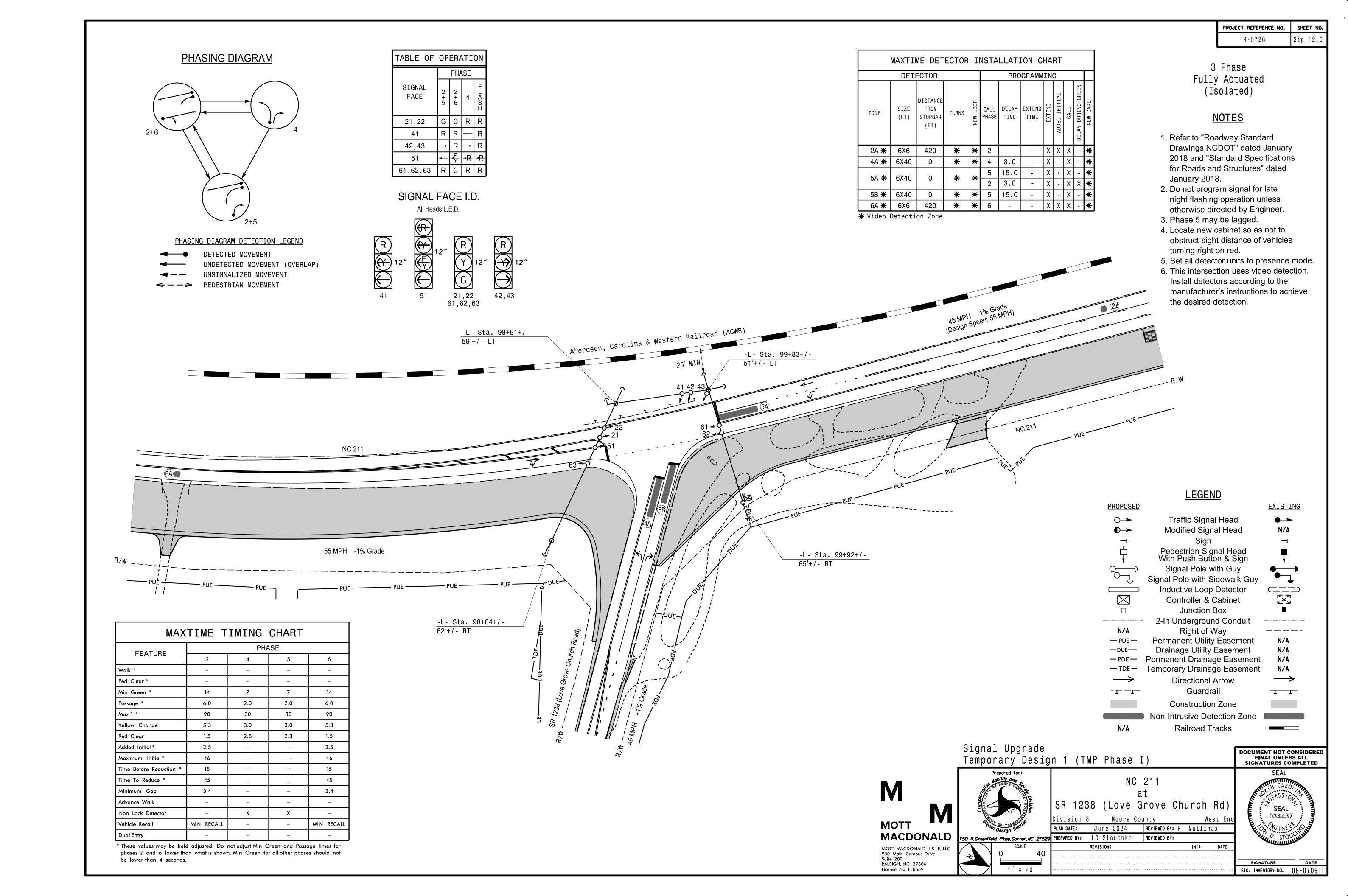
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground leveland the high point of the roadway.
- 8. The pole manufacturer willdetermine the totalheight (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or

N/A

- H1 plus 1/2 of the totalheight of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10.The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signalheads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



SIG. INVENTORY NO. 08-0098



18 CHANNEL CONFLICT MONITOR ON PROGRAMMING DETAIL WD ENABLE Ω (remove jumpers and set switches as shown) REMOVE DIODE JUMPERS 2-5, 2-6, 2-11, 2-12, 4-12, 5-11, 5-12, 6-11 and 11-12. RF 2010RP DISABLE ─ WD 1.0 SEC GY ENABLE SF#1 POLARITY LEDguard RF ŠSM FYA COMPACT— FYA 1-9 FYA 3-10 FYA 5-11 FYA 7-12 REMOVE JUMPERS AS SHOWN NOTES: 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S2, S5, S7, S8, AUX S4,
	AUX S5
Phases Used	2, 4, 5, 6
Overlap "1"	NOT USED
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	*

*See overlap programming detail on sheet 2

PROJECT REFERENCE NO. R-5726 Sig.12.

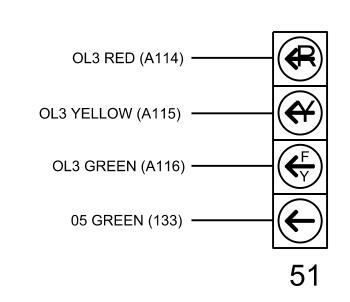
				SIC	GNA	٦L ۲	łΕΑ	DΗ	00	K-U	IP C	HA	RT					
LOAD SWITCH NO.	TITCH NO. ST S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 S1 S2 S3 S4 S5 S6																	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41	NU	★ 51	61,62 63	NU	NU	NU	NU	NU	NU	NU	★ 51	42,43	NU
RED		128			101			134									A101	
YELLOW		129					*	135										
GREEN		130						136										
RED ARROW																A114		
YELLOW ARROW					102											A115	A102	
FLASHING YELLOW ARROW																A116		
GREEN ARROW					103		133										A103	

NU = Not Used

★See pictorial of head wiring in detail this sheet.

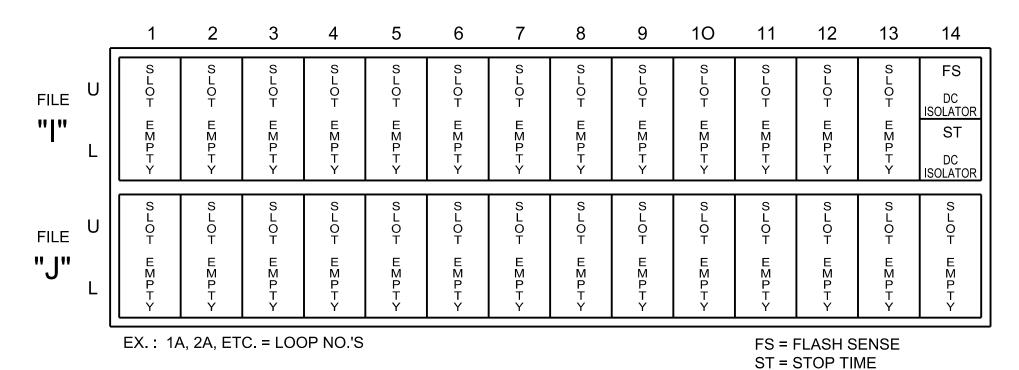
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT (front view)

= DENOTES POSITION OF SWITCH



2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

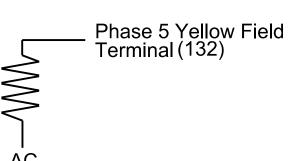
SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T1 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

M **MOTT MACDONALD** MOTT MACDONALD I & E, LLC 930 Main Campus Drive

RALEIGH, NC 27606 License No. F-0669

Electrical Detail - Sheet 1 of 2 Temporary Design 1 (TMP Phase I) ELECTRICAL AND PROGRAMMING

DETAILS FOR: Prepared for:

NC 211 SR 1238 (Love Grove Church Rd)

Division 8 Moore County West En REVIEWED BY: R. Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

SEAL 034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

^{*}Denotes install load resistor. See load resistor installation detail this sheet.

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	Off	Off	FYA 4 - Section	Normal
Included Phases	-	-	6	4,5
Modifier Phases	-	1	5	0
Modifier Overlaps	-	•	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

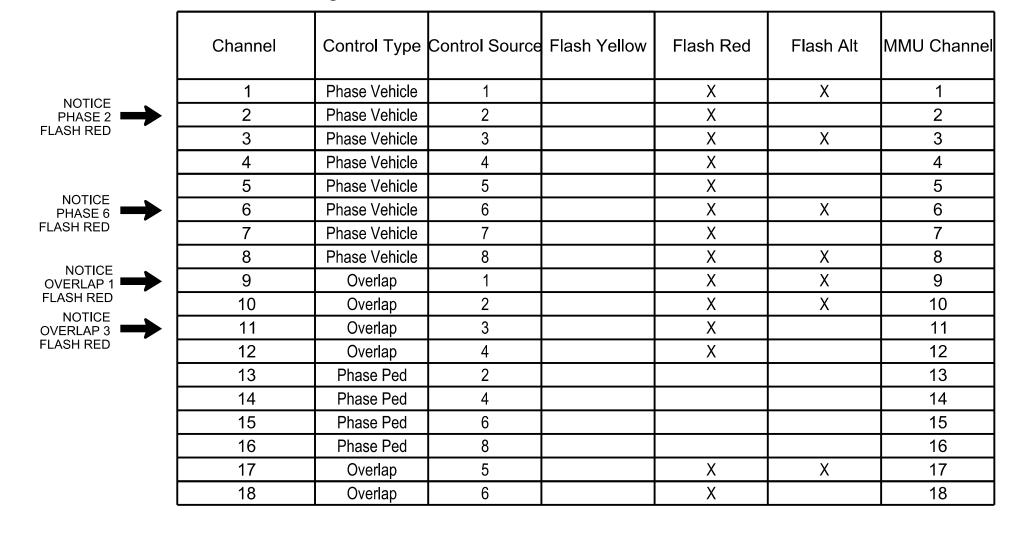
Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters
Startup Clearance Hold
6

Unit Flash Parameters
All Red Flash Exit Time
6

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T1 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

MOTT
MACDONALD

MOTT MACDONALD 1 & E, LLC
930 Main Campus Drive
Suite 200
RALEIGH, NC 27606

License No. F-0669

Electrical Detail - Sheet 2 of 2
Temporary Design 1 (TMP Phase I)

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

Prepared for:

Prepared for:

Mobility and

RORTH CARGON MODITION OF THE AMERICAN MODI

at
SR 1238 (Love Grove Church Rd)

Division 8 Moore County West En
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko REVIEWED BY:
REVISIONS INIT. DATE

NC 211

SEAL

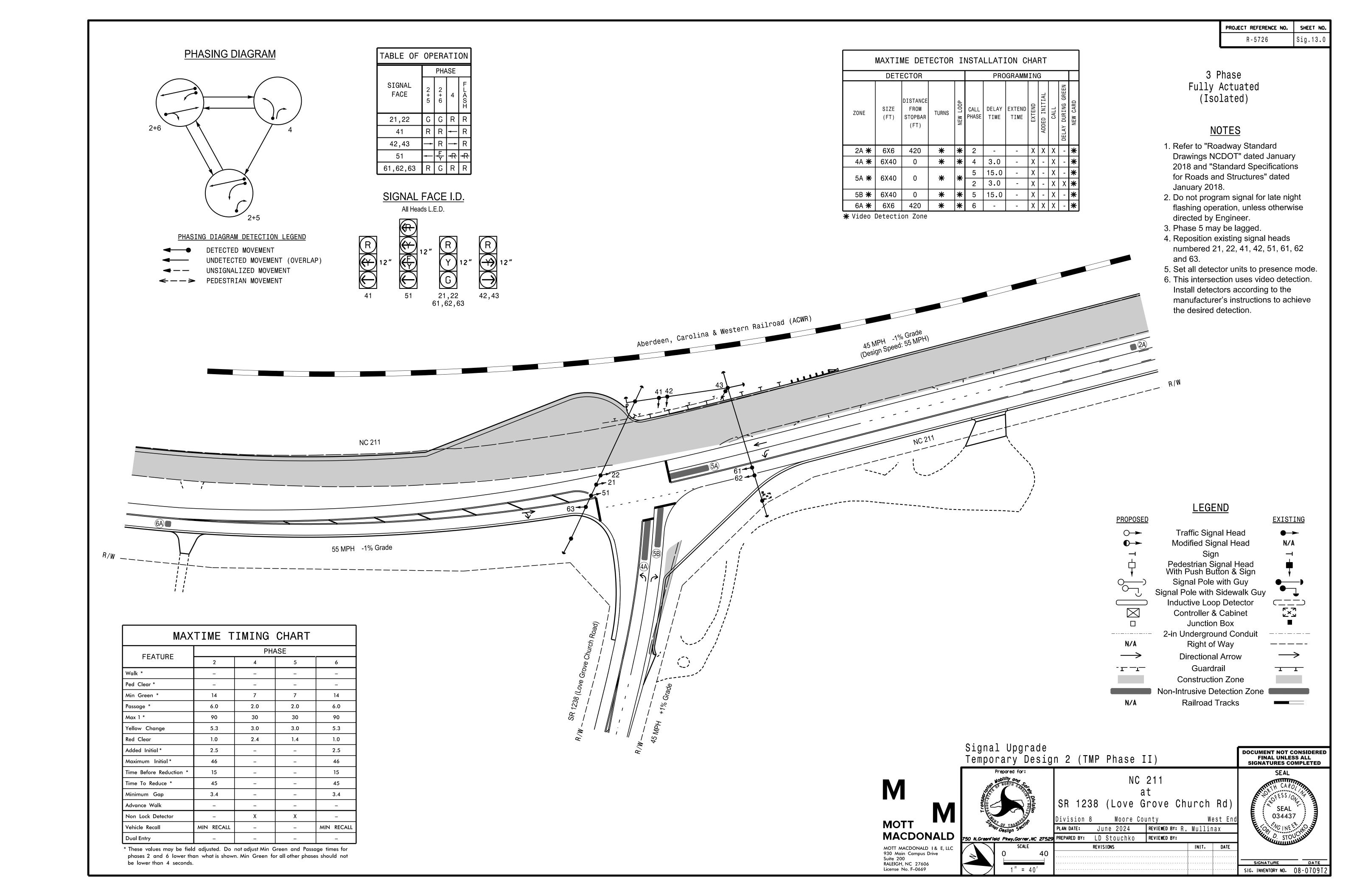
SEAL

SEAL

OSA4437

OSTOUCHILL

OST



INPUT FILE POSITION LAYOUT

(front view)

11 12

FS = FLASH SENSE ST = STOP TIME

13

DC ISOLATOR

ST

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S2, S5, S7, S8, AUX S4,
	AUX S5
Phases Used	2, 4, 5, 6
Overlap "1"	NOT USED
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "3"	

*See overlap programming detail on sheet 2

PROJECT REFERENCE NO. R-5726 Sig 13.

	SIGNAL HEAD HOOK-UP CHART																	
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41	NU	★ 51	61,62 63	NU	NU	NU	NU	NU	NU	NU	★ 51	42,43	NU
RED		128			101			134									A101	
YELLOW		129					*	135										
GREEN		130						136										
RED ARROW																A114		
YELLOW ARROW					102											A115	A102	
FLASHING YELLOW ARROW																A116		
GREEN ARROW					103		133										A103	

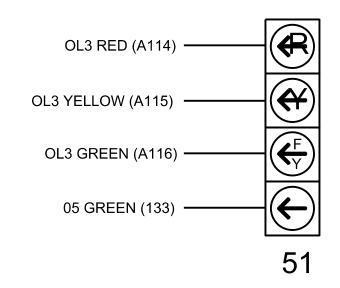
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



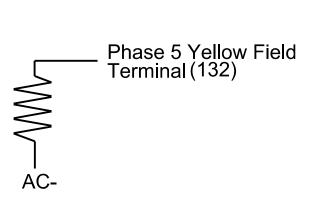
SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)

EX.: 1A, 2A, ETC. = LOOP NO.'S



RALEIGH, NC 27606

Electrical Detail - Sheet 1 of 2 Temporary Design 2 (TMP Phase II)

DETAILS FOR: Prepared for:

ELECTRICAL AND PROGRAMMING

NC 211 SR 1238 (Love Grove Church Rd)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T2

DESIGNED: June 2024 SEALED: 7/11/2024

REVISED:

Division 8 Moore County West En REVIEWED BY: R Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS

INIT. DATE

SIG. INVENTORY NO. 08-0709T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

034437

MOTT MACDONALD MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	Off	Off	FYA 4 - Section	Normal
Included Phases	-	1	6	4,5
Modifier Phases	-	-	5	0
Modifier Overlaps	=			-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

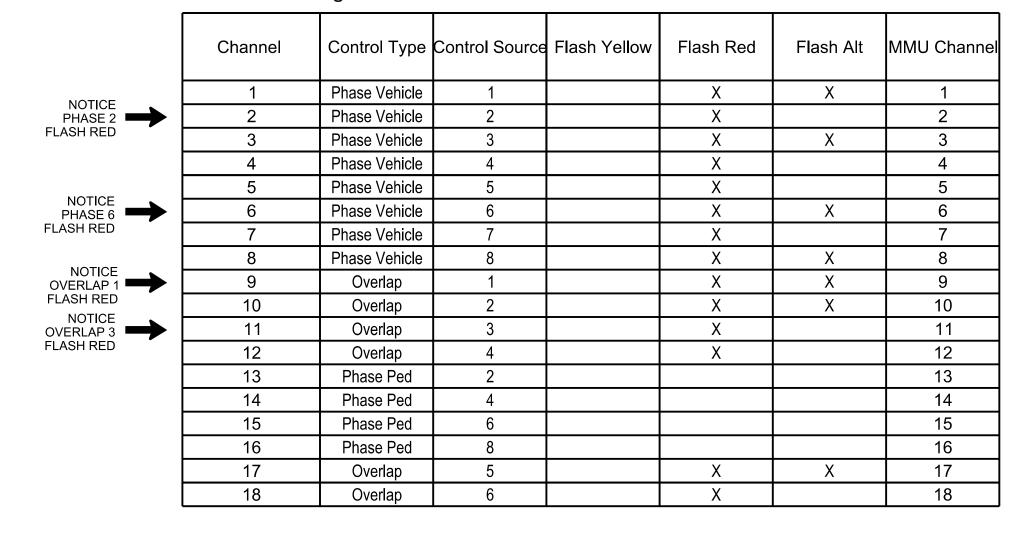
Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Unit Flash Parameters

All Red Flash Exit Time

Startup Parameters

Startup Clearance Hold

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T2 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

ELECTRICAL AND PROGRAMMING DETAILS FOR:

MOTT

Suite 200

RALEIGH, NC 27606

License No. F-0669

MACDONALD

MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive

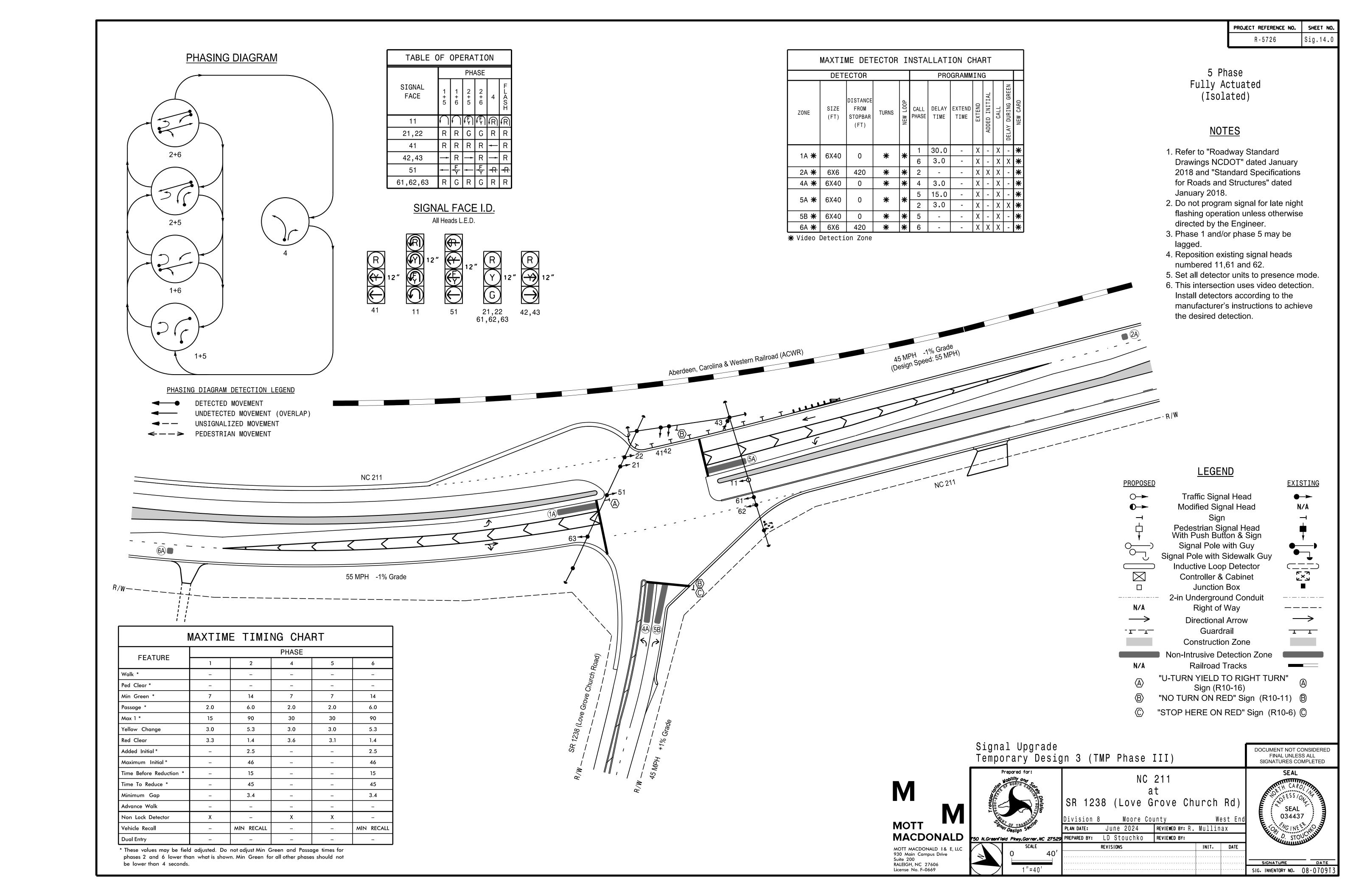
Electrical Detail - Sheet 2 of 2 Temporary Design 2 (TMP Phase II)

NC 211 SR 1238 (Love Grove Church Rd)

Moore County June 2024 REVIEWED BY: R. Mullinax PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



18 CHANNEL CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

ON OFF

RP DISABLE - WD 1.0 SEC - GY ENABLE

- SF#1 POLARITY

- FYA COMPACT-

LEDguard

= DENOTES POSITION OF SWITCH

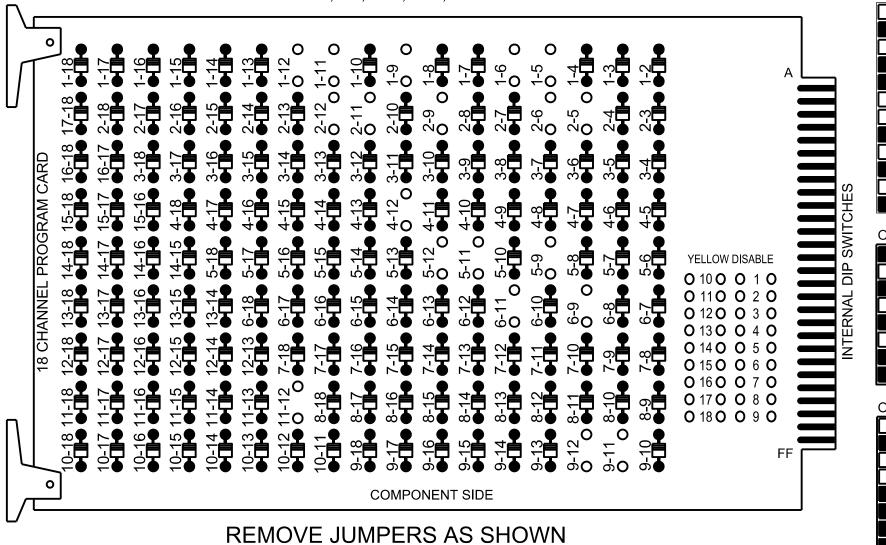
DC ISOLATOR

ST

FS = FLASH SENSE ST = STOP TIME

WD ENABLE Ω

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-12, 2-5, 2-6, 2-9, 2-11, 2-12, 4-12, 5-9, 5-11, 5-12, 6-9, 6-11, 9-11, 9-12 and 11-12.



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- 3. Ensure that the Red Enable is active at all times during normal operation.

INPUT FILE POSITION LAYOUT

(front view)

1 2 3 4 5 6 7 8 9 10 11 12 13 14

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	.2070LX
Cabinet	.332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	.Base
Output File Positions	.18 With Aux. Output File
Load Switches Used	.S1, S2, S5, S7, S8, AUX S1,
	AUX S4, AUX S5
Phases Used	1, 2, 4, 5, 6
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	*

*See overlap programming detail on sheet 2

PROJECT REFERENCE NO.

	SIGNAL HEAD HOOK-UP CHART																	
LOAD SWITCH NO.	ITCH NO. ST S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 S1 S2 S3 S4 S5 S6																	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	★ 11	21,22	NU	NU	41	NU	★ 51	61,62 63	NU	NU	NU	NU	★ 11	NU	NU	★ 51	42,43	NU
RED		128			101			134									A101	
YELLOW	*	129					*	135										
GREEN		130						136										
RED ARROW													A121			A114		
YELLOW ARROW					102								A122			A115	A102	
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127				103		133										A103	

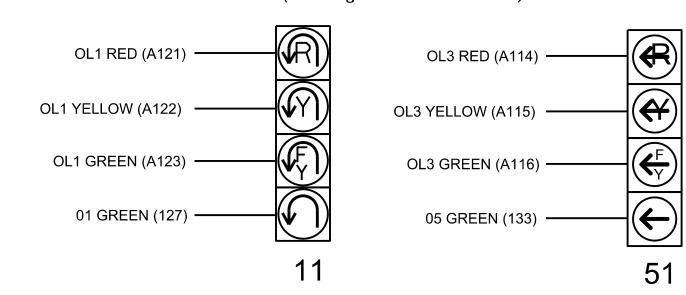
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

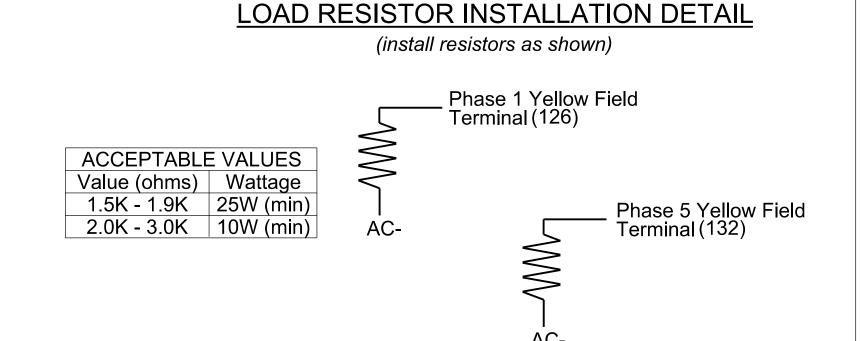
(wire signal heads as shown)



SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T3 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:



EX.: 1A, 2A, ETC. = LOOP NO.'S

MACDONALD MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive

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Suite 200

RALEIGH, NC 27606 License No. F-0669

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

Electrical Detail - Sheet 1 of 2 Temporary Design 3 (TMP Phase III)

NC 211 SR 1238 (Love Grove Church Rd)

Division 8 Moore County West Er REVIEWED BY: R. Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS INIT. DATE 034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	Normal
Included Phases	2	1	6	4,5
Modifier Phases	1		5	-
Modifier Overlaps	-	1	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH. MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

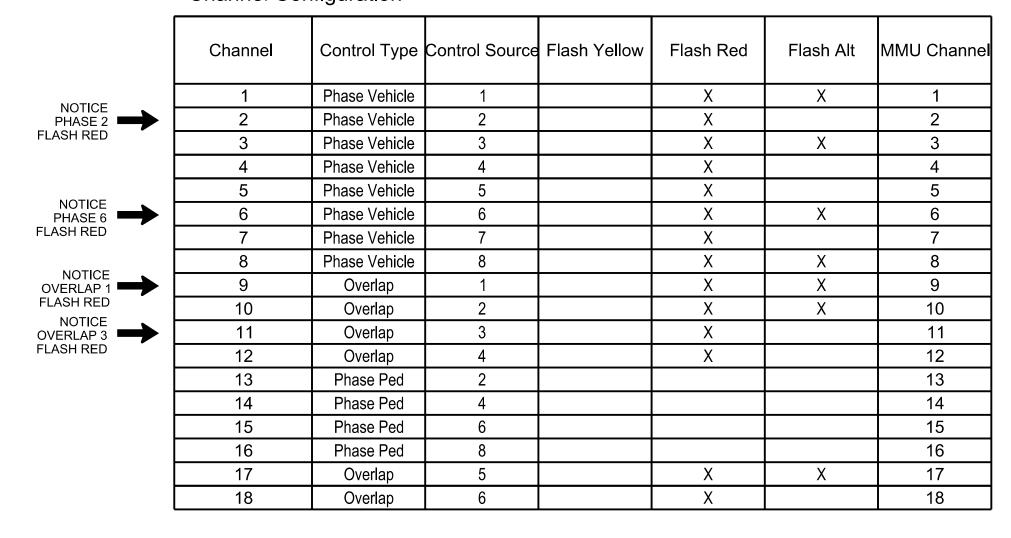
Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



MAXTIME STARTUP AND SOFTWARE FLASH **PROGRAMMING DETAIL**

Front Panel

Main Menu >Controller >Unit

Web Interface

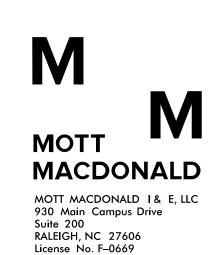
Home >Controller >Unit

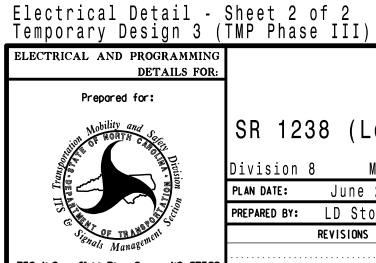
Modify parameters as shown below and save changes.

Startup Clearance Hold

Startup Parameters Unit Flash Parameters All Red Flash Exit Time

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709T3 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:



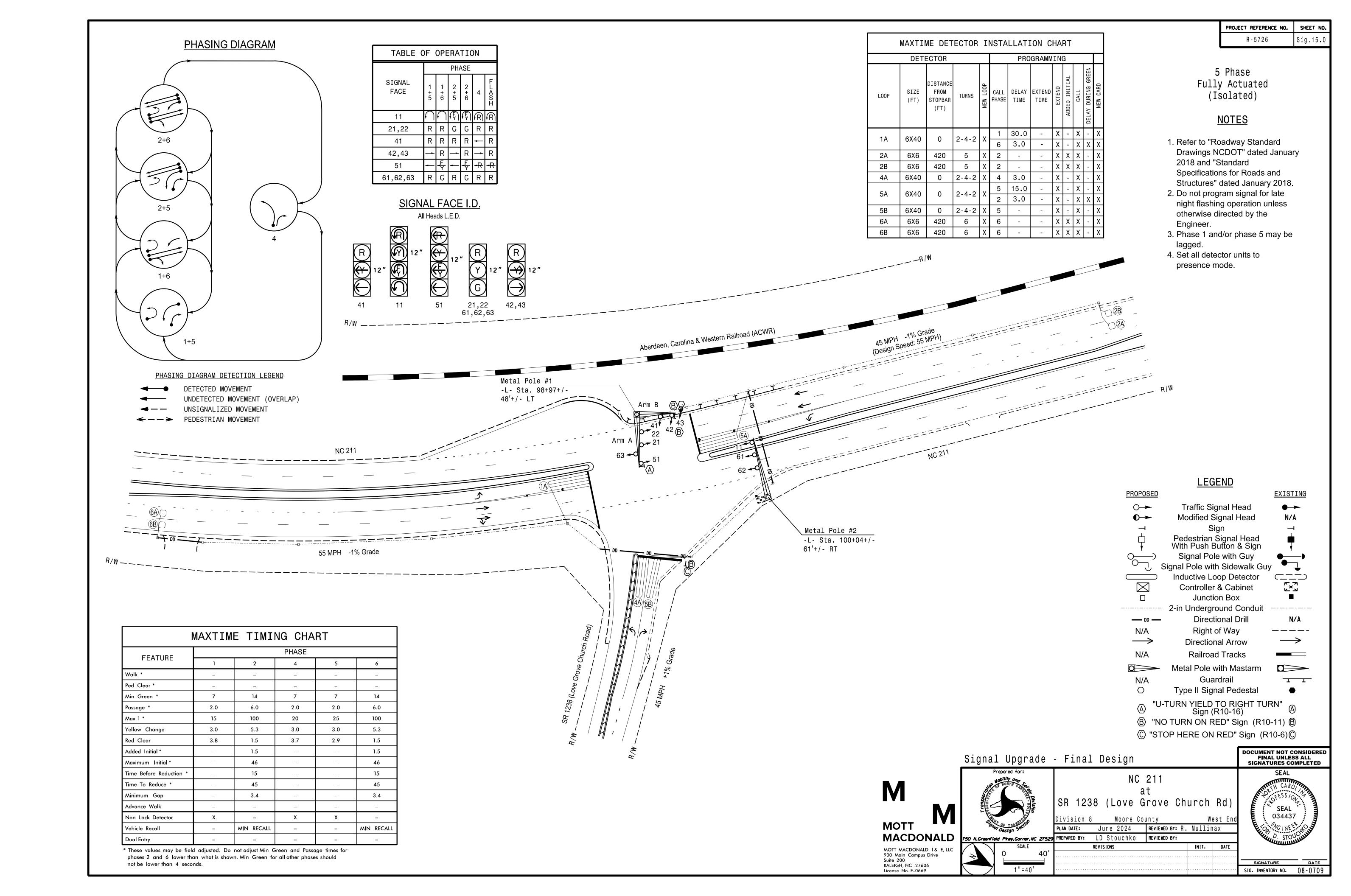


NC 211 SR 1238 (Love Grove Church Rd)

Moore County REVIEWED BY: R. Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	.18 With Aux. Output File
Load Switches Used	S1, S2, S5, S7, S8, AUX S ²
	AUX S4, AUX S5
Phases Used	1, 2, 4, 5, 6
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	*
•	

*See overlap programming detail on sheet 2

PROJECT REFERENCE NO. R-5726 Sig.15.

	SIGNAL HEAD HOOK-UP CHART																	
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	★ 11	21,22	NU	NU	41	NU	★ 51	61,62 63	NU	NU	NU	NU	★ 11	NU	NU	★ 51	42,43	NU
RED		128			101			134									A101	
YELLOW	*	129					*	135										
GREEN		130						136										
RED ARROW													A121			A114		
YELLOW ARROW					102								A122			A115	A102	
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127				103		133										A103	

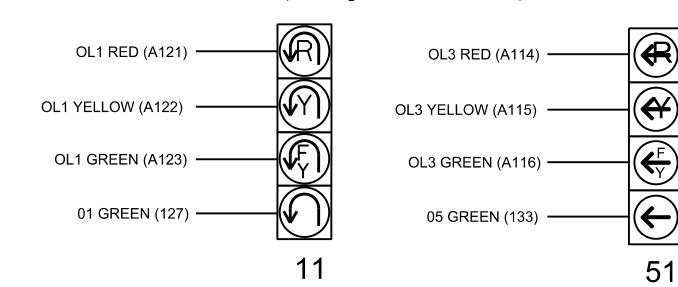
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE CONNECTION & PROGRAMMING CHART

,	1	2	3	4	5	6	7	8	9	10	11	12	13	14
_{FILE} U	Ø 1 1A	ø 2 2A	NOT USED	SLOF	S L O T	ø 4 4A	S L O T	S L O T	S L O T	SLOT	SLOT	SLOT	S L O T	FS DC ISOLATOR
" "	NOT USED	ø 2 2B	ø 5 5B	EMPTY	E M P T Y	NOT USED	E M P T Y	E M P T Y	E M P T Y	E M P T Y	EMPTY	E M P T Y	E M P T Y	ST DC ISOLATOR
_{FILE} U	Ø 5	ø 6 6A	S L O T	SLOT	S L O T	S L O T	S L O T	SLOT	SLOT	S L O T	S L O T	S L O T	S L O T	S L O T
"J" _	NOT USED	ø 6 6B	E M P T Y	EMPHY	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	EMPTY	E M P T Y	E M P T Y	E M P T Y

INPUT FILE POSITION LAYOUT

(front view)

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE ST = STOP TIME

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	30.0		Х		Χ	
IA	102-1,2	110	30	-	29	6	3.0		Х		Χ	Χ
2A	TB2-5,6	I2U	39	1	2	2			Х	Χ	Χ	
2B	TB2-7,8	I2L	43	5	3	2			Х	Χ	Χ	
4A	TB4-9,10	I6U	41	3	8	4	3.0		Х		Χ	
5A	TB3-1,2	J1U	55	17	15	5	15.0		Х		Χ	
ЗA	100-1,2	310	່ວວ	-	31	2	3.0		Х		Χ	Х
5B	TB2-11,12	I3L	76	42	5	5			Х		Χ	
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Χ	
6B	TB3-7,8	J2L	44	6	17	6			Χ	Χ	Χ	

INPUT FILE POSITION LEGEND: J2L SLOT 2 -LOWER -

LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown) Phase 1 Yellow Field Terminal (126) ACCEPTABLE VALUES Value (ohms) | Wattage 1.5K - 1.9K 25W (min) Phase 5 Yellow Field 2.0K - 3.0K | 10W (min) Terminal (132)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

MOTT MACDONALD MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606 License No. F-0669

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Electrical Detail - Sheet 1 of 2

SR 1238 (Love Grove Church Rd) Moore County West Er PLAN DATE: June 2024 REVIEWED BY: R. Mullinax PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

NC 211

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 034437

SIG. INVENTORY NO.

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	Normal
Included Phases	2	1	6	4,5
Modifier Phases	1		5	-
Modifier Overlaps	-	1	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTIOE	1	Phase Vehicle	1		Х	Х	1
NOTICE PHASE 2	2	Phase Vehicle	2		Х		2
FLASH RED	3	Phase Vehicle	3		Х	Х	3
Γ	4	Phase Vehicle	4		Х		4
Γ	5	Phase Vehicle	5		Х		5
NOTICE PHASE 6	6	Phase Vehicle	6		Х	Х	6
FLASH RED	7	Phase Vehicle	7		Х		7
	8	Phase Vehicle	8		Х	Х	8
NOTICE OVERLAP 1	9	Overlap	1		Х	Х	9
FLASH RED	10	Overlap	2		Х	Х	10
NOTICE OVERLAP 3	11	Overlap	3		Х		11
FLASH RED	12	Overlap	4		Х		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5	-	Χ	Χ	17
	18	Overlap	6		Χ		18

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters
Startup Clearance Hold

Unit Flash Parameters

All Red Flash Exit Time

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0709 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

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930 Main Campus Drive
Suite 200
RALEIGH, NC 27606

License No. F-0669

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:

Prepared for:

Mobility and Steel Division Months and Steel Division M

NC 211 at SR 1238 (Love Grove Church Rd)

Division 8 Moore County West En

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

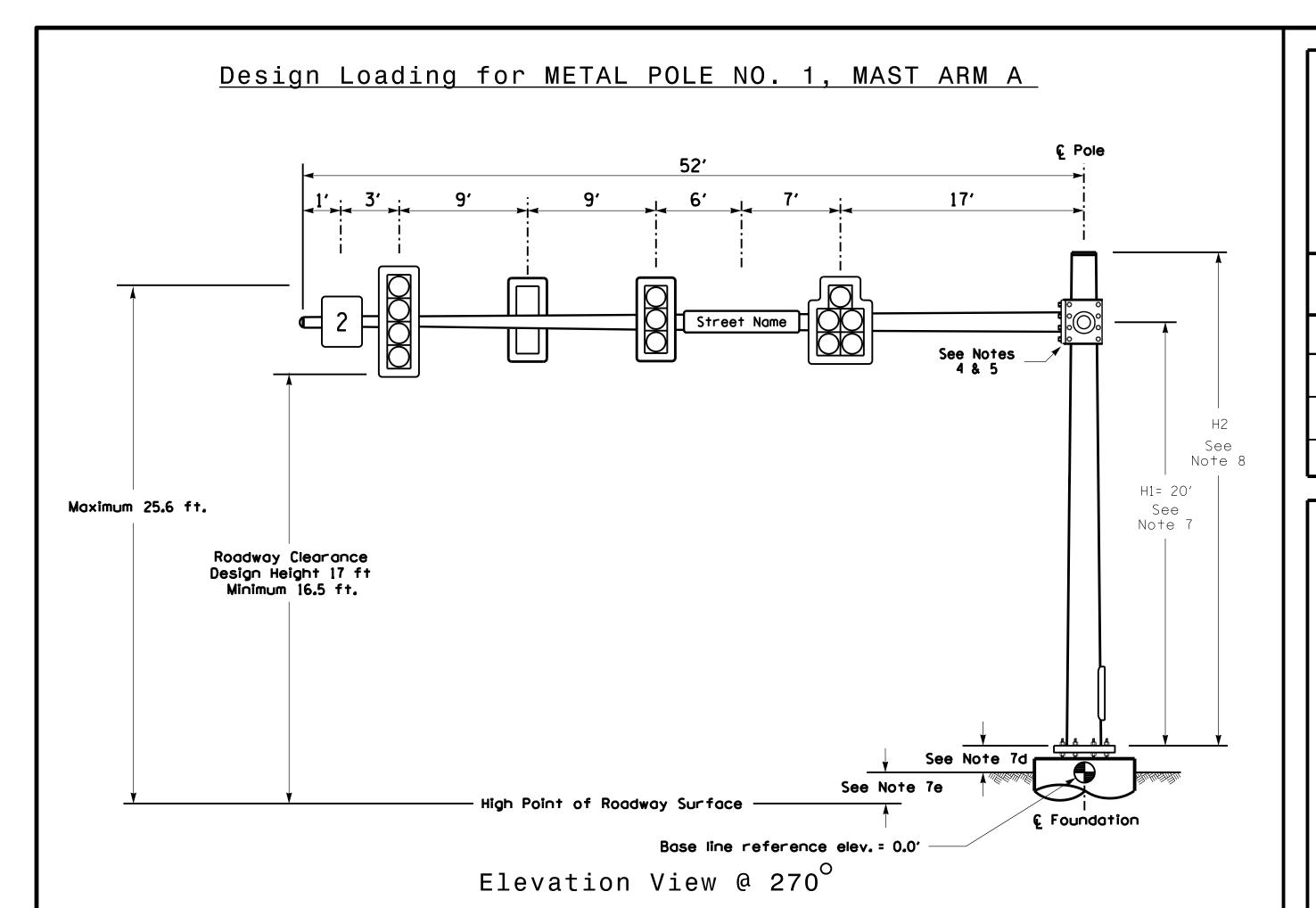
REVISIONS INIT. DATE

DATE DATE

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-0709



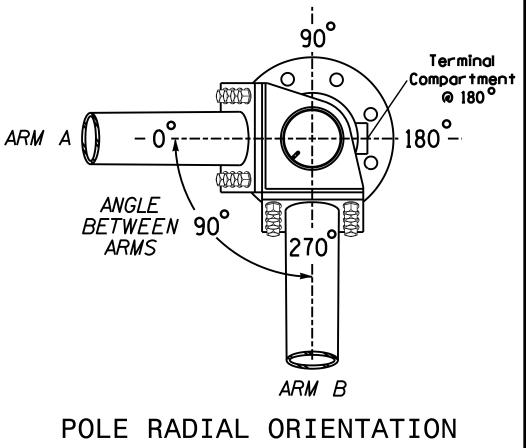
Design Loading for METAL POLE NO. 1, MAST ARM B 38' Street Name See Notes 4 & 5 Н2 Note 8 H1 = 20.5Maximum 25.6 ft. See Note 7 Roadway Clearance Design Height 17 ft Minimum 16.5 ft. Minimum 8' See Note 7d See Note 7e High Point of Roadway Surface-**G** Foundation Base line reference elev. = 0.0' Elevation View @ 0

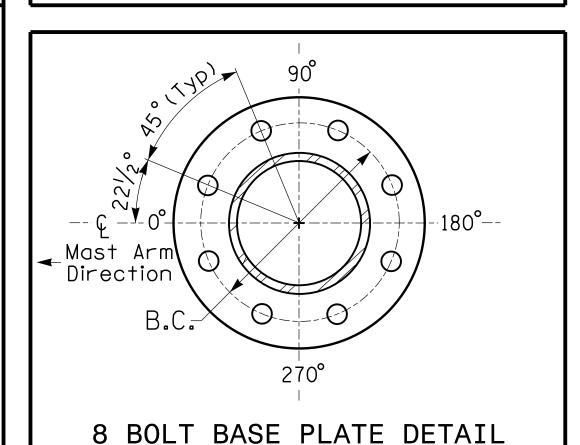
SPECIAL NOTE

The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 1	
Baseline reference point at © Foundation @ ground level	0.0 ft.	
Elevation difference at High point of roadway surface	-1.0 ft.	
Elevation difference at Edge of travelway or face of curb	0.7 ft.	





See Note 6

BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL

For 8 Bolt Base Plate

METAL POLE No. 1

M	V
MOTT MACDONA	•
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Sig.15.3

PROJECT REFERENCE NO.

R-5726

	MAST ARM LOADING SC	HEDU	LE	
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12″-5 SECTION-WITH BACKPLATE	16.3·S.F.	42.0" W X 56.0"L	103 ·LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5·S.F.	25 . 5″ W X 66 . 0″ L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 ·S.F.	25.5" W X 52.5" L	60 Ł BS
2	SIGN RIGID MOUNTED	7.5 ·S.F.	30.0" W X 36.0" L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16 . 0·S . F.	24.0" W X 96.0" L	36 LBS

NOTES

DESIGN REFERENCE MATERIAL

1. Design the traffic signal structure and foundation in accordance with:

The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions. The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions. The 2018 NCDOT Roadway Standard Drawings.

The traffic signal project plans and special provisions.

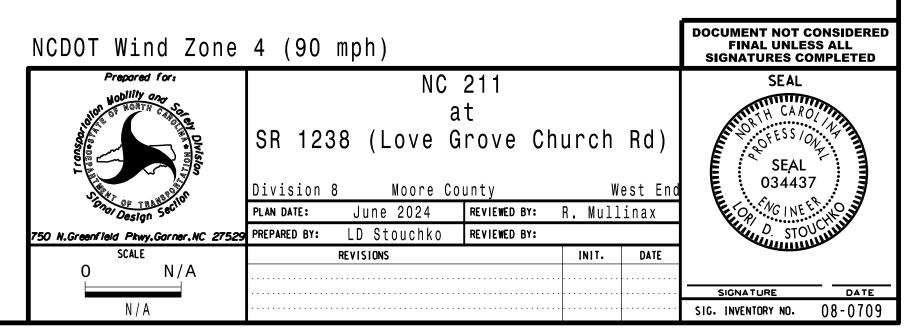
The NCDOT "Metal Pole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

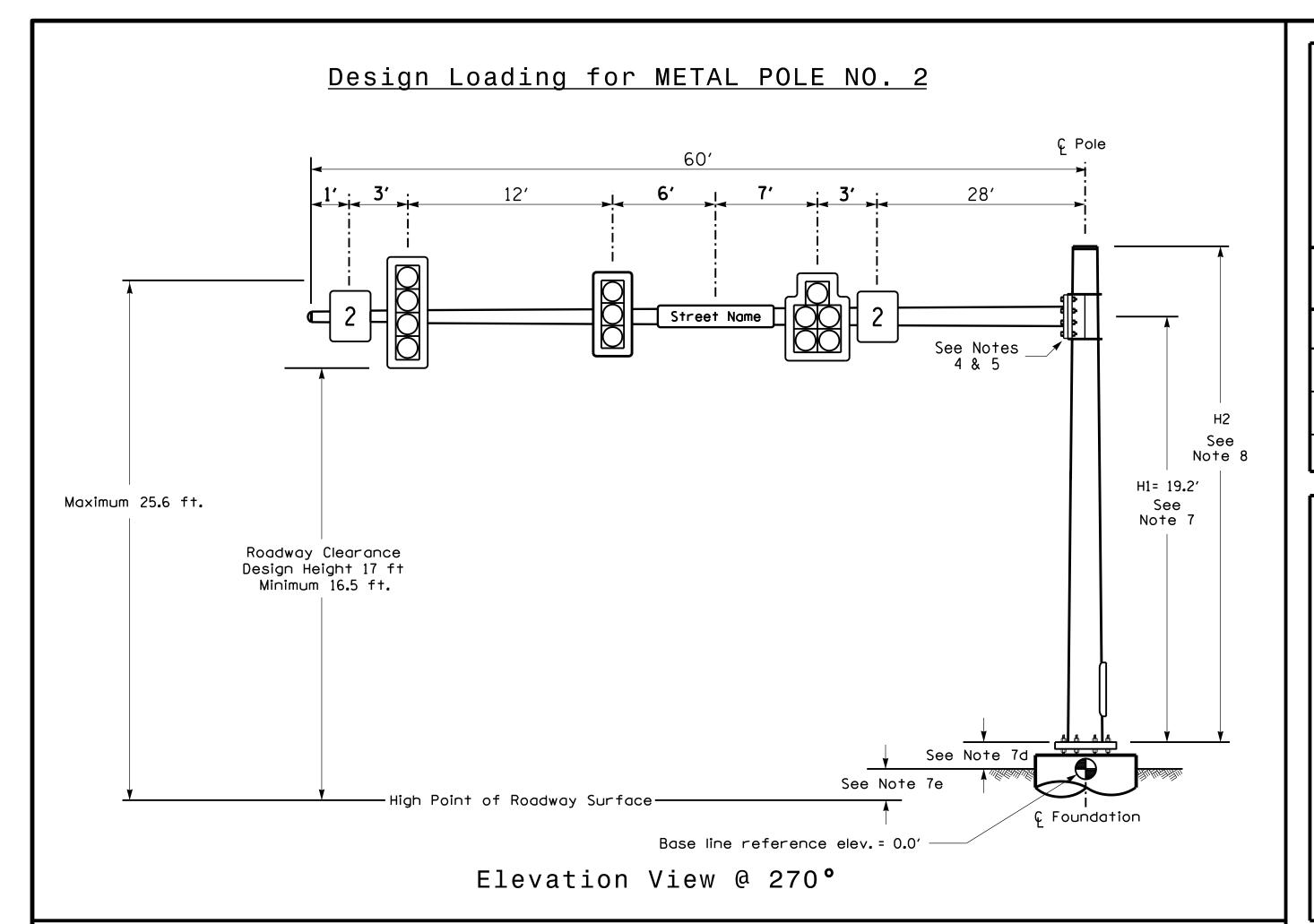
DESIGN REQUIREMENTS

- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signal heads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.

d. The top of the pole base plate is 0.75 feet above the ground elevation.

- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
 - Mast arm attachment height (H1) plus 2 feet, or
 - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 11. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



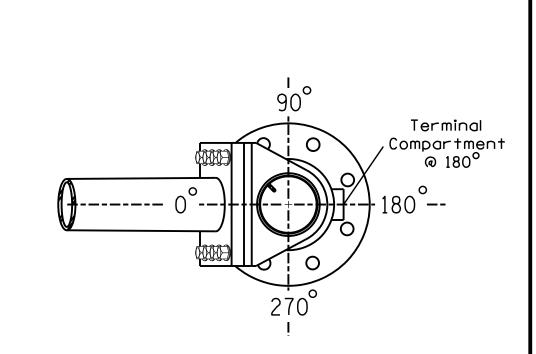


SPECIAL NOTE

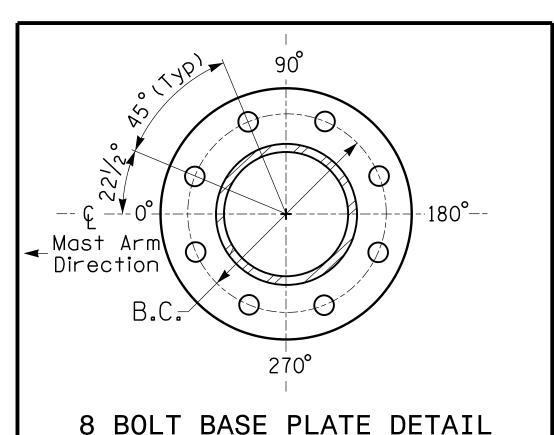
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

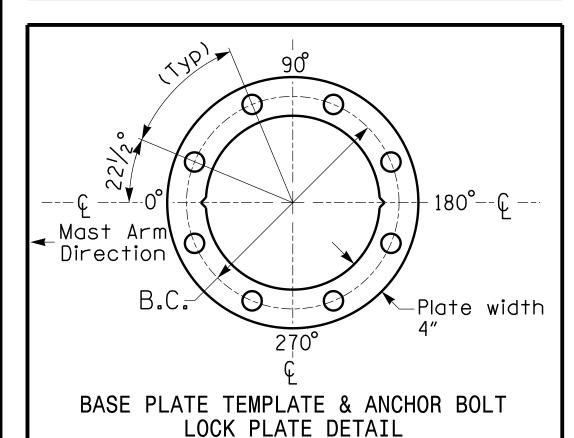
Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 2	
Baseline reference point at & Foundation @ ground level	0.0 ft.	
Elevation difference at High point of roadway surface	-0.2 ft.	
Elevation difference at Edge of travelway or face of curb	0.0 ft.	



POLE RADIAL ORIENTATION





For 8 Bolt Base Plate

See Note 6

METAL POLE No. 2

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PROJECT REFERENCE NO.

R-5726

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12″-5 SECTION-WITH BACKPLATE	16.3·S.F.	42.0" W X 56.0"L	103 ·LBS
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2	SIGN RIGID MOUNTED	7.5 ·S.F.	30.0" W X 36.0" L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0·S.F.	24.0" W X 96.0"L	36 LBS

<u>NOTES</u>

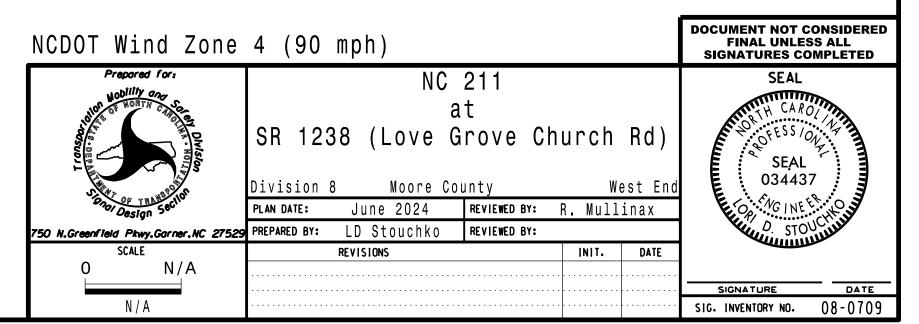
DESIGN REFERENCE MATERIAL

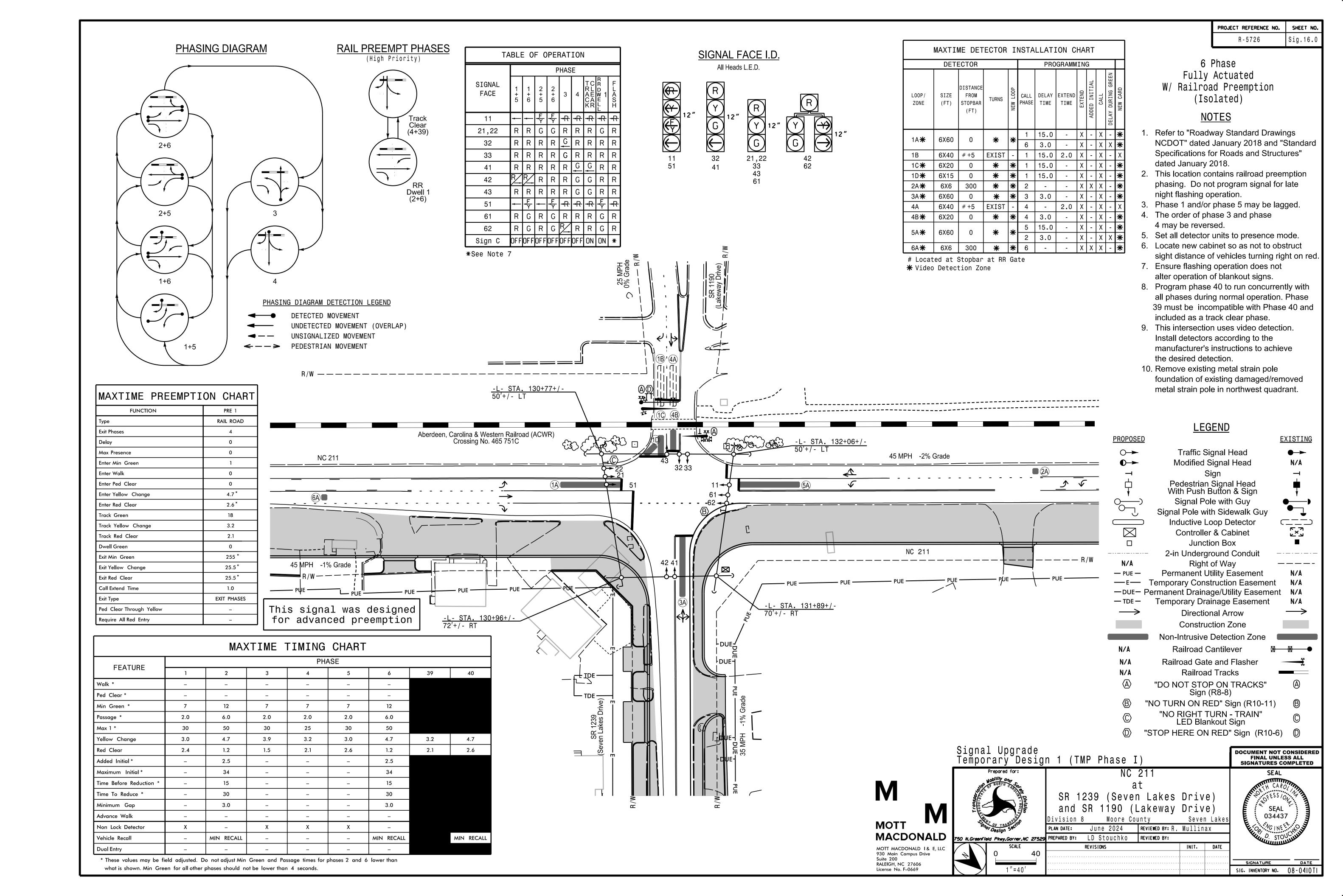
- 1. Design the traffic signal structure and foundation in accordance with:
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- The traffic signal project plans and special provisions.
- The NCDOT "Metal Pole Standards" located at the following NCDOT website:
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DESIGN REQUIREMENTS

- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
 Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch \times 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
 d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed
- foundation ground leveland the high point of the roadway.

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1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

WD ENABLE $\mathbf{\Omega}$ (remove jumpers and set switches as shown) REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11, and 9-11. RP DISABLE ─ WD 1.0 SEC GY ENABLE NTERNAL DIS CALLOTT SF#1 POLARITY — LEDguard — RF SSM FYA COMPACT— FYA 1-9
FYA 3-10 — FYA 5-11 FYA 7-12 REMOVE JUMPERS AS SHOWN NOTES:

ON OFF

= DENOTES POSITION OF SWITCH

ST = STOP TIME PRE = PREEMPT

Phase 5 Yellow Field

Terminal (132)

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify
- 2. Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- 3. Program Phase 39 for No Startup Veh Call.
- 4. Program Phase 40 for Min Recall.
- 5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
-	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*
Overlan "4"	NOT LISED

- that signal heads flash in accordance with the signal plan.

Cabinet	332 w/ Aux
Software	
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	1, 2, 3, 4, 5, 6, 39**, 40*
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	NOT USED

*See overlap programming detail on sheet 2

INPUT FILE CONNECTION & PROGRAMMING CHART

								(IIOIII	view)						
	,	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE	U	S L O T	S L O T	S L O T	S L O T	S L O T	Ø 4 4A	Ø 1 1B	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	FS DC ISOLATOR
" "	L	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	NOT USED	NOT USED	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	ST DC ISOLATOR
FILE	U	S L OT	S L O T	PRE1 AC ISOLATOR											
"J"	L	E M P T Y	EMPTY	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	NOT USED						
		EX.: 1/	A, 2A, ET	C. = LOC	OP NO.'S							FS =	FLASH S	ENSE	

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES

Value (ohms) Wattage

1.5K - 1.9K 25W (min)

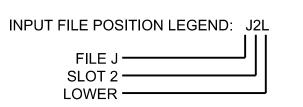
2.0K - 3.0K 10W (min)

Phase 1 Red Field

Terminal (125)

INPUT FILE POSITION LAYOUT

1B TB6-1,2 I7U 65 31 10 1 15.0 2.0 X X	LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
	1B	TB6-1,2	I7U	65	31	10	1	15.0	2.0	Х		Х	
4A TB4-9,10 I6U 41 3 8 4 2.0 X X	4A	TB4-9,10	I 6U	41	. ≺	8	4		2.0	X I		Х	



SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD SWITCH NO S7 S8 S9 S10 S11 S12 AUX S1 AUX S2 AUX S3 AUX AUX S5 AUX S6 S2 S3 S1 CMU CHANNEL NO. 13 PHASE SIGNAL HEAD NO 51 61,62 NU NU NU NU 11 NU NU 32 | 33 | 62 | 42 2 ***** 128 101 | 101 | 116 | 116 | RED 134 117 | 117 | 102 102 ***** 135 129 YELLOW 103 | 103 | 118 | 118 | GREEN 130 RED ARROW YELLOW ARROW 117 FLASHING YELLOW ARROW GREEN ARROW 127 | 127 | 118 103 133

SIGNAL HEAD HOOK-UP CHART

Ķ

- *Denotes install load resistor. See load resistor installation detail this sheet.
- ★See pictorial of head wiring in detail this sheet

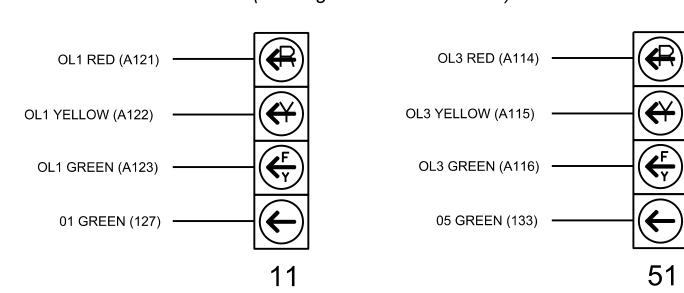
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

PROJECT REFERENCE NO.

R-5726

Sig.16.



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T1 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 1 of 3 Temporary Design 1 (TMP Phase I)

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

ivision 8 Moore County Seven Lakes REVIEWED BY: R. Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS

SEAL 034437

INIT. DATE

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-0410T1

M **MOTT MACDONALD** MOTT MACDONALD I & E, LLC

930 Main Campus Drive Suite 200 RALEIGH, NC 27606

License No. F-0669

^{**}Phase used for preemption timing purposes only

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	OFF
Included Phases	2	=	6	
Modifier Phases	1	-	5	ı
Modifier Overlaps	-	-	-	•
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH **PROGRAMMING DETAIL**

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters

Startup Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

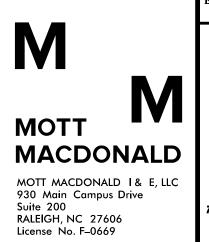
Web Interface

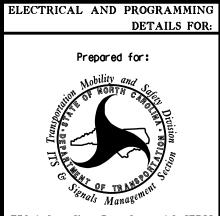
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTIOE	1	Phase Vehicle	1		Х	Х	1
NOTICE PHASE 2	2	Phase Vehicle	2		Х		2
FLASH RED	3	Phase Vehicle	3		Х	Х	3
	4	Phase Vehicle	4		Х		4
NOTICE	5	Phase Vehicle	5		Х		5
PHASE 6	6	Phase Vehicle	6		Х	Χ	6
FLASH RED	7	Phase Vehicle	7		Х		7
NOTICE _	8	Phase Vehicle	8		Х	Χ	8
OVERLAP 1	9	Overlap	1		Х	Χ	9
FLASH RED NOTICE	10	Overlap	2		X	Χ	10
OVERLAP 3	11	Overlap	3		X		11
FLASH RED	12	Overlap	4		Х		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		Χ	Χ	17
	18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T1 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:





Electrical Detail - Sheet 2 of 3 Temporary Design 1 (TMP Phase I)

NC 211 Division 8 Moore County PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS

SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive) Seven Lakes June 2024 REVIEWED BY: R. Mullinax

034437 INIT. DATE

SIG. INVENTORY NO. 08-0410T1

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

PREEMPTION PROGRAMMING

Front Panel

Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

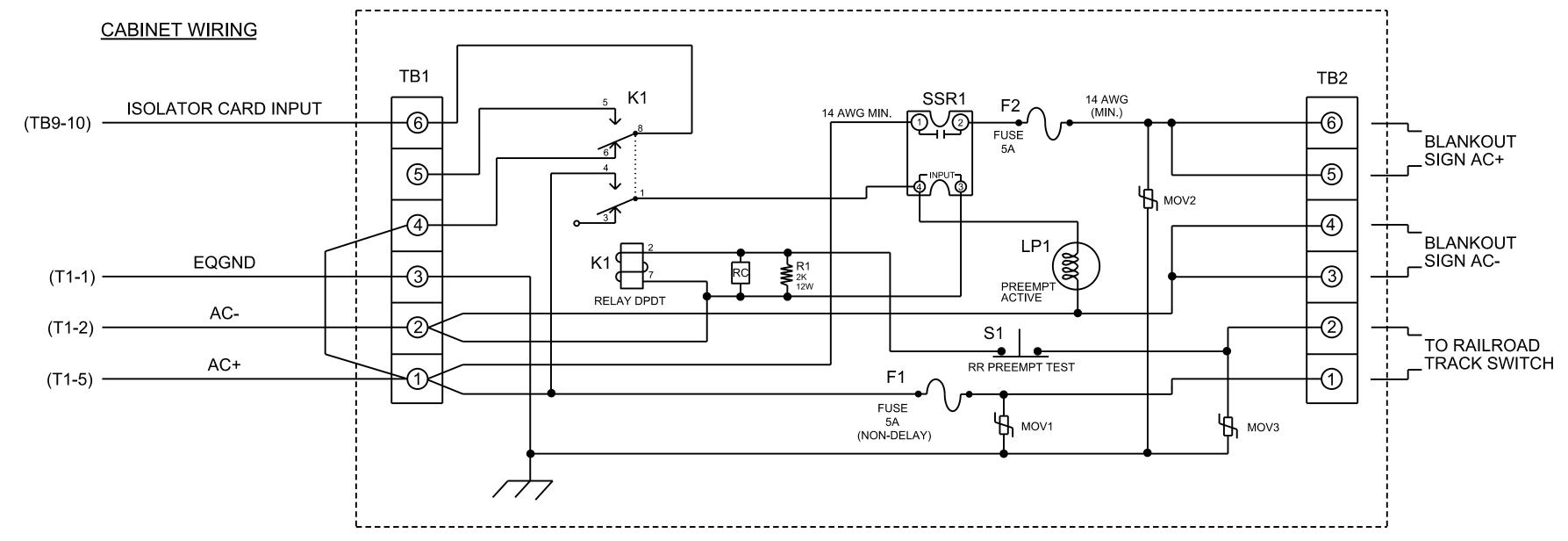
Web Interface

Home >Controller >Preempt Configuration >Preempts

Preempt Configuration

Preempt	1
Enabled	Enabled
Туре	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	-
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.7
Enter Red Clear	2.6
Track Green	18
Track Yellow Clr	3.2
Track Red Clear	2.1
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovrd Flash	Х
Not Ovrd Nxt Pre	-
Require All Red Entry	
Track Clear Ovrd	Х
Ped Clear During Yellow	-
Entry Omit OLTG	Х
Track Reserve	Х

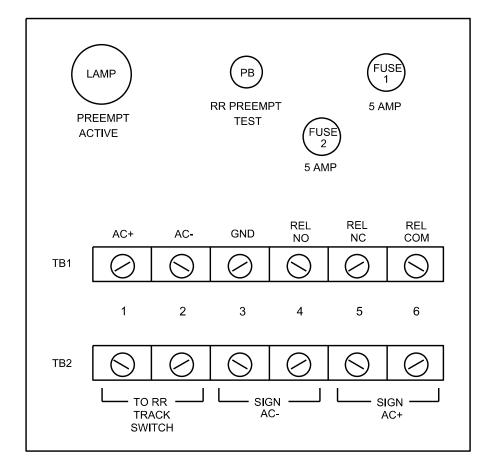
PREEMPTION AND BLANKOUT SIGN CONTROL BOX



NOTES

- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

FRONT VIEW



SEQUENCE DETAIL

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

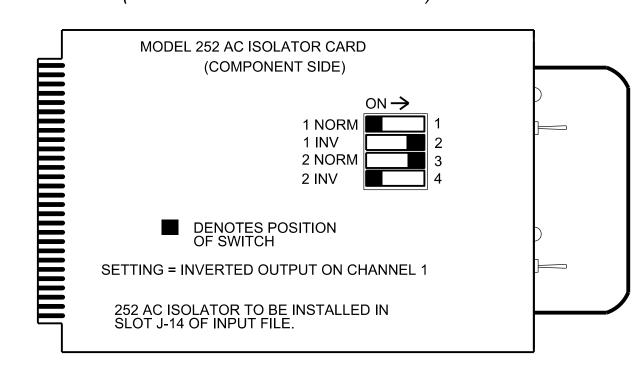
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



Electrical Detail - Sheet 3 of 3 Temporary Design 1 (TMP Phase I) ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:

M

MOTT

MACDONALD

MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive Suite 200

RALEIGH, NC 27606 License No. F-0669

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T1

DESIGNED: June 2024

SEALED: 7/11/2024

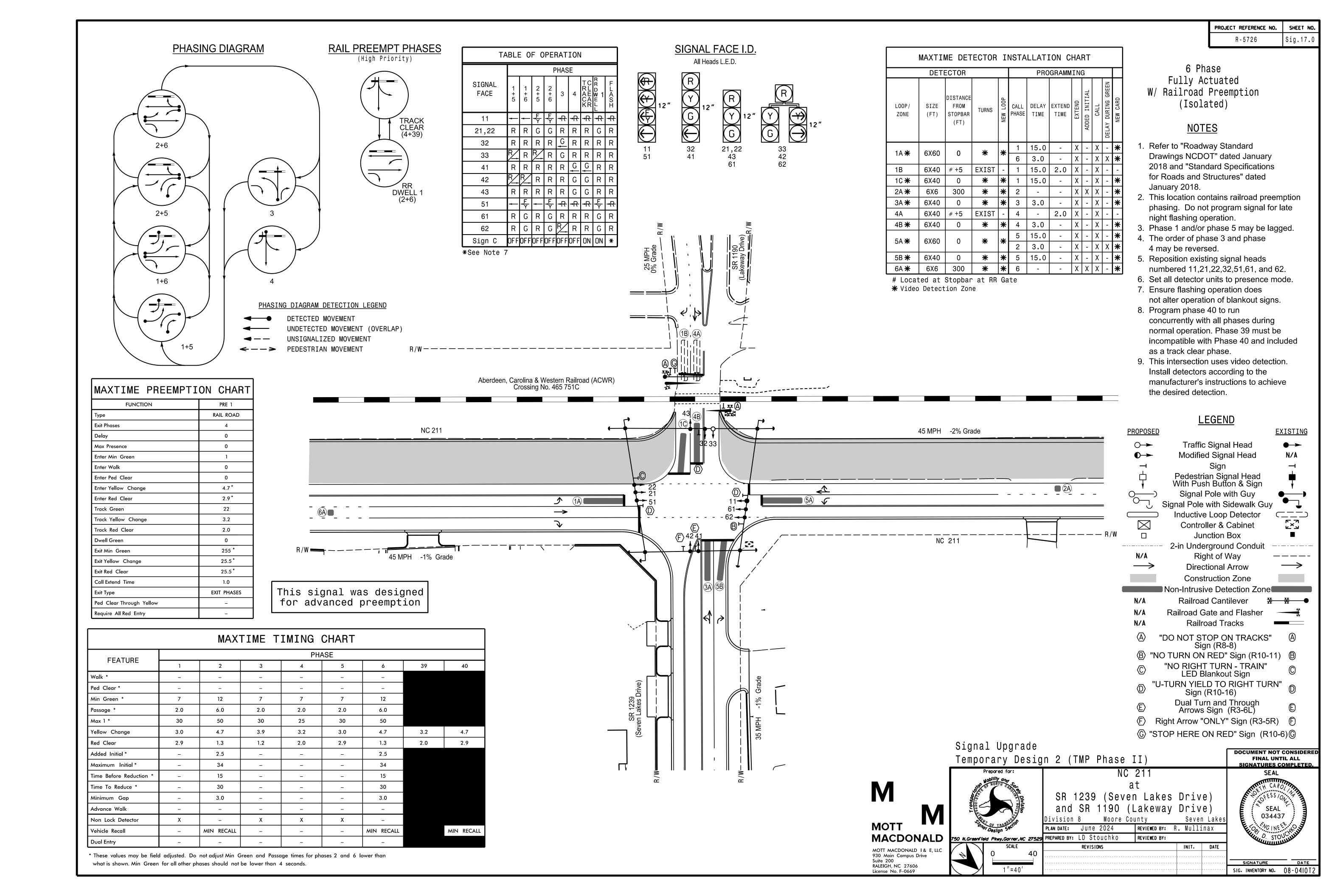
REVISED:

Division 8 Moore County Seven Lakes June 2024 REVIEWED BY: R. Mullinax PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 08-0410T1

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



REMOVE JUMPERS AS SHOWN

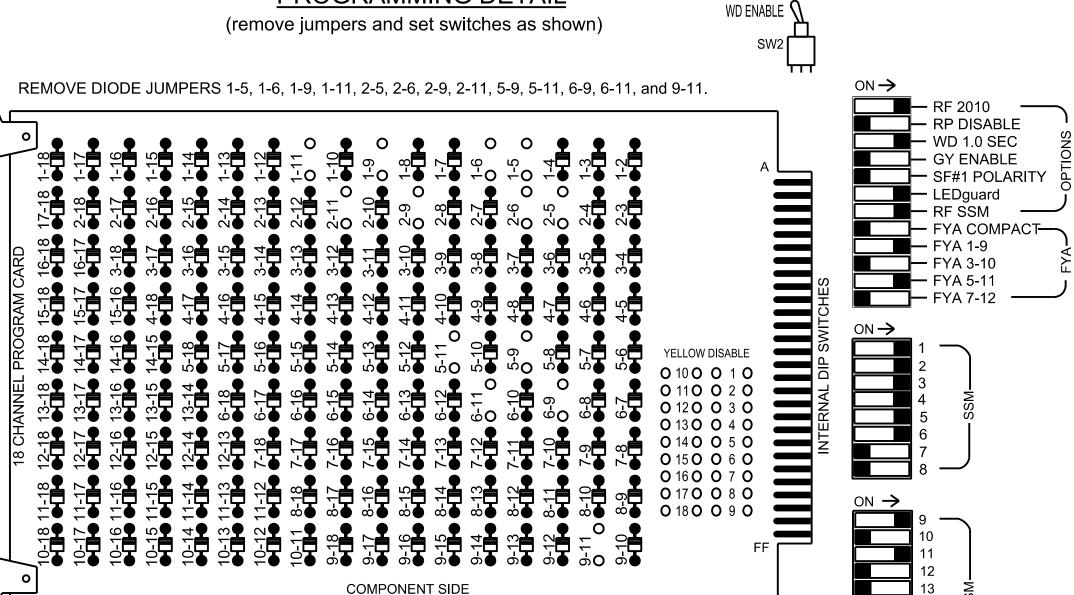
1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES:



ON

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer

- 5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Cabinet	332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	.18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	1, 2, 3, 4, 5, 6, 39**, 40**
Overlap "1"	*
Overlap "2"	
Overlap "3"	*
Overlap "4"	NOT USED
•	

- shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- 3. Program Phase 39 for No Startup Veh Call.
- 4. Program Phase 40 for Min Recall.

Controller.

= DENOTES POSITION OF SWITCH

.2070LX

*See overlap programming detail on sheet 2

PROJECT REFERENCE NO. R-5726 Sig 17.

						SIC	3N/	\L H	ΙΕΑ	DΗ	00	K-U	PC	HA	RT							
S	1	S2	S3		S4		S	55	S6	S	57	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
,	1	2	13		3		4	4	14	ţ	5	6	15	7	8	16	9	10	17	11	12	18
,	1	2	2 PED		3		4	4	4 PED	ţ		6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
11	42	21.22	NU	32	33	62	41	42,43	NU	33	★ 51	61,62	NU	NU	NU	NU	★ 11	NU	NU	★ 51	NU	NU
	*	128		116	116		101	101		*		134										
		129		117	117		102	102				135										
		130		118	118		103	103				136										
																	A121			A114		
	126					117				132							A122			A115		
																	A123			A116		
127	127			118		118	103			133	133											
	11	126	1 2 1 2 11 42 21.22	1 2 13 1 2 PED 11* 42 21.22 NU ** 128 129 130 126 126	1 2 13 1 2 PED 11* 42 21.22 NU 32 * 128 116 129 117 130 118 126	1 2 13 3 1 2 PED 3 11** 42 21.22 NU 32 33 ** 128 116 116 129 117 117 130 118 118 126	S1 S2 S3 S4 1 2 13 3 1 2 2 PED	S1 S2 S3 S4 S 1 2 13 3 4 1 2 2PED 3 4 11* 42 21.22 NU 32 33 62 41 * 128 116 116 101 101 129 117 117 102 130 118 118 103 126 117 117 117	S1 S2 S3 S4 S5 1 2 13 3 4 1 2 2 PED 3 4 11 42 21.22 NU 32 33 62 41 42,43 4 128 116 116 10 101 101 129 117 117 102 102 130 118 118 118 103 103 126 126 117 <td>S1 S2 S3 S4 S5 S6 1 2 13 3 4 14 1 2 2 PED 3 4 PED 11 42 21.22 NU 32 33 62 41 42,43 NU 4 128 116 116 16 101 101 101 129 117 117 102 102 102 130 118 118 103 103 103 126 126 117 117 117 117 117</td> <td>S1 S2 S3 S4 S5 S6 S 1 2 13 3 4 14 8 1 2 PED 3 4 PED 8 11 42 21.22 NU 32 33 62 41 42,43 NU 33 * 128 116 116 116 101 101 101 * 129 117 117 102 102 102 102 130 118 118 103 103 132 126 117 117 117 117 132</td> <td>S1 S2 S3 S4 S5 S6 S7 1 2 13 3 4 14 5 1 2 PED 3 4 PED 5 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 * 128 116 116 101 101 101 * * 129 117 117 102 102 . . . 130 118 118 103 103 . . . 126 . . . 117 126 .</td> <td>S1 S2 S3 S4 S5 S6 S7 S8 1 2 13 3 4 14 5 6 1 2 2 PED 3 4 PED 5 6 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 * 128 116 116 101 101 101 * 134 129 117 117 102 102 102 135 130 118 118 118 103 103 132 136 126 126 117 117 117 117 117 132 132</td> <td>S1 S2 S3 S4 S5 S6 S7 S8 S9 1 2 13 3 4 14 5 6 15 1 2 2 PED 3 4 42 5 6 6 PED 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU * 128 116 116 101 101 101 * 134 129 117 117 102 102 135 130 118 118 103 103 136 126 117 132 </td> <td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 1 2 13 3 4 14 5 6 15 7 1 2 $\frac{2}{PED}$ 3 4 $\frac{4}{PED}$ 5 6 $\frac{6}{PED}$ 7 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU 128 116 116 101 101 101 3 134 134 134 134 134 134 134 134 135 135 135 135 135 135 136<!--</td--><td>$\begin{array}{c ccccccccccccccccccccccccccccccccccc$</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 1 2 13 3 4 14 5 6 15 7 8 16 1 2 $\frac{2}{PED}$ 3 4 $\frac{4}{PED}$ 5 6 $\frac{6}{PED}$ 7 8 $\frac{8}{PED}$ 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU <td< td=""><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 1 2 13 3 4 14 5 6 15 7 8 16 9 1 2 PED 3 4 PED 5 6 66 7 8 PED OL1 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 1 2 13 3 4 14 5 6 15 7 8 16 9 10 1 2 $\frac{2}{\text{PED}}$ 3 4 $\frac{4}{\text{PED}}$ 5 6 $\frac{6}{\text{PED}}$ 7 8 $\frac{8}{\text{PED}}$ OL1 OL2 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61,62 NU <</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S2 AUX S3 AUX S4 AUX S3 AUX S3 AUX S4 AUX S4 AUX S4 AUX S4</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S3 AS4 AUX S2 S3 S4 S4 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 1 2 PED 3 4 4 9 15 6 6 7 8 8B PED 0L1 0L2 SPARE 0L3 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU NU NU NU NU NU 11 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S2 S3 S3 S4 S4 S5 AUX S2 S3 S4 S4 S5 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 12 1 2 PED 3 4 PED 5 6 PED 7 8 PED OL1 OL2 SPARE OL3 OL4 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61.62 NU NU NU NU NU NU NU NU 11 NU NU</td></td<></td></td>	S1 S2 S3 S4 S5 S6 1 2 13 3 4 14 1 2 2 PED 3 4 PED 11 42 21.22 NU 32 33 62 41 42,43 NU 4 128 116 116 16 101 101 101 129 117 117 102 102 102 130 118 118 103 103 103 126 126 117 117 117 117 117	S1 S2 S3 S4 S5 S6 S 1 2 13 3 4 14 8 1 2 PED 3 4 PED 8 11 42 21.22 NU 32 33 62 41 42,43 NU 33 * 128 116 116 116 101 101 101 * 129 117 117 102 102 102 102 130 118 118 103 103 132 126 117 117 117 117 132	S1 S2 S3 S4 S5 S6 S7 1 2 13 3 4 14 5 1 2 PED 3 4 PED 5 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 * 128 116 116 101 101 101 * * 129 117 117 102 102 . . . 130 118 118 103 103 . . . 126 . . . 117 126 .	S1 S2 S3 S4 S5 S6 S7 S8 1 2 13 3 4 14 5 6 1 2 2 PED 3 4 PED 5 6 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 * 128 116 116 101 101 101 * 134 129 117 117 102 102 102 135 130 118 118 118 103 103 132 136 126 126 117 117 117 117 117 132 132	S1 S2 S3 S4 S5 S6 S7 S8 S9 1 2 13 3 4 14 5 6 15 1 2 2 PED 3 4 42 5 6 6 PED 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU * 128 116 116 101 101 101 * 134 129 117 117 102 102 135 130 118 118 103 103 136 126 117 132	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 1 2 13 3 4 14 5 6 15 7 1 2 $\frac{2}{PED}$ 3 4 $\frac{4}{PED}$ 5 6 $\frac{6}{PED}$ 7 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU 128 116 116 101 101 101 3 134 134 134 134 134 134 134 134 135 135 135 135 135 135 136 </td <td>$\begin{array}{c ccccccccccccccccccccccccccccccccccc$</td> <td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 1 2 13 3 4 14 5 6 15 7 8 16 1 2 $\frac{2}{PED}$ 3 4 $\frac{4}{PED}$ 5 6 $\frac{6}{PED}$ 7 8 $\frac{8}{PED}$ 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU <td< td=""><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 1 2 13 3 4 14 5 6 15 7 8 16 9 1 2 PED 3 4 PED 5 6 66 7 8 PED OL1 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 1 2 13 3 4 14 5 6 15 7 8 16 9 10 1 2 $\frac{2}{\text{PED}}$ 3 4 $\frac{4}{\text{PED}}$ 5 6 $\frac{6}{\text{PED}}$ 7 8 $\frac{8}{\text{PED}}$ OL1 OL2 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61,62 NU <</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S2 AUX S3 AUX S4 AUX S3 AUX S3 AUX S4 AUX S4 AUX S4 AUX S4</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S3 AS4 AUX S2 S3 S4 S4 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 1 2 PED 3 4 4 9 15 6 6 7 8 8B PED 0L1 0L2 SPARE 0L3 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU NU NU NU NU NU 11 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S2 S3 S3 S4 S4 S5 AUX S2 S3 S4 S4 S5 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 12 1 2 PED 3 4 PED 5 6 PED 7 8 PED OL1 OL2 SPARE OL3 OL4 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61.62 NU NU NU NU NU NU NU NU 11 NU NU</td></td<></td>	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 1 2 13 3 4 14 5 6 15 7 8 16 1 2 $\frac{2}{PED}$ 3 4 $\frac{4}{PED}$ 5 6 $\frac{6}{PED}$ 7 8 $\frac{8}{PED}$ 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU <td< td=""><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 1 2 13 3 4 14 5 6 15 7 8 16 9 1 2 PED 3 4 PED 5 6 66 7 8 PED OL1 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 1 2 13 3 4 14 5 6 15 7 8 16 9 10 1 2 $\frac{2}{\text{PED}}$ 3 4 $\frac{4}{\text{PED}}$ 5 6 $\frac{6}{\text{PED}}$ 7 8 $\frac{8}{\text{PED}}$ OL1 OL2 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61,62 NU <</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S2 AUX S3 AUX S4 AUX S3 AUX S3 AUX S4 AUX S4 AUX S4 AUX S4</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S3 AS4 AUX S2 S3 S4 S4 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 1 2 PED 3 4 4 9 15 6 6 7 8 8B PED 0L1 0L2 SPARE 0L3 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU NU NU NU NU NU 11 NU NU</td><td>S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S2 S3 S3 S4 S4 S5 AUX S2 S3 S4 S4 S5 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 12 1 2 PED 3 4 PED 5 6 PED 7 8 PED OL1 OL2 SPARE OL3 OL4 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61.62 NU NU NU NU NU NU NU NU 11 NU NU</td></td<>	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 1 2 13 3 4 14 5 6 15 7 8 16 9 1 2 PED 3 4 PED 5 6 66 7 8 PED OL1 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 1 2 13 3 4 14 5 6 15 7 8 16 9 10 1 2 $\frac{2}{\text{PED}}$ 3 4 $\frac{4}{\text{PED}}$ 5 6 $\frac{6}{\text{PED}}$ 7 8 $\frac{8}{\text{PED}}$ OL1 OL2 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61,62 NU <	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S2 AUX S3 AUX S4 AUX S3 AUX S3 AUX S4 AUX S4 AUX S4 AUX S4	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S3 AS4 AUX S2 S3 S4 S4 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 1 2 PED 3 4 4 9 15 6 6 7 8 8B PED 0L1 0L2 SPARE 0L3 11 42 21.22 NU 32 33 62 41 42,43 NU 33 51 61,62 NU NU NU NU NU NU NU 11 NU NU	S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S2 S2 S3 S3 S4 S4 S5 AUX S2 S3 S4 S4 S5 1 2 13 3 4 14 5 6 15 7 8 16 9 10 17 11 12 1 2 PED 3 4 PED 5 6 PED 7 8 PED OL1 OL2 SPARE OL3 OL4 11 42 21.22 NU 32 33 62 41 42.43 NU 33 51 61.62 NU NU NU NU NU NU NU NU 11 NU NU

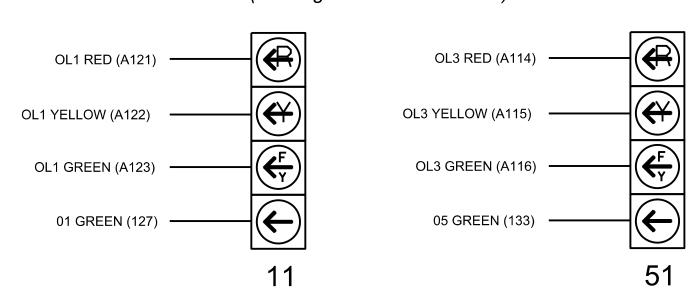
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

_	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	S L O T	SLOT I	SLOT	%L0⊢ L	SLOT	Ø 4 4A	ø 1 1B	SLOT L	SLOT	SLOT I	8L0F L	8L0F L	S L O T	FS DC ISOLATOR
" "	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	NOT USED	NOT USED	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	ST DC ISOLATOR
FILE U	S L O T	SLOT	SLOT	SLOT	S L O T	S L O T	S L O T	SLOT	SLOT	SLOF	SLOT	SLOT	S L O T	PRE1 AC ISOLATOR
"J"	E M P T Y	E M P T Y	E M P T Y	EMPTY	E M P T Y	E M P T Y	E M P T Y	E M P T Y	EMPTY	EMPTY	EMPTY	E M P T Y	E M P T Y	NOT USED
L	EX.: 1/	A, 2A, ET	C. = LOC	P NO.'S		<u> </u>		<u> </u>	<u> </u>	·	FS = F	FLASH S	ENSE	

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.		DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1B	TB6-1,2	I7U	65	31	10	1	15.0	2.0	Х		Х	
4A	TB4-9,10	I 6U	41	3	8	4		2.0	Х		X	

INPUT FILE POSITION LEGEND: J2L SLOT 2 -LOWER -

SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T2 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 1 of 3 Temporary Design 2 (TMP Phase II)

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

NC 211 SR 1239 (Seven Lakes Drive)

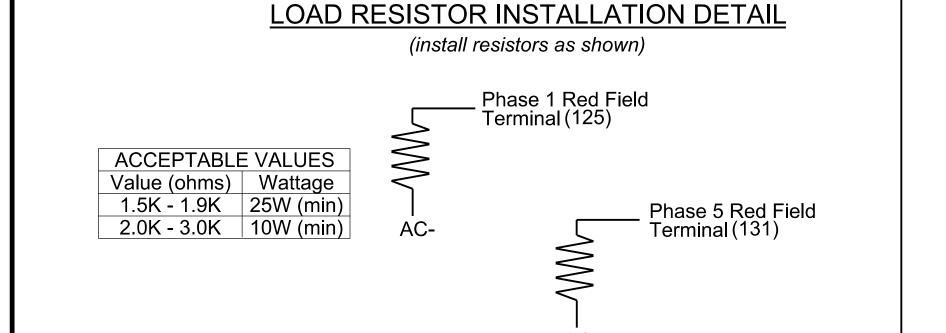
and SR 1190 (Lakeway Drive) ivision 8 Moore County Seven Lakes REVIEWED BY: R. Mullinax PLAN DATE: June 2024 PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS

034437 INIT. DATE

SIG. INVENTORY NO. 08-0410 T 2

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MOTT **MACDONALD** MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606

^{**}Phase used for preemption timing purposes only

Front Panel

Main Menu > Controller > Overlap > Overlap Parameters / Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	OFF	FYA 4 - Section	OFF
Included Phases	2	1	6	-
Modifier Phases	1	1	5	1
Modifier Overlaps	-	1	-	ı
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters Startup Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTICE	1	Phase Vehicle	1		Х	X	1
NOTICE PHASE 2	2	Phase Vehicle	2		Х		2
FLASH RED	3	Phase Vehicle	3		Х	Х	3
	4	Phase Vehicle	4		Χ		4
NOTICE	5	Phase Vehicle	5		Χ		5
PHASE 6 FLASH RED	6	Phase Vehicle	6		Χ	Χ	6
FLASH KED	7	Phase Vehicle	7		Χ		7
NOTICE _	8	Phase Vehicle	8		Χ	Χ	8
OVERLAP 1 FLASH RED	9	Overlap	1		X	Χ	9
NOTICE .	10	Overlap	2		Χ	Χ	10
OVERLAP 3	11	Overlap	3		Χ		11
FLASH RED	12	Overlap	4		Χ		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		Χ	Χ	17
	18	Overlap	6		Х		18

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T2 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 2 of 3 Temporary Design 2 (TMP Phase II)

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

MOTT

MACDONALD

MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606 License No. F–0669

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

Seven Lakes REVIEWED BY: R. Mullinax June 2024 PREPARED BY: LD Stouchko REVIEWED BY:

> INIT. DATE SIG. INVENTORY NO. 08-0410T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

034437

REVISIONS

Division 8 Moore County PLAN DATE:

RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

PREEMPTION PROGRAMMING

Front Panel

Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

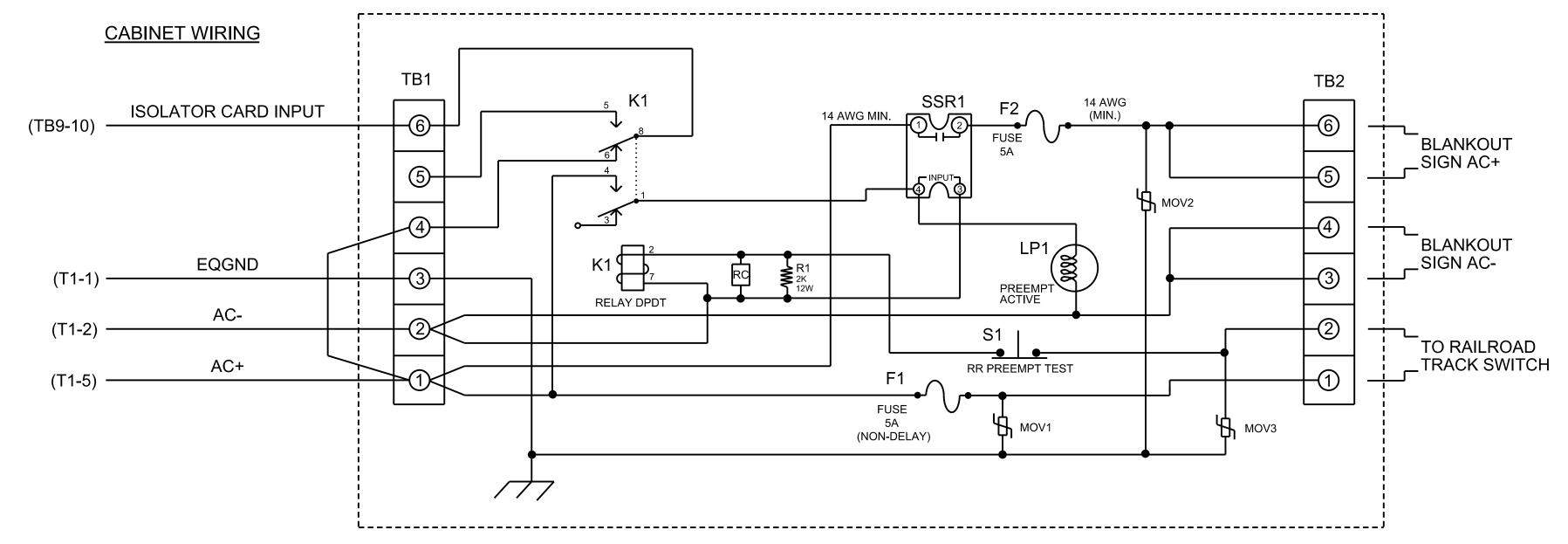
Web Interface

Home >Controller >Preempt Configuration >Preempts

Preempt Configuration

Preempt Com	guration
Preempt	1
Enabled	Enabled
Type	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	-
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.7
Enter Red Clear	2.9
Track Green	22
Track Yellow Clr	3.2
Track Red Clear	2.0
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovrd Flash	X
Not Ovrd Nxt Pre	<u> </u>
Require All Red Entry	-
Track Clear Ovrd	Х
Ped Clear During Yellow	-
Entry Omit OLTG	Х
Track Reserve	Х

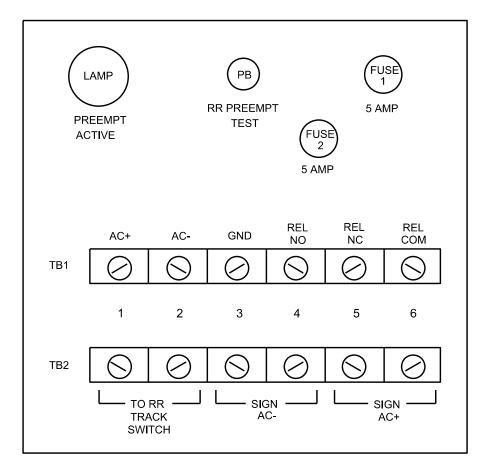
PREEMPTION AND BLANKOUT SIGN CONTROL BOX



NOTES

- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

FRONT VIEW



SEQUENCE DETAIL

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

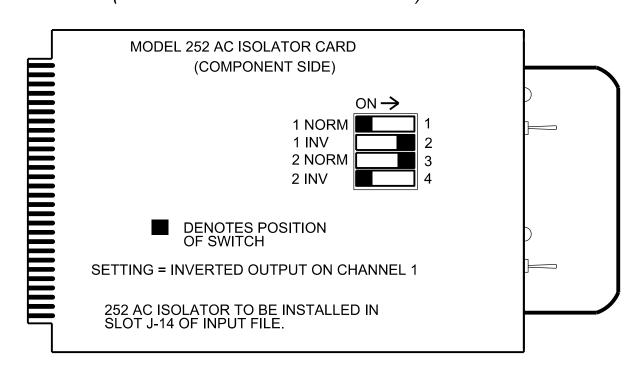
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

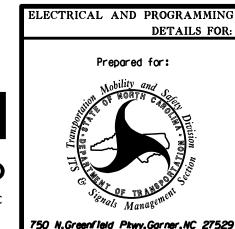
PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T2 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 3 of 3 Temporary Design 2 (TMP Phase II)



SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

NC 211

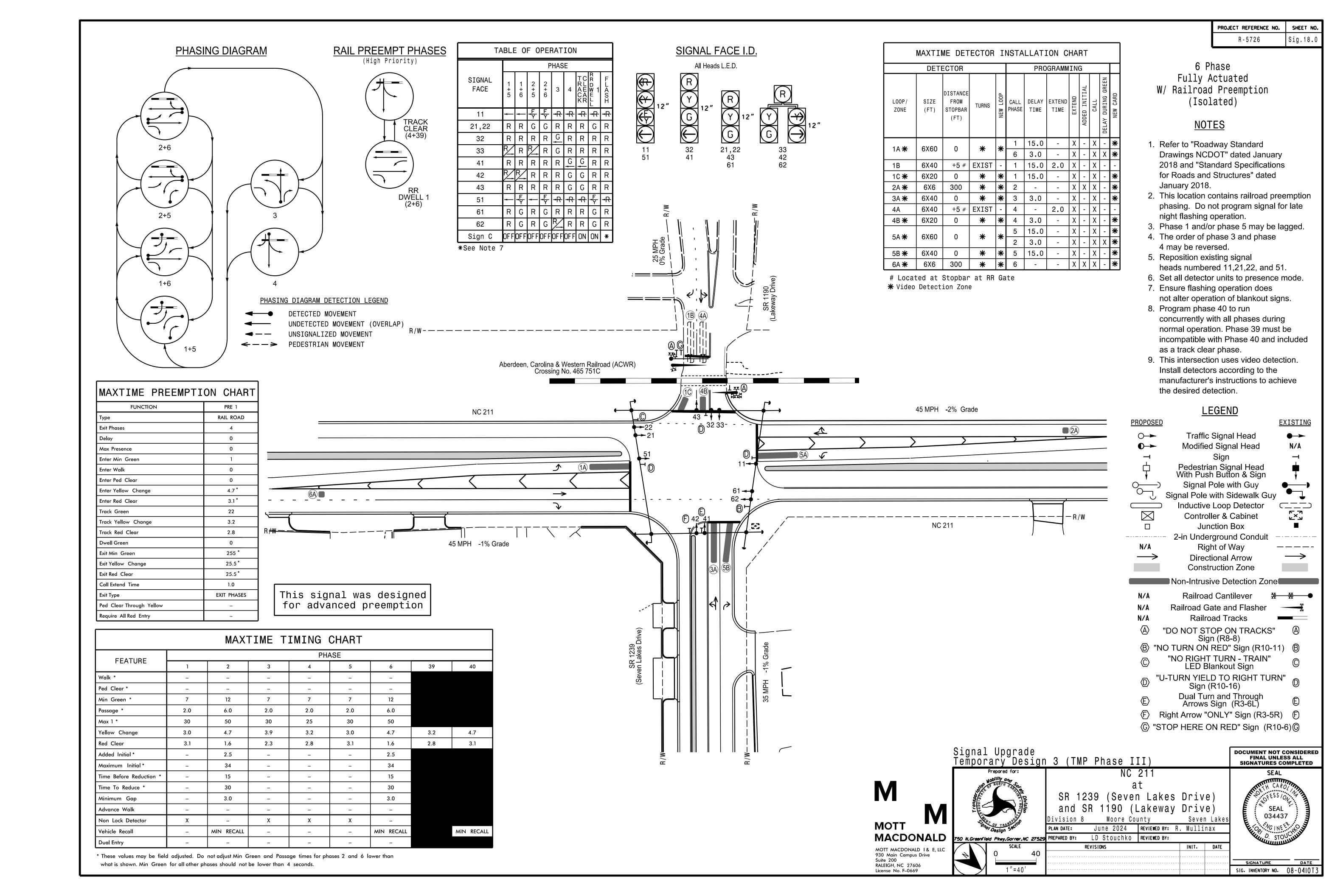
Division 8 Moore County Seven Lakes June 2024 REVIEWED BY: R. Mullinax PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-0410T2

MOTT MACDONALD MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606



REMOVE JUMPERS AS SHOWN

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES:

WD ENABLE **\(\)** (remove jumpers and set switches as shown) REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11, and 9-11. GY ENABLE NTERNAL DIS CALLOCAL SF#1 POLARITY — LEDguard — RF SSM FYA COMPACT— FYA 1-9 FYA 3-10 — FYA 5-11 FYA 7-12

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer
- 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- 3. Program Phase 39 for No Startup Veh Call.
- 5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	1, 2, 3, 4, 5, 6, 39**, 40**
Overlap "1"	 *
Overlap "2"	NOT USED
Overlan "3"	*

**Phase used for preemption timing purposes only

- shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk,
- 4. Program Phase 40 for Min Recall.

Overlap "4"....

= DENOTES POSITION OF SWITCH

*See overlap programming detail on sheet 2

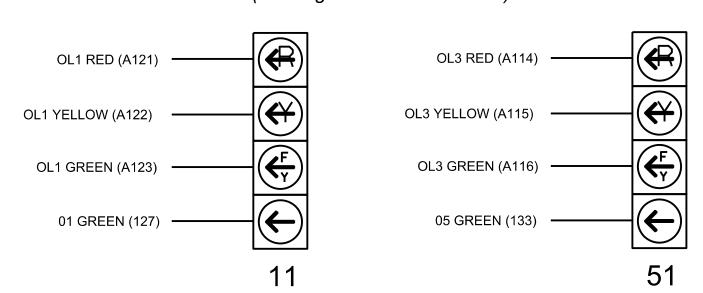
PROJECT REFERENCE NO. Sig.18.

							SIC	GN/	\L H	ΙΕΑ	DΗ	00	K-U	P C	HA	RT							
LOAD SWITCH NO.	S	51	S2	S3		S4		S5		S6	S	57	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	,	1	2	13		3		4		14	ļ	5	6	15	7	8	16	9	10	17	11	12	18
PHASE		1	2	2 PED		3		4		4 PED	ţ	5	6	6 PED	7	8	8 PED	OL1		SPARE			SPARE
SIGNAL HEAD NO.	★ 11	42	21.22	NU	32	33	62	41	42,43	NU	33	★ 51	61,62	NU	NU	NU	NU	11	NU	NU	★ 51	NU	NU
RED		*	128		116	116		101	101		*		134										
YELLOW			129		117	117		102	102				135										
GREEN			130		118	118		103	103				136										
RED ARROW																		A121			A114		
YELLOW ARROW		126					117				132							A122			A115		
FLASHING YELLOW ARROW																		A123			A116		
GREEN ARROW	127	127			118		118	103			133	133											
*																							
*																							
NU = Not Us																							

*Denotes install load resistor. See load resistor installation detail this sheet. igstar See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	S L O T E	S L O T	S L O T E	S L O T E	S L O T	Ø 4 4A	Ø 1 1B	S L O T	S L O T	SLOT E	S L O T	SLOT E	S L O T	FS DC ISOLATOR
" "	M P T Y	E M P T Y	M P T Y	M P T Y	E M P T Y	NOT USED	NOT USED	E M P T Y	M P T Y	MPTY	E M P T Y	M P T Y	E M P T Y	DC ISOLATOR
FILE U	SLOT E	SLOT EM	S L O T E	S L O T E	SLOT E	S L O T E	SLOT E	SLOT E	SLOT E	SLOF E	SLOT E	SLOT E	S L O T E	PRE1 AC ISOLATOR
J	M P T Y	M P T Y	M P T Y	M P T Y	E M P T Y	M P T Y	M P T Y	E M P T Y	M P T Y	M P T Y	M P T Y	M P T Y	E M P T Y	NOT USED

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

ON

INPUT FILE CONNECTION & PROGRAMMING CHART

....NOT USED

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1B	TB6-1,2	1 7U	65	31	10	1	15.0	2.0	Х		Х	
4A	TB4-9,10	I6U	41	3	8	4		2.0	Х		Χ	

INPUT FILE POSITION LEGEND: J2L SLOT 2 -LOWER -

SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T3 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

M **MOTT MACDONALD** MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive Suite 200

RALEIGH, NC 27606 License No. F-0669

Electrical Detail - Sheet 1 of 3 Temporary Design 3 (TMP Phase III) ELECTRICAL AND PROGRAMMING

DETAILS FOR: Prepared for:

NC 211 SR 1239 (Seven Lakes Drive)

and SR 1190 (Lakeway Drive) Moore County Seven Lakes ivision 8 PLAN DATE:

REVIEWED BY: R. Mullinax June 2024 PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 08-0410 T 3

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Phase 1 Red Field Terminal (125) ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) Phase 5 Red Field 2.0K - 3.0K 10W (min) Terminal (131)

EX.: 1A, 2A, ETC. = LOOP NO.'S

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	OFF
Included Phases	2	-	6	-
Modifier Phases	1	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters Startup Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTIOE	1	Phase Vehicle	1		Х	Χ	1
NOTICE PHASE 2	2	Phase Vehicle	2		Х		2
FLASH RED	3	Phase Vehicle	3		Х	Х	3
	4	Phase Vehicle	4		Χ		4
NOTICE .	5	Phase Vehicle	5		Х		5
PHASE 6 FLASH RED	6	Phase Vehicle	6		Χ	Χ	6
FLASH RED	7	Phase Vehicle	7		Χ		7
NOTICE .	8	Phase Vehicle	8		Χ	Χ	8
OVERLAP 1	9	Overlap	1		Χ	Χ	9
FLASH RED NOTICE	10	Overlap	2		Χ	Χ	10
OVERLAP 3	11	Overlap	3		Χ		11
FLASH RED	12	Overlap	4		Χ		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		Х	Х	17
	18	Overlap	6		Χ		18

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T3 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 2 of 3 Temporary Design 3 (TMP Phase III)

ELECTRICAL AND PROGRAMMING **MOTT MACDONALD** MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606 License No. F–0669

DETAILS FOR: Prepared for:

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

Division 8 Moore County Seven Lakes REVIEWED BY: R. Mullinax June 2024 PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS INIT. DATE 034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PREEMPTION PROGRAMMING

Front Panel

Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

Web Interface

Home >Controller >Preempt Configuration >Preempts

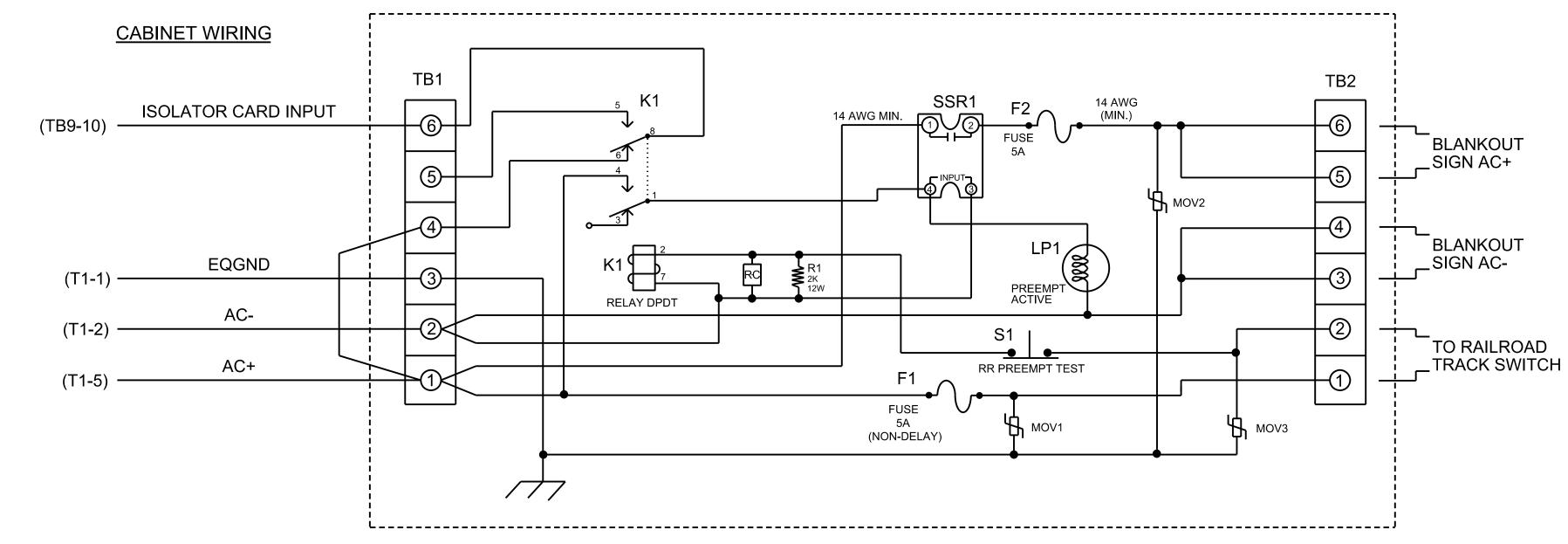
Preempt Configuration

<u></u>	
Preempt	1
Enabled	Enabled
Туре	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	÷
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.7
Enter Red Clear	3.1
Track Green	22
Track Yellow Clr	3.2
Track Red Clear	2.8
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovrd Flash	Х
Not Ovrd Nxt Pre	-
Require All Red Entry	-
Track Clear Ovrd	Х
Ped Clear During Yellow	-
Entry Omit OLTG	Х
Track Reserve	Х

RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

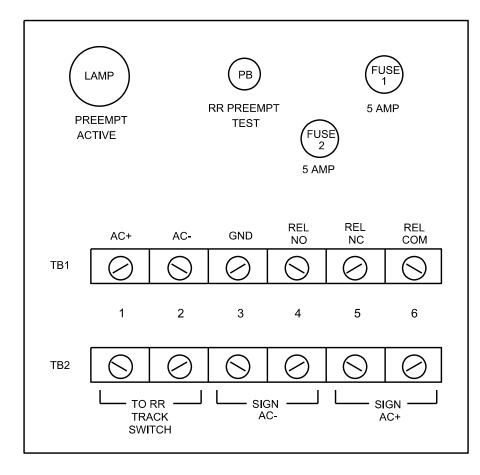
PREEMPTION AND BLANKOUT SIGN CONTROL BOX



NOTES

- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

FRONT VIEW



SEQUENCE DETAIL

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

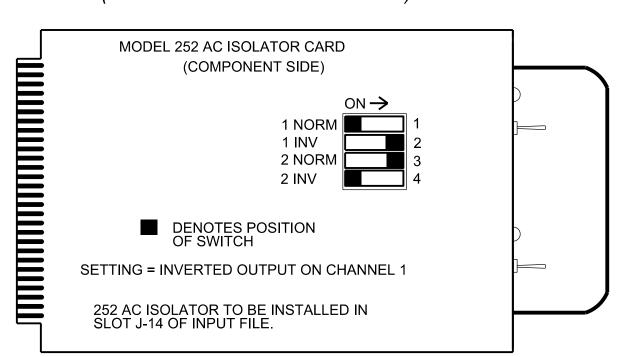
Home >Controller >Sequence

Sequence 1

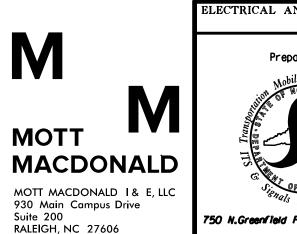
Ring	Sequence Data									
1	1,2,a,3,4,b									
2	5,6,a,7,8,b									
3	39,c,40,d									

PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410T3
DESIGNED: June 2024
SEALED: 7/11/2024
REVISED:



License No. F-0669

Electrical Detail - Sheet 3 of 3
Temporary Design 3 (TMP Phase III)

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:

SR 1239 (Seven Lakes Drive)

and SR 1190 (Lakeway Drive)

Division 8 Moore County Seven Lake

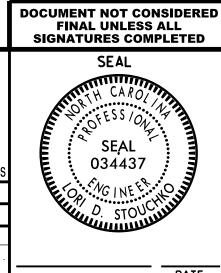
and SR 1190 (Lakeway Drive)

Division 8 Moore County Seven Lakes

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

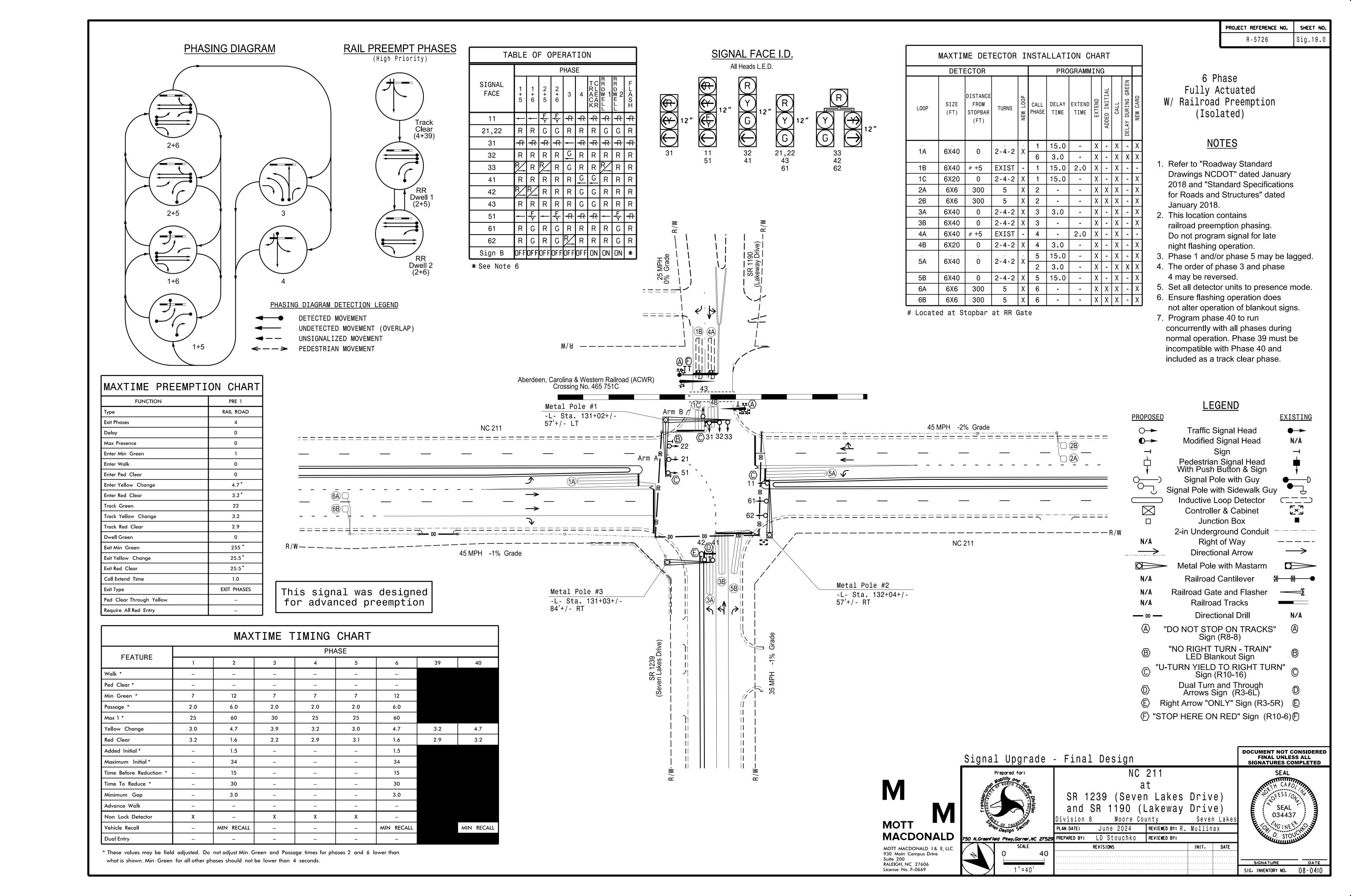
PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS INIT. DATE



PROJECT REFERENCE NO.

R-5726



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- 3. Program Phase 39 for No Startup Veh Call.
- 4. Program Phase 40 for Min Recall.
- 5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

....NOT USED

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	1, 2, 3, 4, 5, 6, 39**, 40**
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	*

*See overlap programming detail on sheet 2 **Phase used for preemption timing purposes only

Overlap "4".....

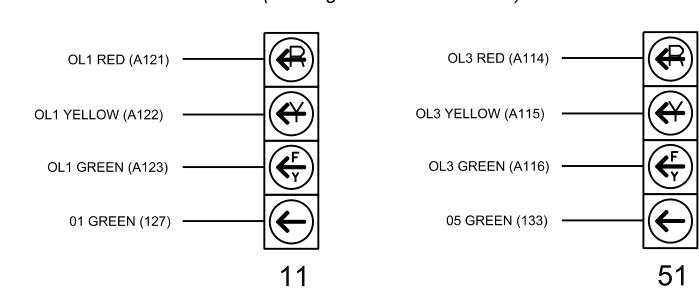
PROJECT REFERENCE NO. R-5726 Sig.19.

									SIGNAL HEAD HOOK-UP CHART															
LOAD SWITCH NO.	S	51	S2	S3		S	4		S5		S6	S6 S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	,	1	2	13		3	3		4		14 5		5	6	15	7	8	16	9	10	17	11	12	18
PHASE		1	2	2 PED		3	3		2	4	4 PED	Ę		6	6 PED	7	8	8 PED	OL1		SPARE		OL4	SPARE
SIGNAL HEAD NO.	★ 11	42	21.22	NU	31	32	33	62	41	42,43	NU	33	★ 51	61,62	NU	NU	NU	NU	★ 11	NU	NU	★ 51	NU	NU
RED		*	128			116	116		101	101		*		134										
YELLOW			129			117	117		102	102				135										
GREEN			130			118	118		103	103				136										
RED ARROW					116														A121			A114		
YELLOW ARROW		126			117			117				132							A122			A115		
FLASHING YELLOW ARROW																			A123			A116		
GREEN ARROW	127	127			118	118		118	103			133	133											
*																								
ķ																								
NII = Not II	lsed																							

*Denotes install load resistor. See load resistor installation detail this sheet. ★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

,	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	Ø 1	ø 2 2A	SLOF	S L O T	ø 3 3A	Ø 4 4A	Ø 1 1B	SLOT	SLOT	SLOT	S L O T	S L O T	S L O T	FS DC ISOLATOR
" " _	NOT USED	ø 2 2B	E M P T Y	E M P T Y	NOT USED	ø 4 4B	Ø 1 1C	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	ST DC ISOLATOR
FILE U	ø 5 5A	ø 6 6A	SLOT	S L O T	S L O T	ø 3 3B	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	PRE1 AC ISOLATOR
"J"	NOT USED	Ø 6 6B	E M P T Y	E M P T Y	E M P T Y	ø 5 5B	E M P T Y	NOT USED						

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

= DENOTES POSITION OF SWITCH

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1.0	TD2 4.2	1411	56	18	1	1	15.0		Х		Х	
1A	TB2-1,2	I1U	96	-	29	6	3.0		Х		Х	Х
1B	TB6-1,2	17U	65	31	10	1	15.0	2.0	Х		Х	
1C	TB6-3,4	I7L	78	44	11	1	15.0		Х		Х	
2A	TB2-5,6	1 2U	39	1	2	2			Х	Χ	Χ	
2B	TB2-7,8	I2L	43	5	3	2			Х	Х	Χ	
3A	TB4-5,6	I5U	58	20	7	3	3.0		Х		Χ	
3B	TB5-9,10	J6U	42	4	22	3			Х		Χ	
4A	TB4-9,10	I6U	41	3	8	4		2.0	Х		Χ	
4B	TB4-11,12	I6L	45	7	9	4	3.0		Х		Χ	
5A	TB3-1,2	J1U	55	17	15	5	15.0		Х		Χ	
JA	163-1,2	310	55	-	31	2	3.0		Х		Χ	Х
5B	TB5-11,12	J6L	46	8	23	5	15.0		Х		Χ	
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Χ	
6B	TB3-7,8	J2L	44	6	17	6			Х	Х	Х	

INPUT FILE POSITION LEGEND: J2L LOWER —

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 1 of 3 ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

Moore County ivision 8 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax PREPARED BY: LD Stouchko REVIEWED BY:

Seven Lakes REVISIONS INIT. DATE

034437

SIG. INVENTORY NO.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K 10W (min)

EX.: 1A, 2A, ETC. = LOOP NO.'S

Phase 1 Red Field Terminal (125)

Phase 5 Red Field Terminal (131)

MOTT **MACDONALD**

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License No. F-0669

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	OFF	FYA 4 - Section	OFF
Included Phases	2	=	6	
Modifier Phases	1	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH **PROGRAMMING DETAIL**

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters Startup Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

OUTPUT CHANNEL CONFIGURATION

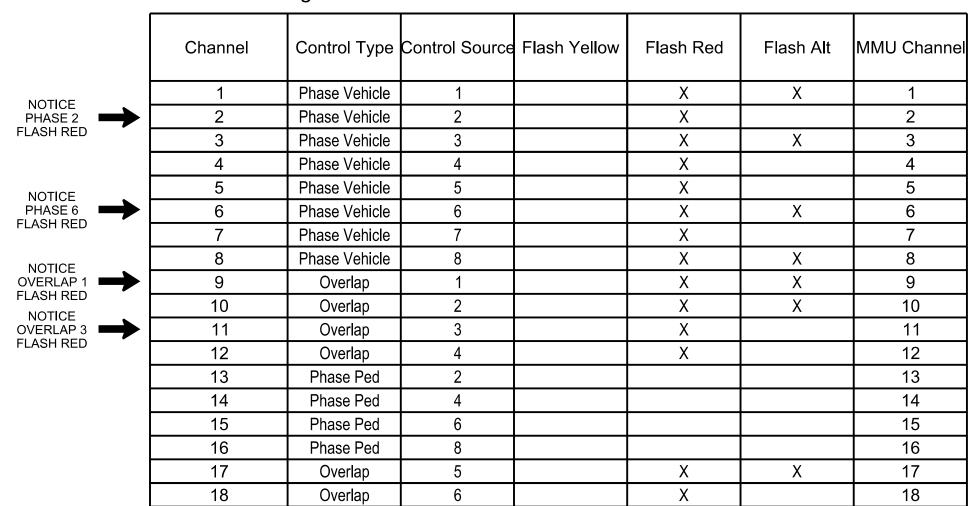
Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410 DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

Electrical Detail - Sheet 2 of 3

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License No. F-0669

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive) Division 8 Moore County

Seven Lakes June 2024 REVIEWED BY: R. Mullinax PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

034437

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-0410

PLAN DATE: 750 N.Greenfield Pkwy.Garner.NC 27529

BLANKOUT

BLANKOUT

TO RAILROAD

_TRACK SWITCH

_SIGN AC-

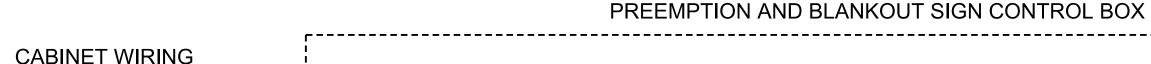
—6

RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

FUSE

LP1
PREEMPT
ACTIVE



CABINET WIRING ISOLATOR CARD INPUT

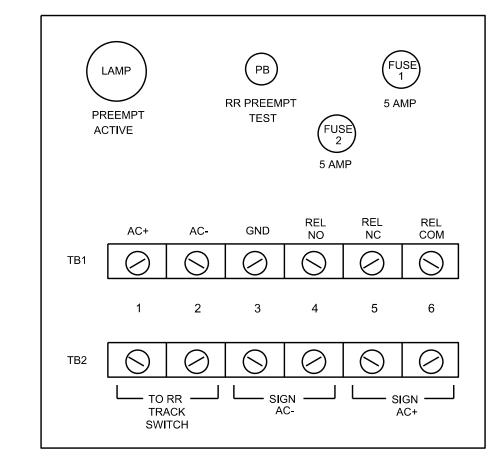
EQGND (T1-1) -RELAY DPDT AC-

RR PREEMPT TEST FUSE 5A ₩ MOV3 MOV1 (NON-DELAY)

NOTES

- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

FRONT VIEW



SEQUENCE DETAIL

PREEMPTION PROGRAMMING

Enabled

Rail Road

4,39

2,5

3

2,5,6

3

4

0

1.0

0

Terminate

1

0

0

4.7

3.2

22

3.2

2.9

255

25.5

25.5

Exit Phases

Χ

Χ

Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

Home >Controller >Preempt Configuration >Preempts

Preempt

Enabled

Type

Track Phases

Track Overlaps

Dwell Phases

Dwell Peds

Dwell Overlaps

Cycling Phases Cycling Peds

Cycling Overlaps

Exit Phases

Exit Overlaps

Delay Call Ext Time

Max Presence

Max Pres Act

Enter Min Green

Enter Walk

Enter Ped Clear

Enter Yellow Change

Enter Red Clear

Track Green

Track Yellow Clr Track Red Clear

Dwell Green

Exit Min Green

Exit Yellow Change

Exit Red Clear

Non Locking Memory

Not Ovrd Flash

Not Ovrd Nxt Pre

Require All Red Entry

Track Clear Ovrd

Ped Clear During Yellow Entry Omit OLTG

Track Reserve

Front Panel

Web Interface

Preempt Configuration

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

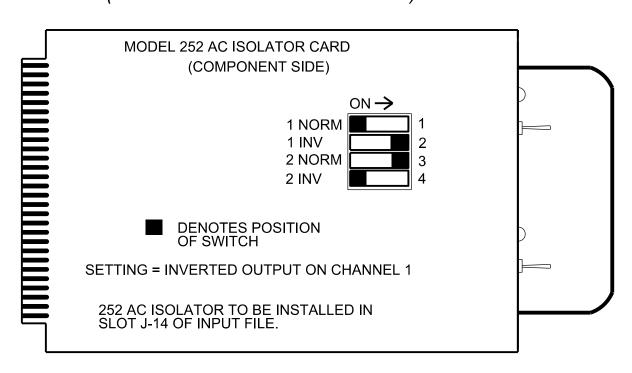
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

PREEMPT 1 AC ISOLATOR (MODEL 252) **OUTPUT PROGRAMMING DETAIL**

(set DIP switches as shown below)



Electrical Detail - Sheet 3 of 3 ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:

M

MOTT

MACDONALD

MOTT MACDONALD 1 & E, LLC 930 Main Campus Drive Suite 200

RALEIGH, NC 27606

NC 211 SR 1239 (Seven Lakes Drive)

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 08-0410

DESIGNED: June 2024

SEALED: 7/11/2024

REVISED:

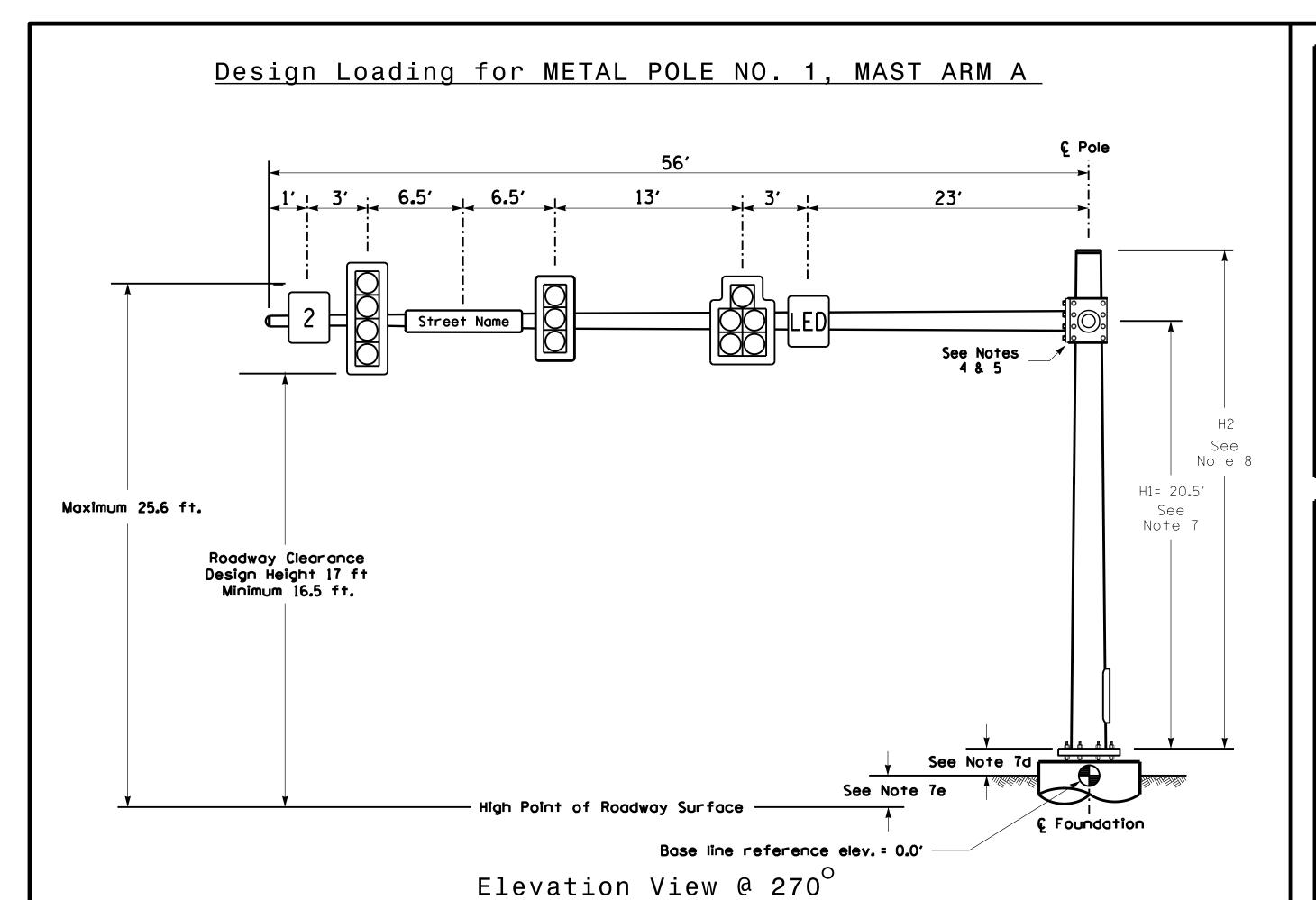
and SR 1190 (Lakeway Drive) Division 8 Moore County Seven Lakes June 2024 REVIEWED BY: R. Mullinax PLAN DATE: PREPARED BY: LD Stouchko REVIEWED BY:

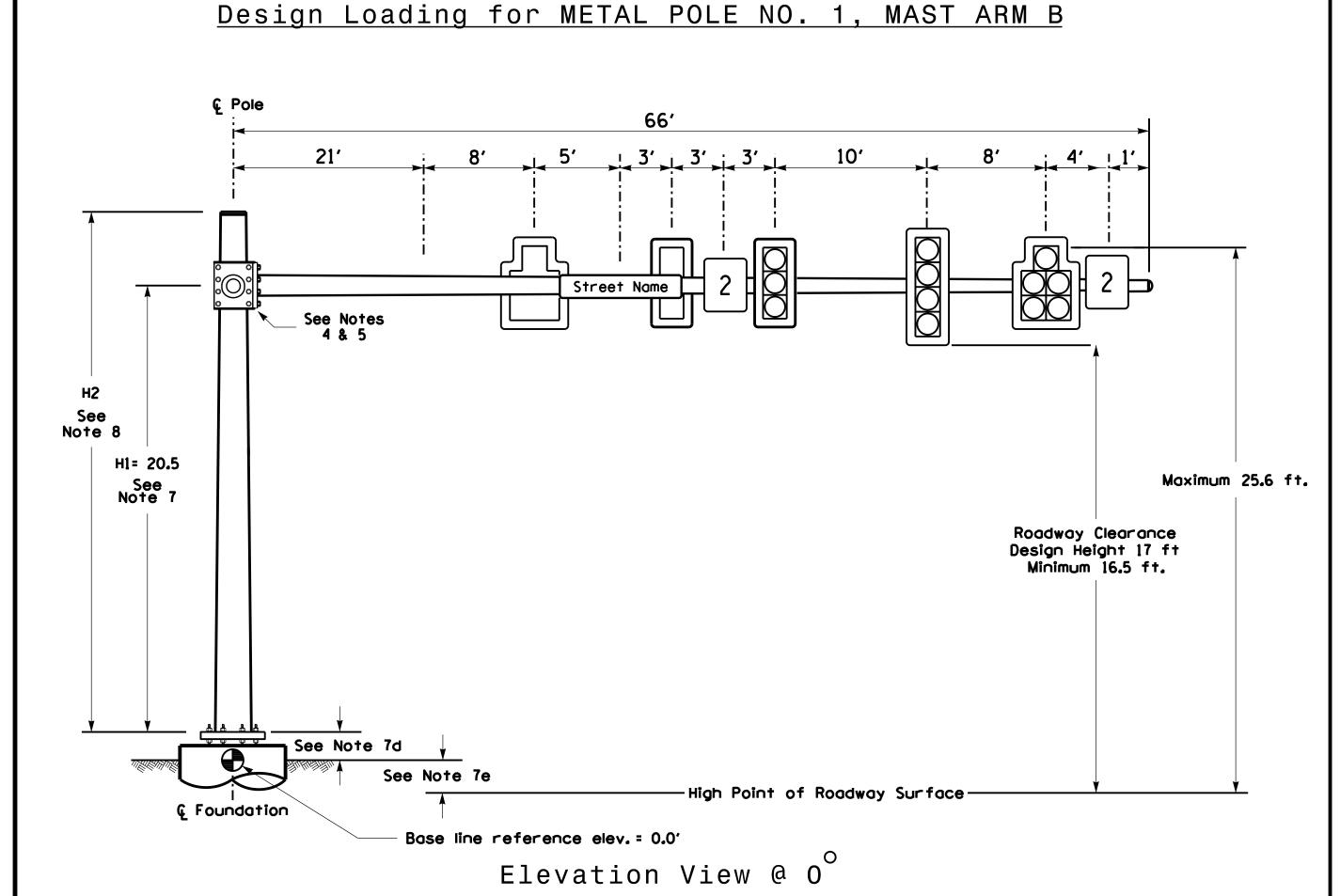
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 034437

SIG. INVENTORY NO.

08-0410

REVISIONS INIT. DATE



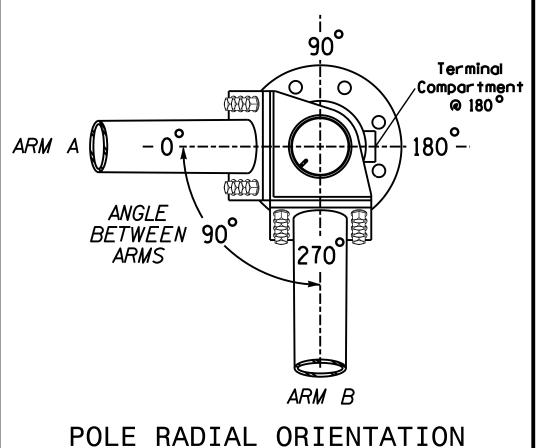


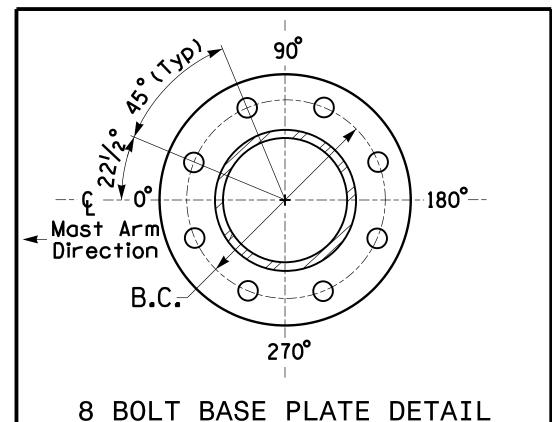
SPECIAL NOTE

The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

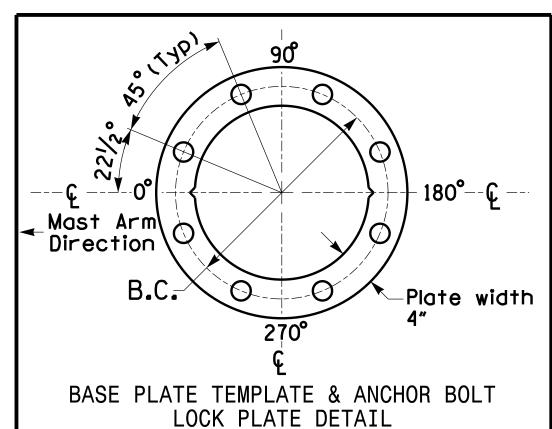
Elevation Data for Mast Arm Attachment (H1)

	(/	
Elevation Differences for:	Pole 1	
Baseline reference point at & Foundation @ ground level	0.0 ft.	
Elevation difference at High point of roadway surface	-1.5 ft.	
Elevation difference at Edge of travelway or face of curb	-0.6 ft.	





See Note 6



For 8 Bolt Base Plate

METAL POLE No. 3

MOTT MACDON	M
MOTT MACDONALD 930 Main Campus E Suite 200 RALEIGH, NC 27606 License No. F–0669) Prive

PROJECT REFERENCE NO.

R-5726

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3 S.F.	42.0" W X 56.0"L	103 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25 . 5" W X 66 . 0" L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5" W X 52.5" L	60 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0" L	14 LBS
LED	L.E.D. BLANKOUT SIGN RIGID MOUNTED	5.0 S.F.	24.0" W X 36.0" L	110 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0" W X 96.0" L	36 LBS

<u>NOTES</u>

DESIGN REFERENCE MATERIAL

1. Design the traffic signal structure and foundation in accordance with: The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions. The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.

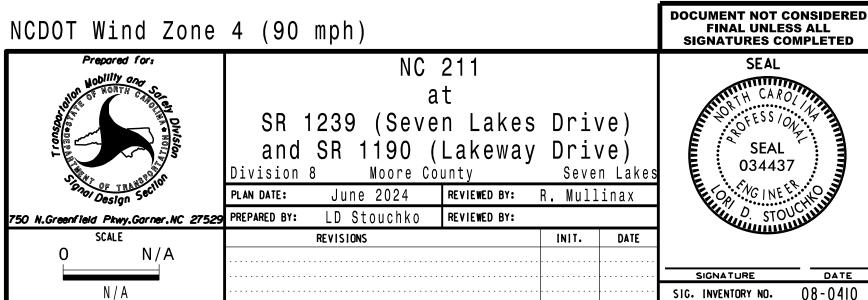
The 2018 NCDOT Roadway Standard Drawings.

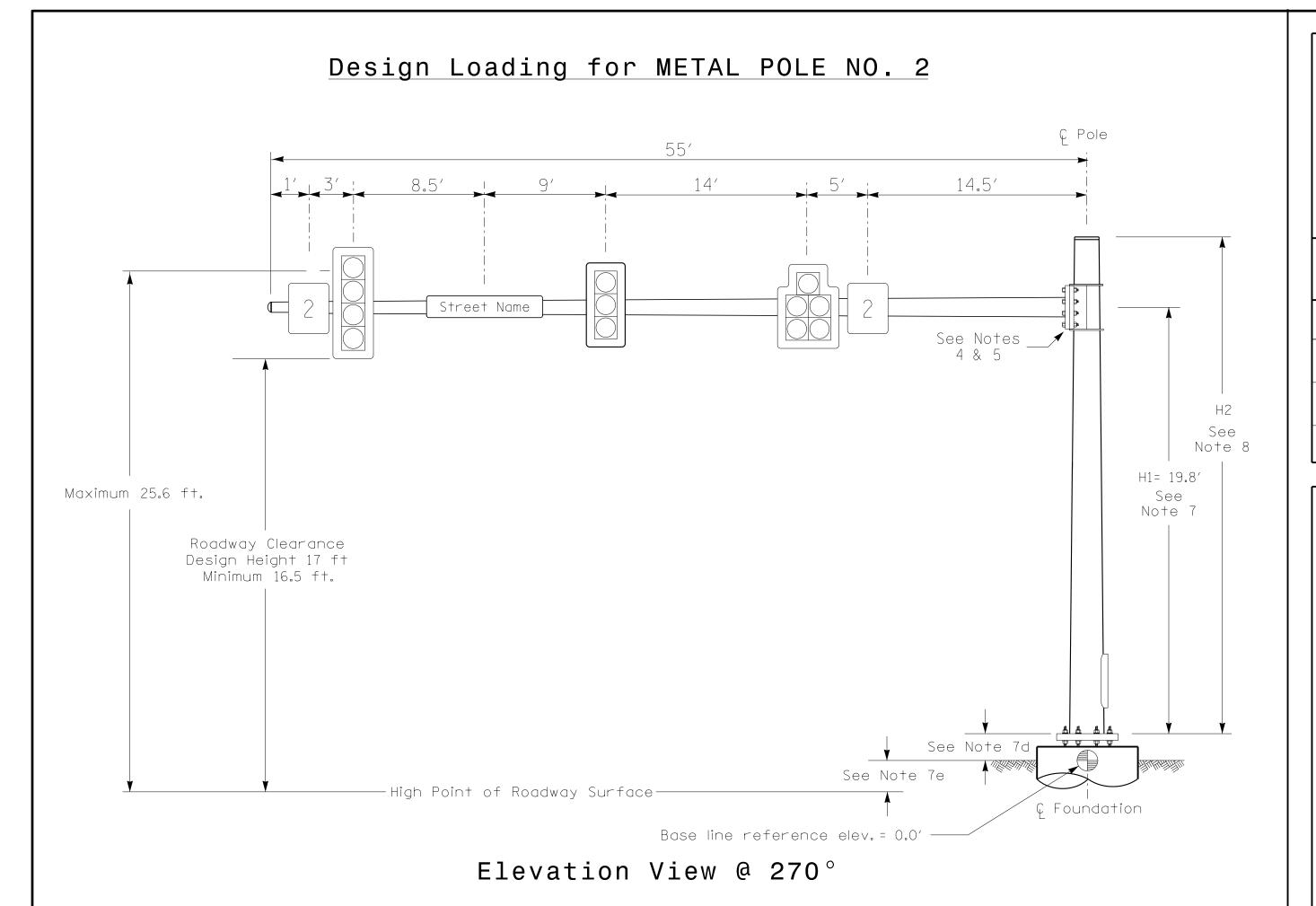
The traffic signal project plans and special provisions. The NCDOT "Metal Pole Standards" located at the following NCDOT website:

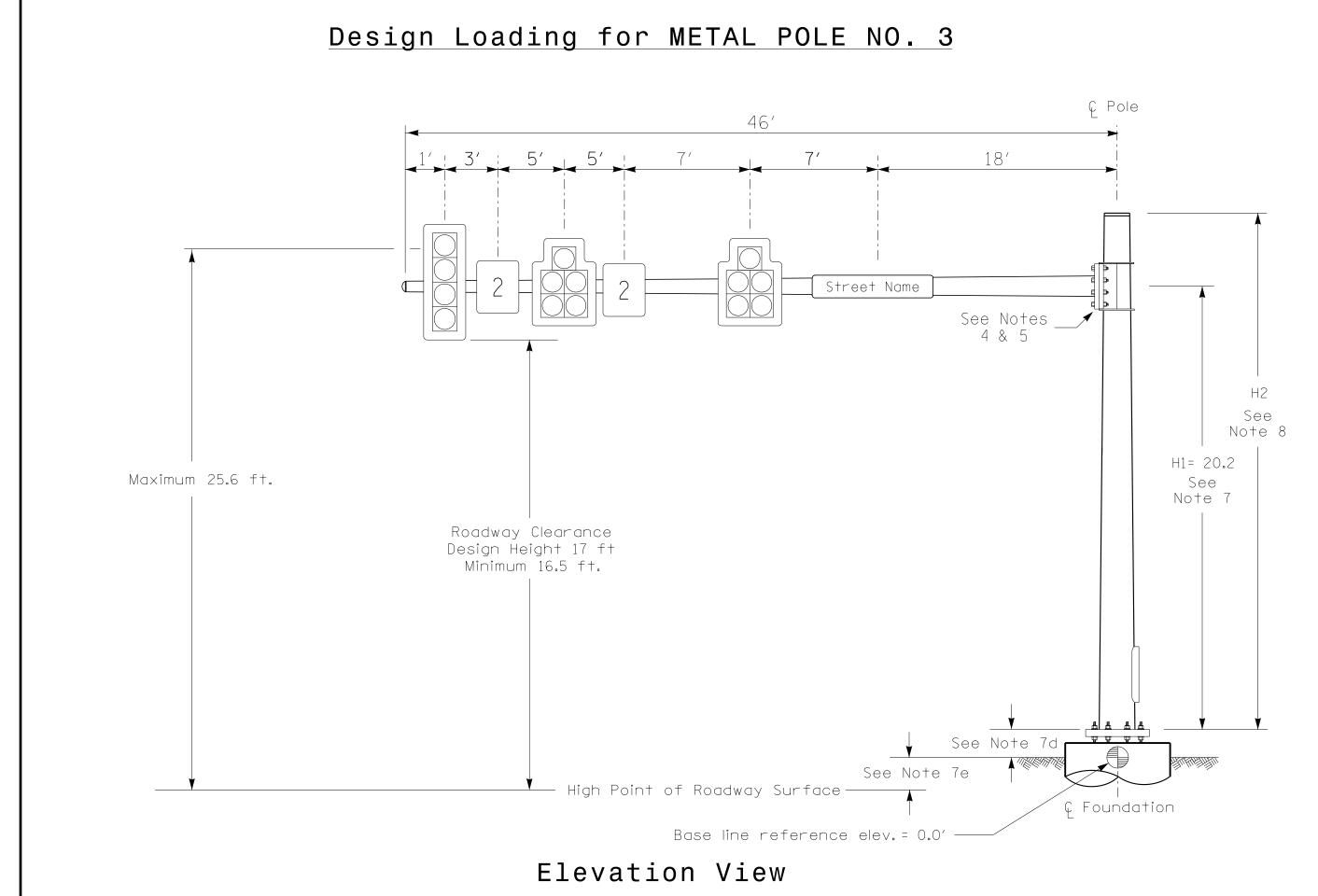
https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

DESIGN REQUIREMENTS

- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signal heads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
 - Mast arm attachment height (H1) plus 2 feet. or
 - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 11. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.





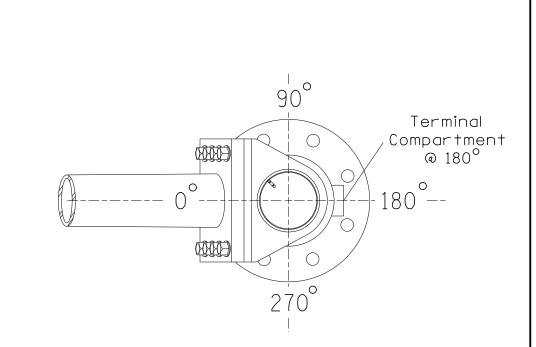


SPECIAL NOTE

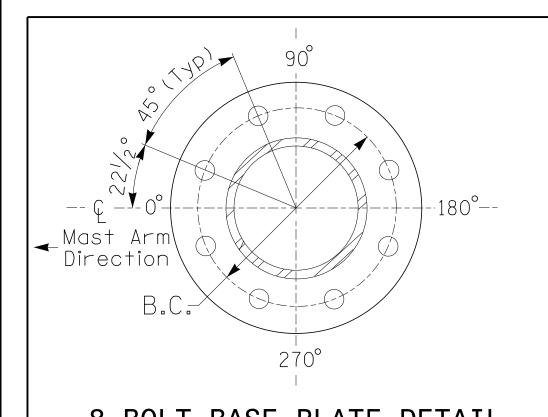
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

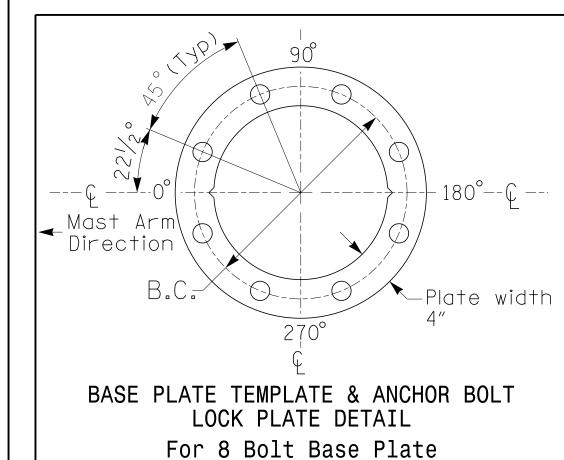
Elevation Differences for:	Pole 2	Pole 3
Baseline reference point at © Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	-0.8 ft.	-1.1 ft.
Elevation difference at Edge of travelway or face of curb	0.5 ft.	-0.6 ft.



POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL See Note 6



METAL POLE No. 2 and 3

MOTT MACDONALD
MOTT MACDONALD 1& E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606 License No. F–0669

R-5726

Sig.19.5

	MAST ARM LOADING SC	HEDU	LE	
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3·S.F.	42.0″W X 56.0″L	103·LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
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2	SIGN RIGID MOUNTED	7.5 ·S.F.	30.0"W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 · S.F.	24.0″W X 96.0″L	36 LBS

<u>NOTES</u>

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- 1. Design the traffic signal structure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
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- the specifications can be found in the traffic signalproject specialprovisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website:
- https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

DESIGN REQUIREMENTS

loads that will be applied at the time of the installation. The contractor should refer to the traffic signalplans for the actualloads that will be applied at the time of the installation. 3. Design all signal supports using stress ratios that do not exceed 0.9. 4. The camber design for the mast arm deflection should provide an appearance of a low

views. These are anticipated worst case "design loads" and may not represent the actual

2. Design the traffic signalstructure using the loading conditions shown in the elevation

- pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground leveland the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the totalheight of the mast arm attachment assembly plus 1 foot. 9. If pole location adjustments are required, the contractor must gain approval from the
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- 10.The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signalheads over the roadway.
- 11. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.





N/A

NC 211 SR 1239 (Seven Lakes Drive) and SR 1190 (Lakeway Drive)

Division 8 Moore County Seven Lakes PLAN DATE: June 2024 REVIEWED BY: R. Mullinax PREPARED BY: LD Stouchko REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 08-04|0

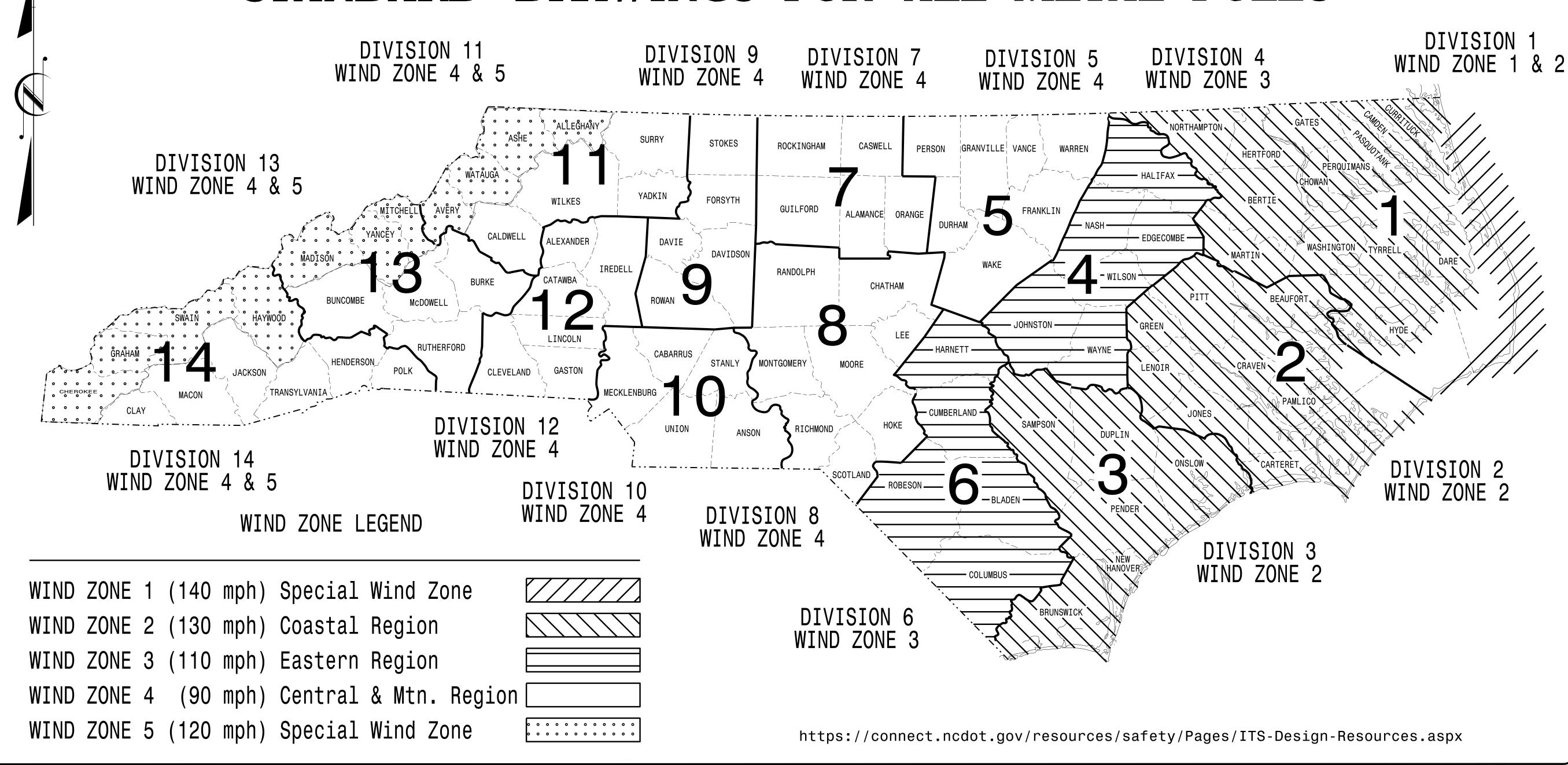
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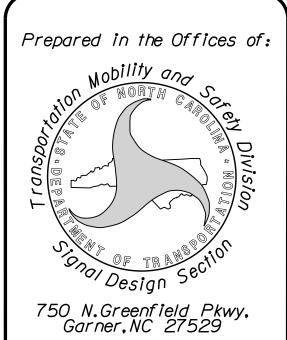
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. SHEET NO Sig.M1

STANDARD DRAWINGS FOR ALL METAL POLES





Designed in conformance with the latest 2015 Interim to the 6th Edition 2013

AASHTO

Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals

DRAWING

NUMBER

Sig. M 1 Sig. M 2

Sig. M 3 Sig. M 4 Typical Fabrication Details-Mast Arm Poles

Sig. M 6 Sig. M 7 Construction Details-Foundations

INDEX OF PLANS

DESCRIPTION

Statewide Wind Zone Map

Typical Fabrication Details-All Metal Poles Typical Fabrication Details-Strain Poles

Typical Fabrication Details-Mast Arm Connection Sig. M 5 Typical Fabrication Details-Strain Pole Attachments

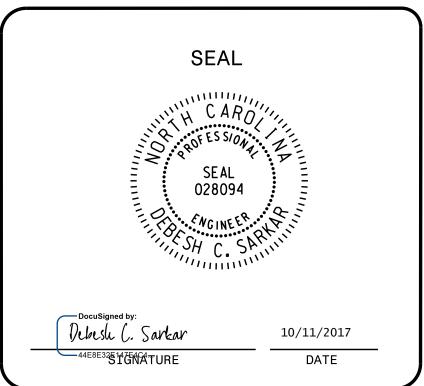
Sig. M 8 Standard Strain Pole Foundation-All Soil Conditions

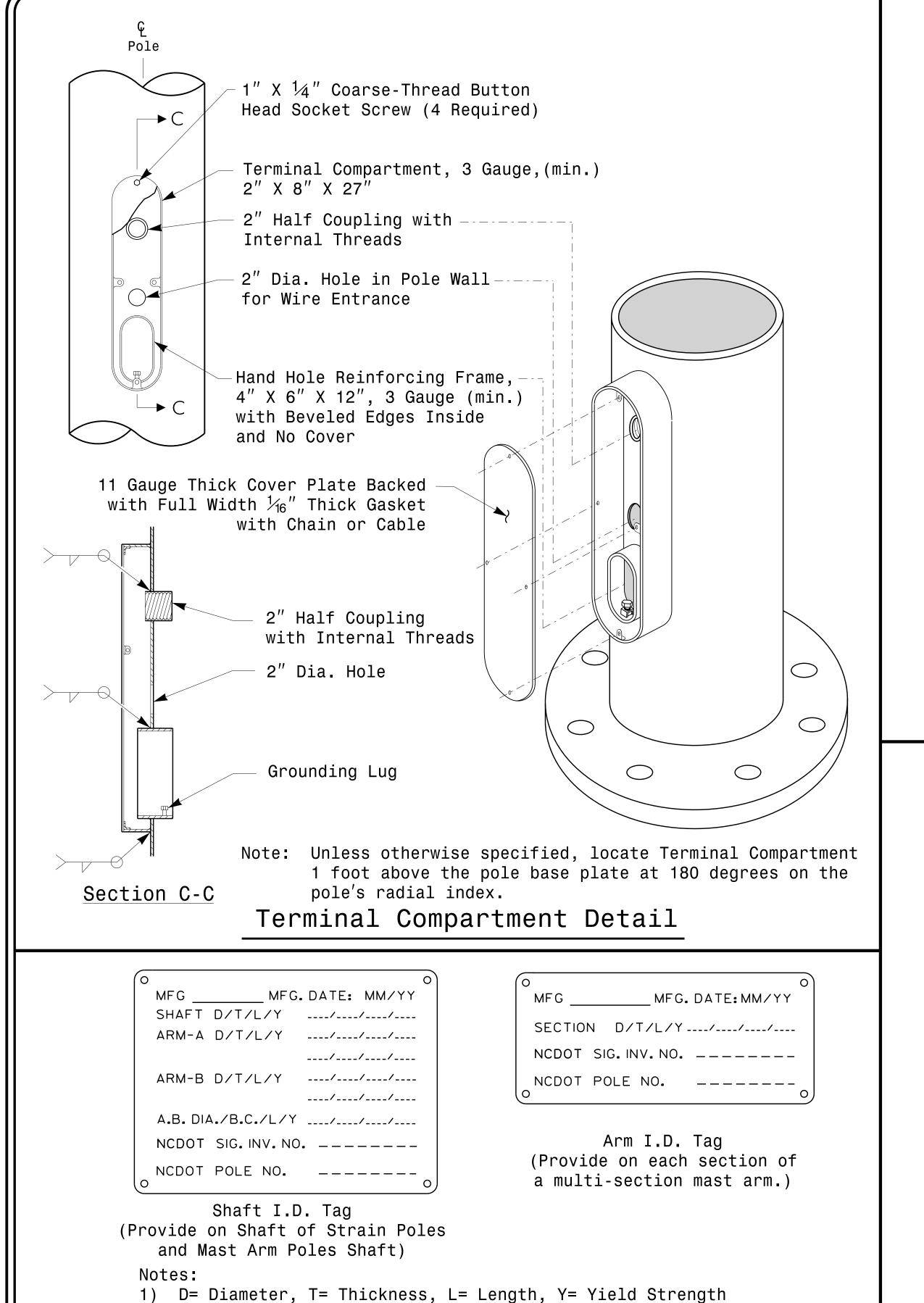
NCDOT CONTACTS:

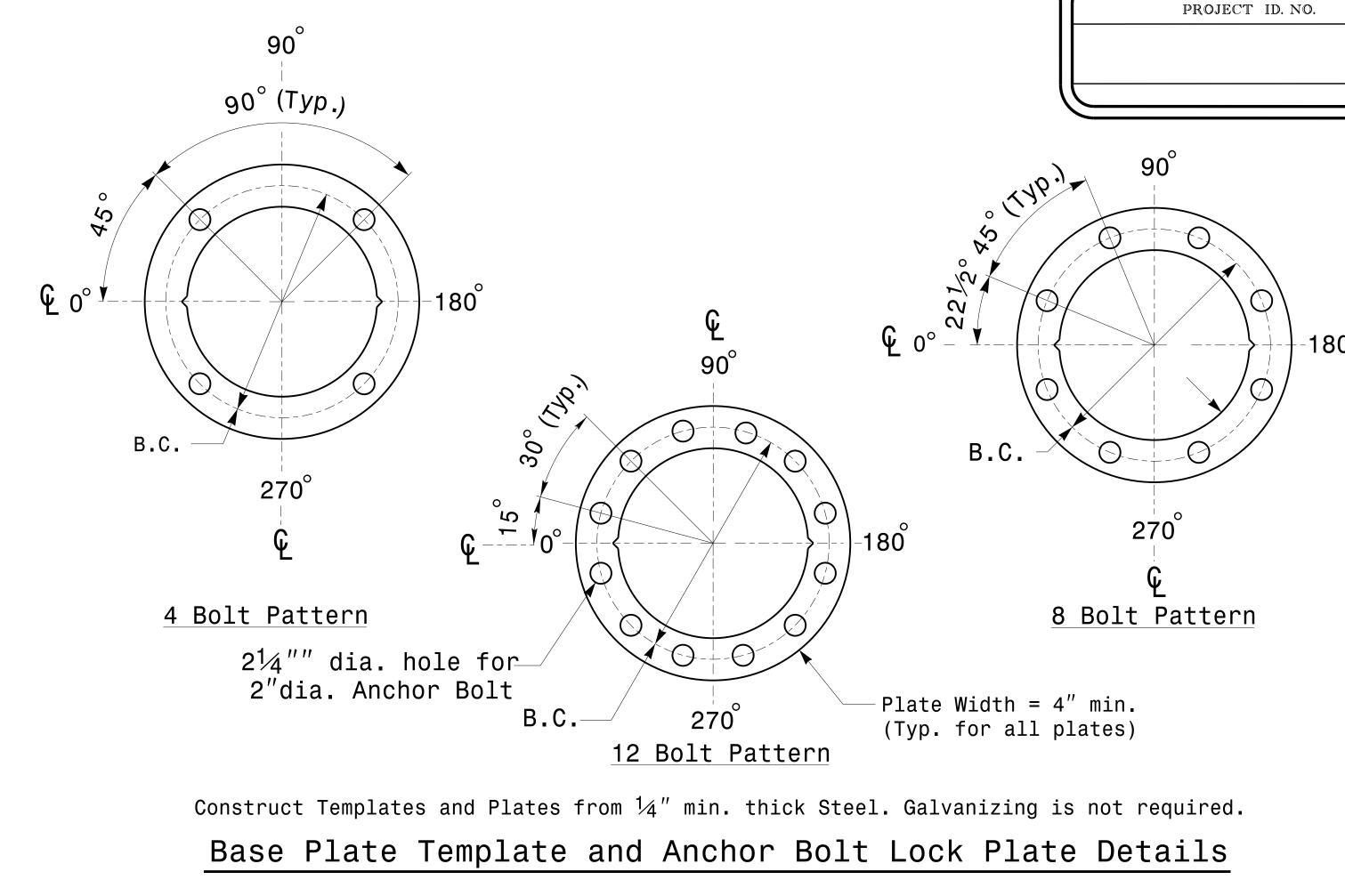
MOBILITY AND SAFETY DIVISION – ITS AND SIGNALS UNIT

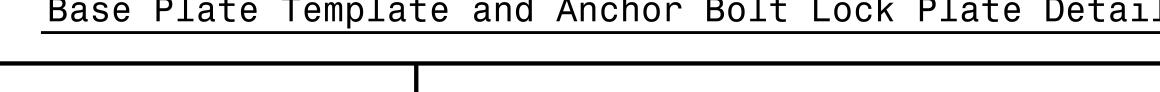
M.M. MCDIARMID, P.E. - STATE ITS AND SIGNALS ENGINEER J. P. GALLOWAY, P.E. - STATE SIGNALS ENGINEER

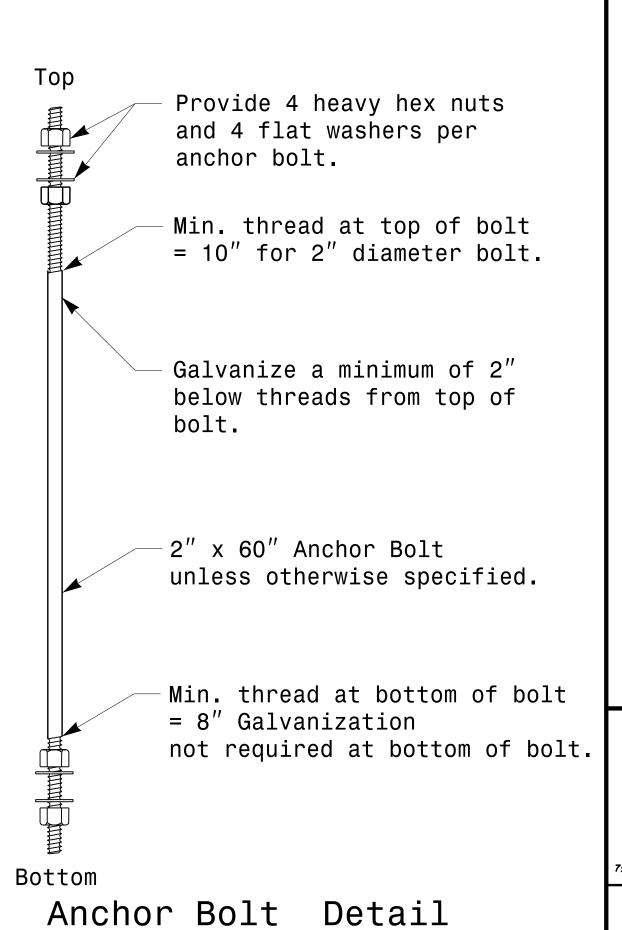
D.C. SARKAR, P.E. – ITS AND SIGNALS SENIOR STRUCTURAL ENGINEER

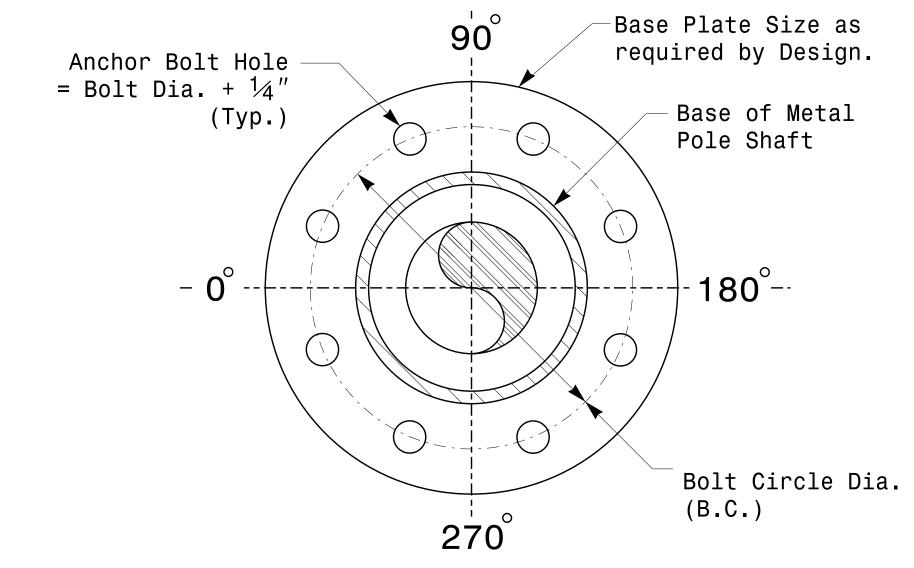












SHEET NO

Sig.M2

•

eta

10/11/2017

Note: Base plate may be circular, octagonal, square or rectangular in shape.

Typical Base Plate Detail

Prepared in the Offices of: Mobility and South Division D	Typical Fabric Fo All Meta	SEAL C ARO SEAL SEAL 028094	
Design Seu	PLAN DATE: OCTOBER 2017	DESIGNED BY: C.F.ANDREWS	- CO. NO INEER
Greenfield Pkwy, Garner, NC 27529	PREPARED BY: N. BITTING	REVIEWED BY: D.C. SARKAR	SH C. SA
SCALE	REVISIONS	INIT. DATE	DocuSigned by:
O NA			Debesh C. Sarkar
NONE			44E8E32 S1GNMACT.URE

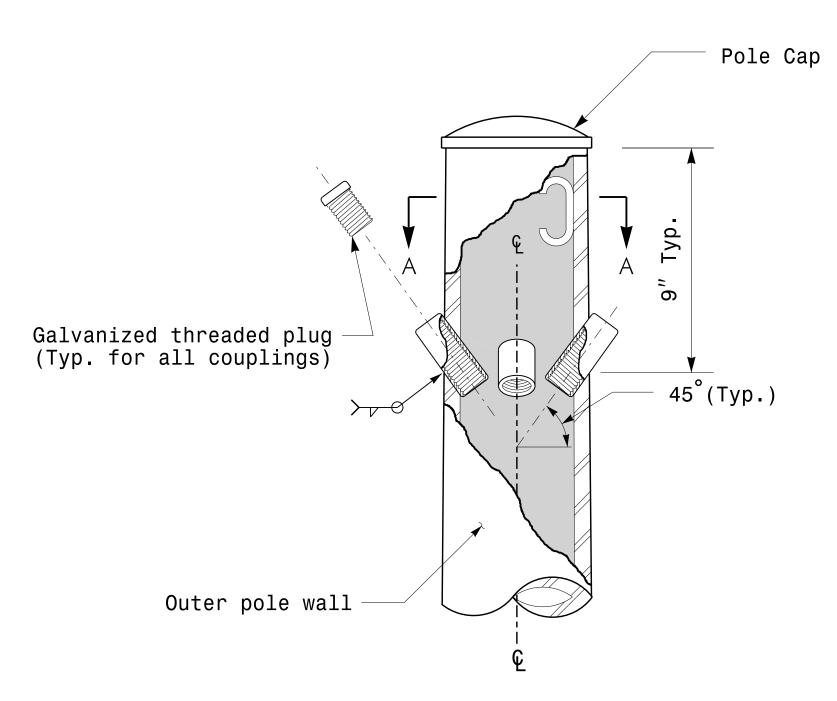
2) A.B. = Anchor Bolt B.C. = Bolt Circle of Anchor Bolts 4) If Custom Design, use "NCDOT STANDARD" line for Signal Inv. Number and pole I.D. number 5) See drawing M3 and M4 for mounting positions of I.D. tags. Identification Tag Details

Strail eta

atio

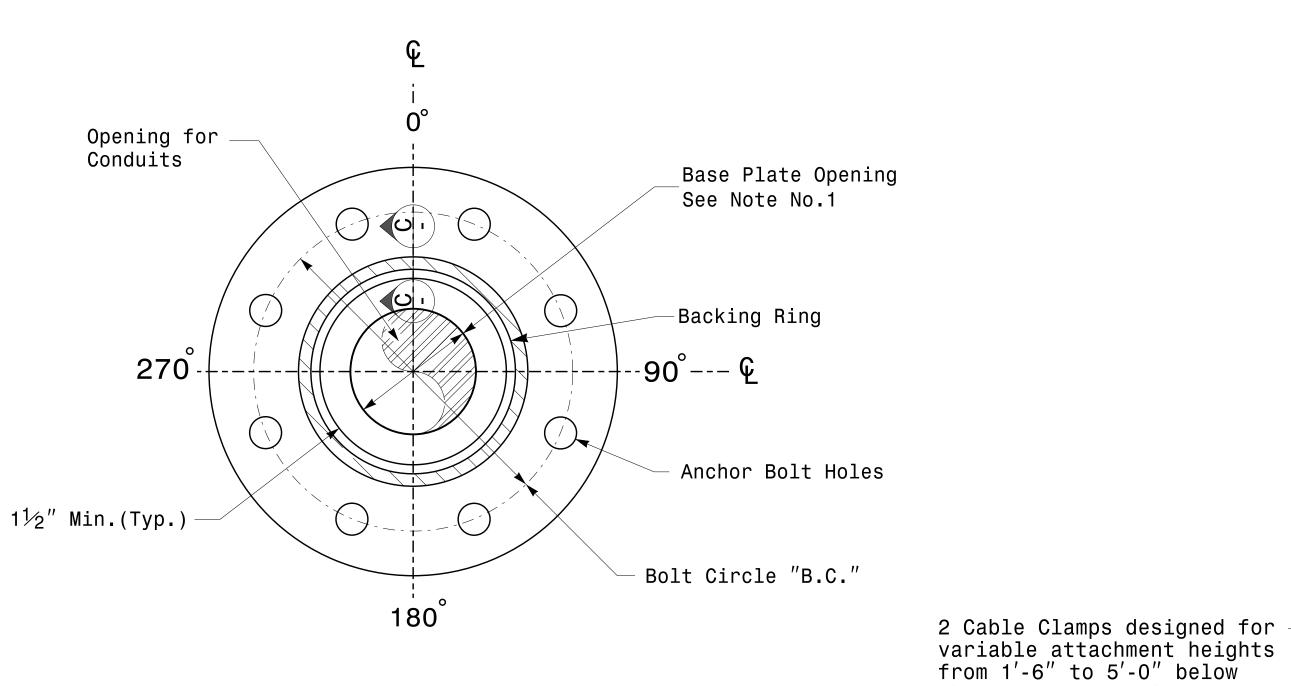
Fabric

Note: 1.Opening in pole base plate shall be equal to pole base inside diameter minus $3\frac{1}{2}$ " but shall not be less than $8\frac{1}{2}$ ".

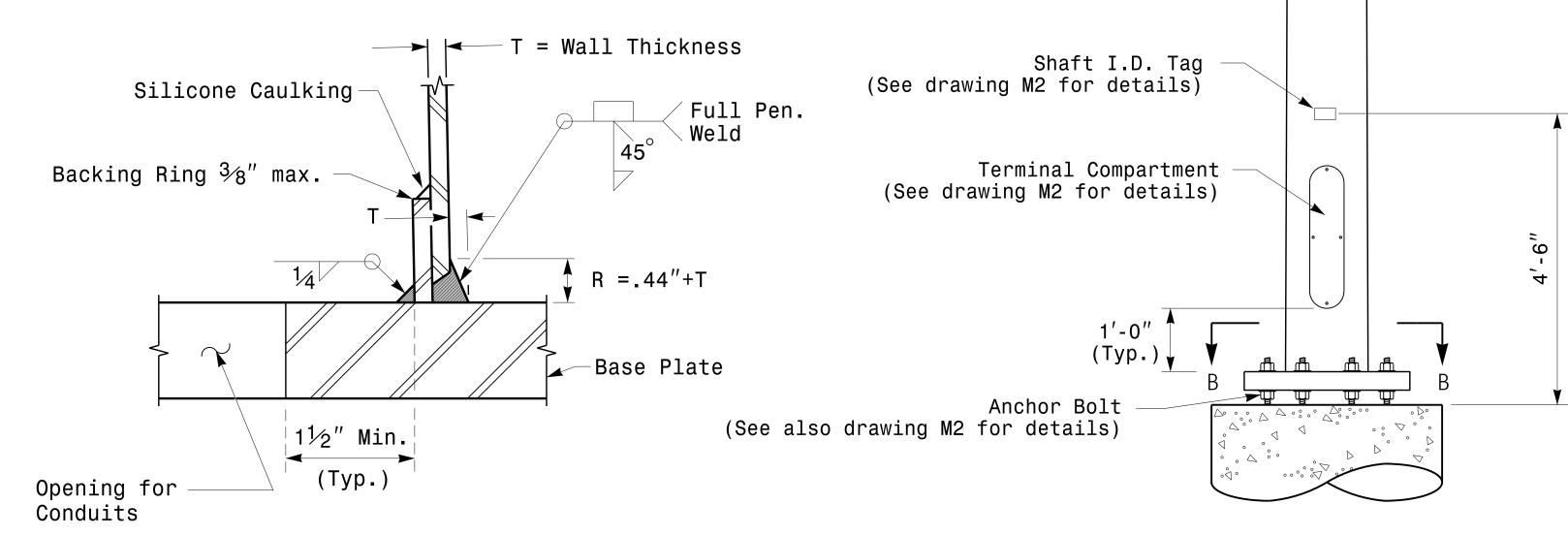


<u>Cable Entrances at Top of Pole</u>

2" Half Coupling with Internal Threads



Section B-B <u>Pole Base Plate Details</u> (8 and 12 Bolt Pattern)



the top of the pole.

Section A-A

1" Half Coupling with Internal Threads

"C" Hook @ 45° (Typ.)

Radial Orientation for Factory Installed Accessories at Top of Pole

Section C-C (Pole Attachment to Base Plate)

<u>Full-Penetration</u> Groove Weld Detail



Typical	Fabrication	Details
	For	
	Strain Poles	

<u>Monotube Strain Pole</u>

OCTOBER 2017 DESIGNED BY: K.C.DURIGON PLAN DATE: PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR REVISIONS NONE

