

Project: R-5726

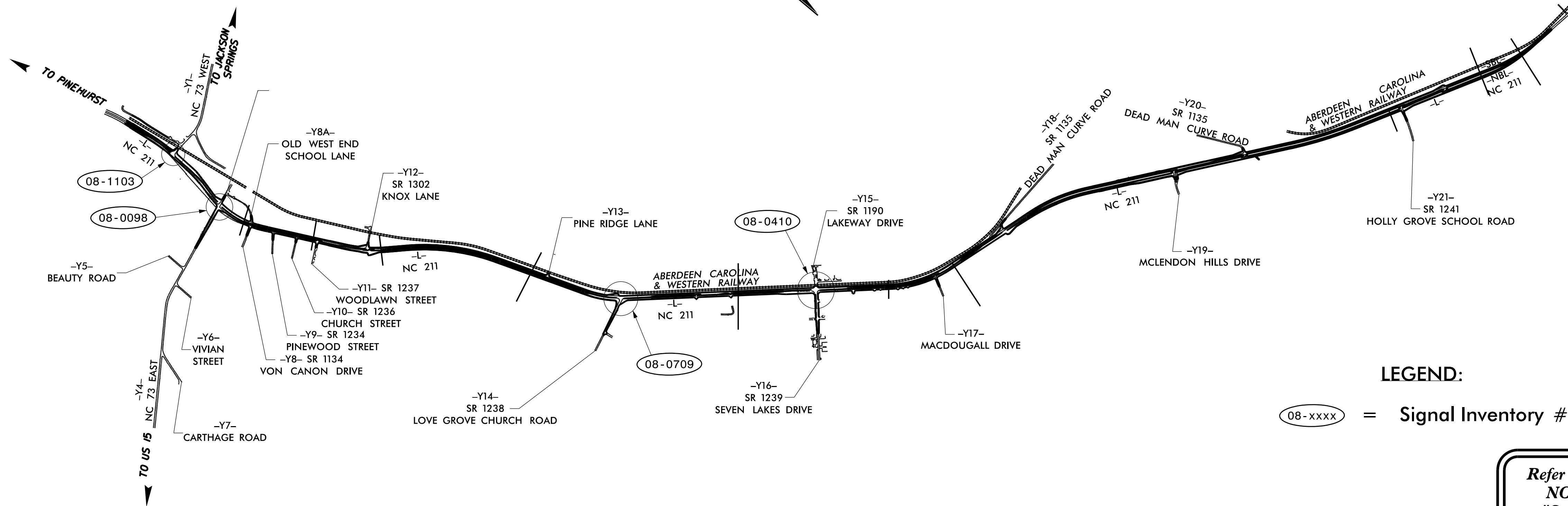
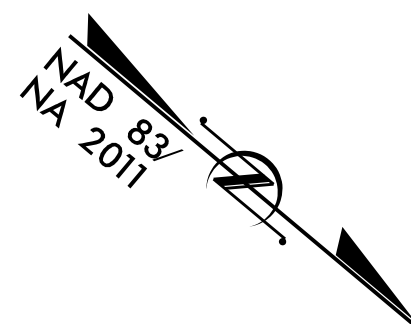
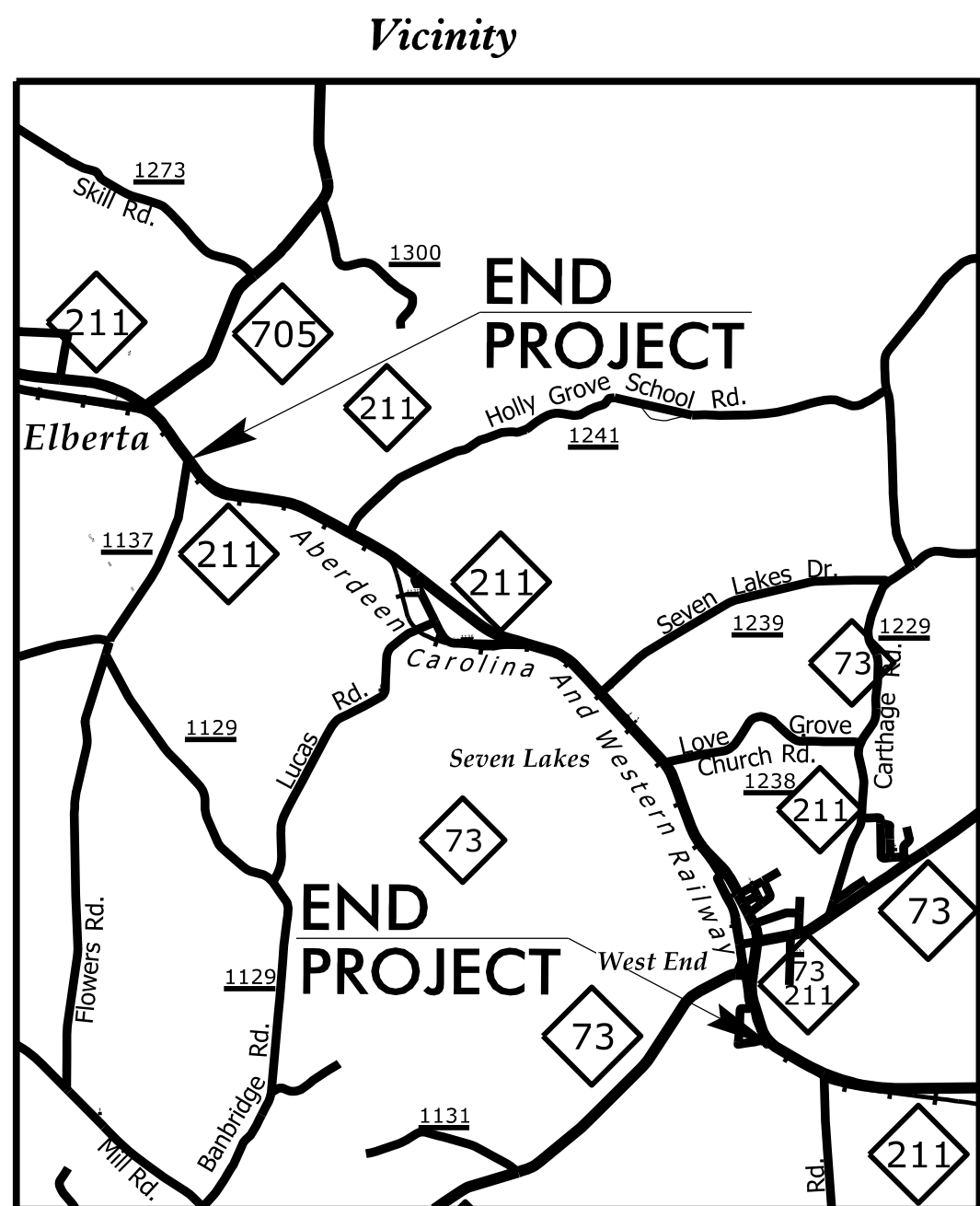
STATE OF NORTH CAROLINA  
DIVISION OF HIGHWAYS

Project No. <b>R-5726</b>	Sheet No. <b>Sig. 1.0</b>
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# MOORE COUNTY

**LOCATION: NC 211 FROM SOUTH OF NC 73 IN WEST END TO NORTH OF SR 1241 (HOLLY GROVE SCHOOL ROAD).**

**TYPE OF WORK: TRAFFIC SIGNALS**



**LEGEND:**

(08-xxxx) = Signal Inventory #

Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.

Sheet #	Reference #	Index of Plans	Location/Description
Sig. 1.0	-----	Title Sheet	
Sig. 1.1	-----	Standard Plate Sheet	
Sig. 2.0-5.5	08-1103	NC 211 at NC 73 (South Intersection)	
Sig. 6.0-11.4	08-0098	NC 211 at NC 73/SR 1133 (Mode Rd)	
Sig. 12.0-15.4	08-0709	NC 211 at SR 1238 (Love Grove Church Rd)	
Sig. 16.0-19.5	08-0410	NC 211 at SR 1239 (Seven Lakes Drive and SR 1190 (Lakeway Drive)	
M1A-M9		Standard Metal Pole Details	

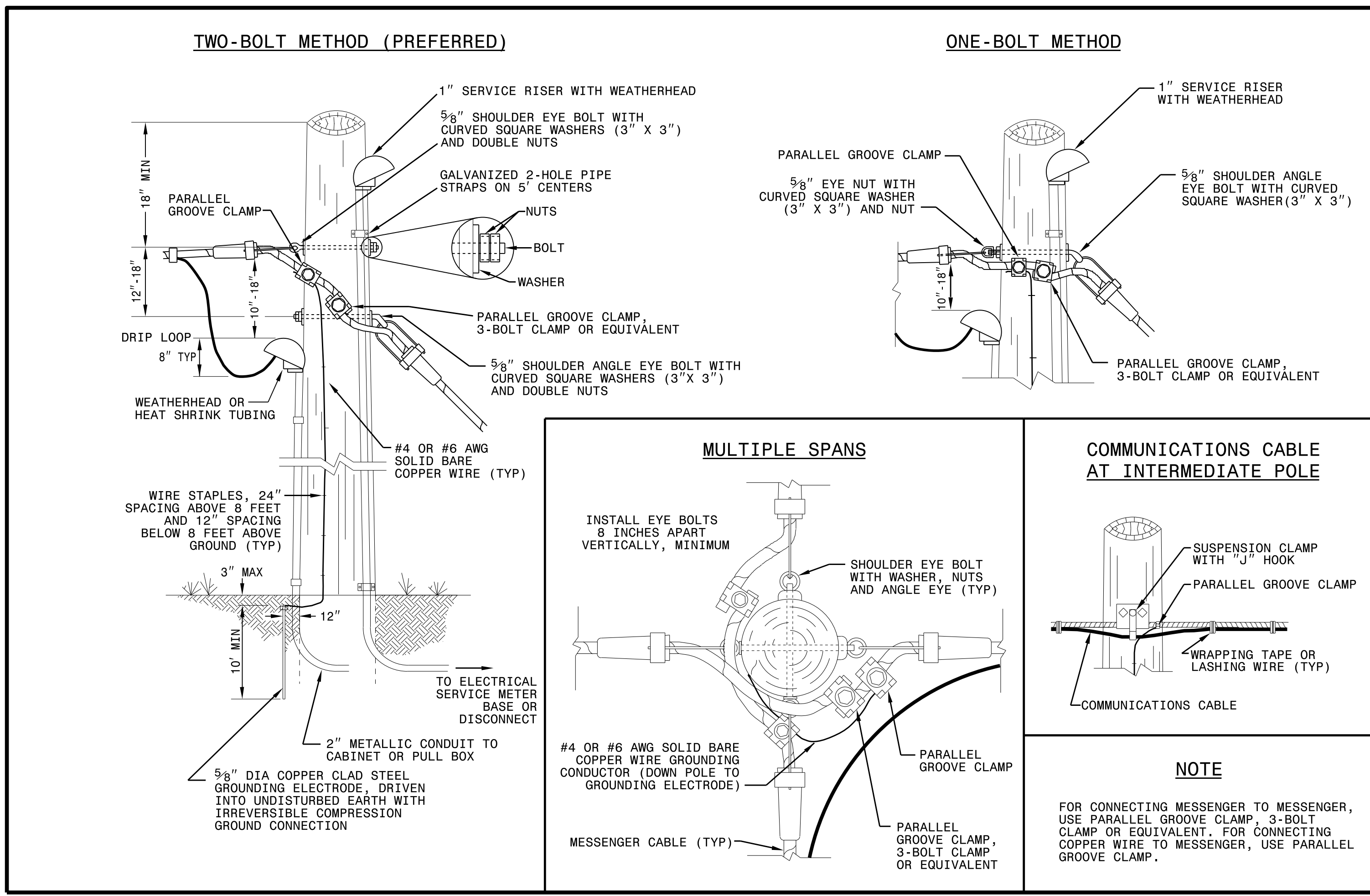
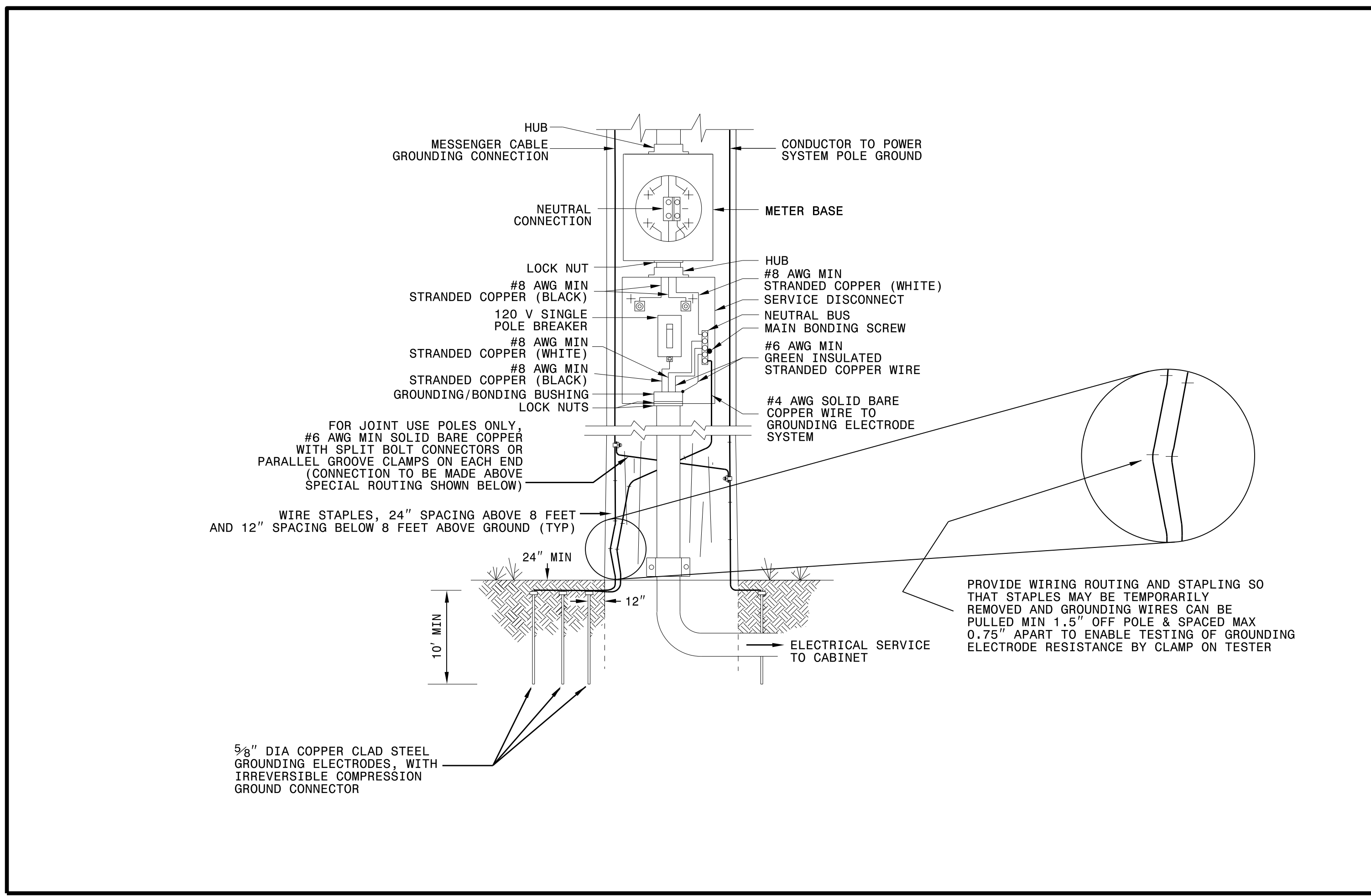
**TRANSPORTATION SYSTEMS  
MANAGEMENT & OPERATIONS**

Contacts:

**Robert J. Ziemba, PE - Central Region Signals Engineer**  
**Ryan W. Hough, PE - Signal Equipment Design Engineer**

Prepared for the office of:  
DIVISION OF HIGHWAYS  
TRANSPORTATION MOBILITY & SAFETY DIVISION

750 N. Greenfield Parkway, Garner, NC 27529



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See Plate for Title

Prepared in the Offices of:

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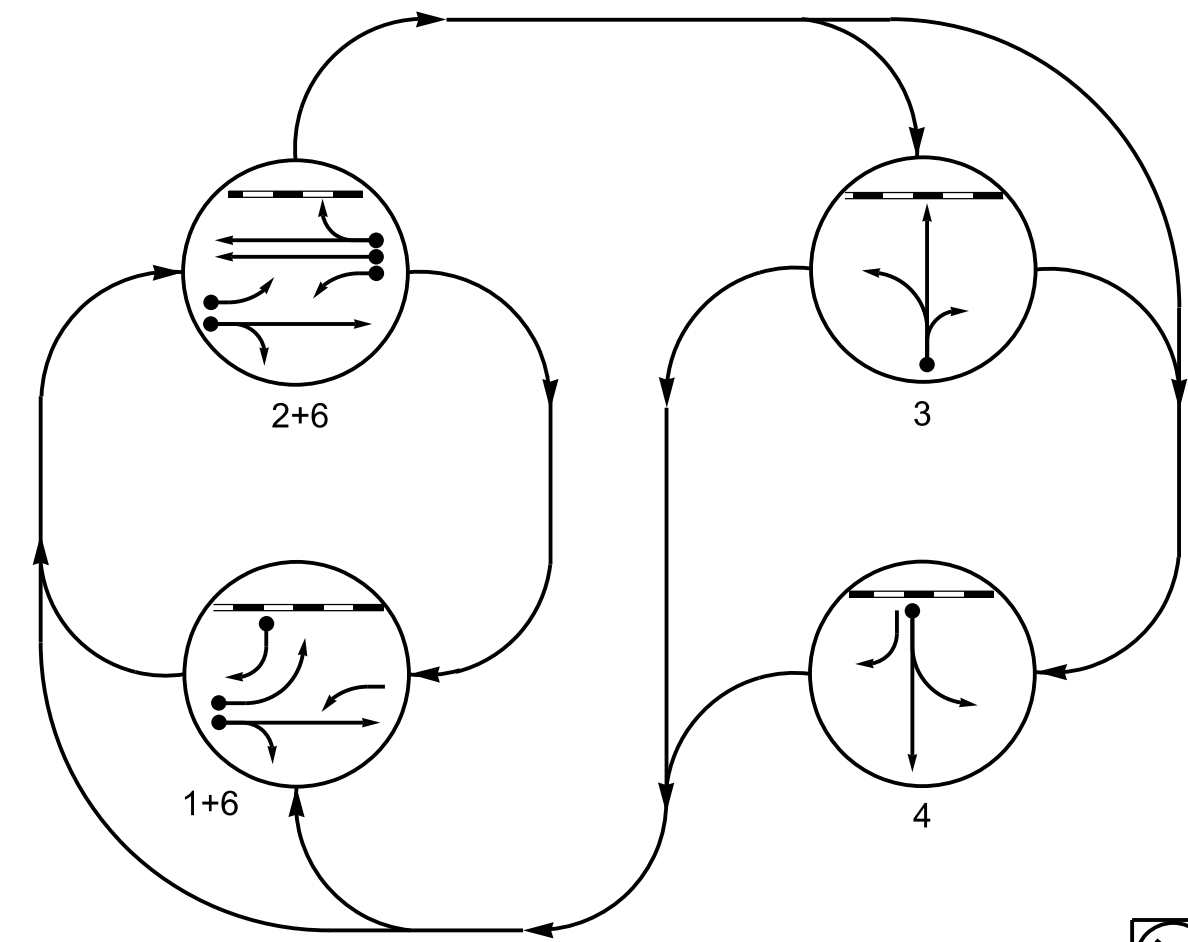
DocuSigned by:  
*Mohd Aslami*

750 N. Greenfield Parkway  
Garner, NC 27529

10/11/2017  
DATE

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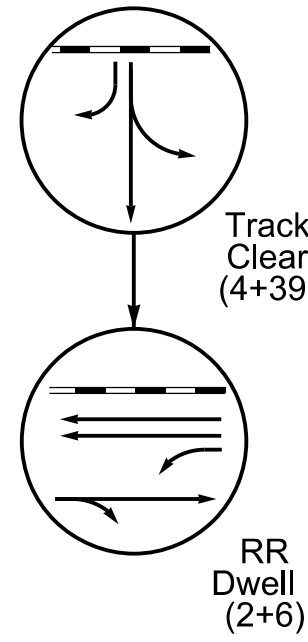
PHASING DIAGRAM



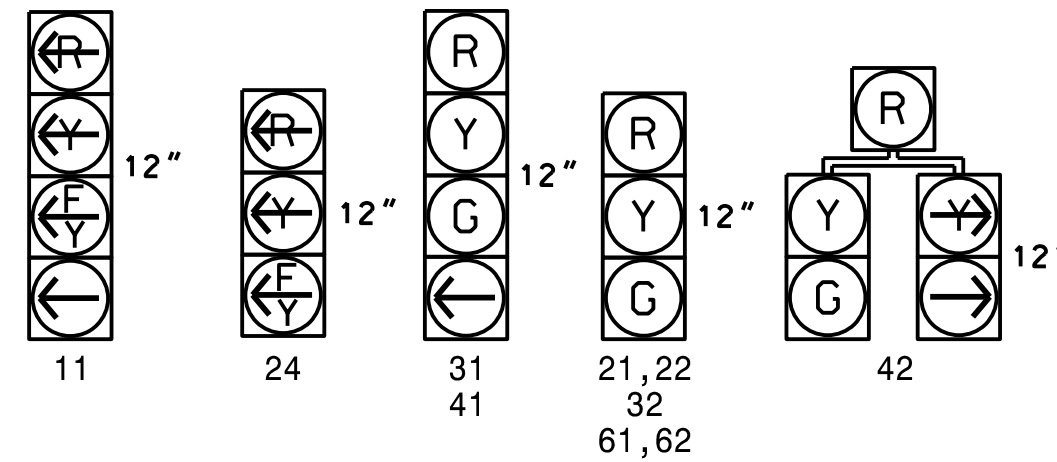
PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

RAIL PREEMPT PHASES  
(High Priority)



SIGNAL FACE I.D.  
All Heads L.E.D.



SIGNAL FACE	PHASE											
	1+6	2+6	3	4	TR RL AE CA KR	R R D W E L	F L A S H					
11	—	—	—	—	—	—	—	—	—	—	—	
21,22	R	G	R	R	R	R	R	R	R	R	R	
24	F	F	—	—	—	—	—	—	—	—	—	
31	R	R	G	R	R	R	R	R	R	R	R	
32	R	R	G	R	R	R	R	R	R	R	R	
41	R	R	R	G	R	R	R	R	R	R	R	
42	R	R	R	G	R	R	R	R	R	R	R	
61,62	G	G	R	R	R	R	R	R	R	R	R	
Sign A	OFF	OFF	OFF	OFF	OFF	ON	ON	ON	ON	ON	ON	*

\* See Note 7

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	INITIAL	CALL	DELAY DURING GREEN	NEW CARD
1A*	6X40	0	*	*	1	15.0	-	X	-	X	-	*
					6	3.0	-	X	-	X	X	*
1B*	6X40	0	*	*	1	15.0	-	X	-	X	-	*
2A*	6X6	200	*	*	2	-	-	X	X	X	-	*
2B*	6X6	200	*	*	2	-	-	X	X	X	-	*
2C*	6X40	0	*	*	2	3.0	-	X	-	X	X	*
3A*	15X6	0	*	*	3	10.0	-	X	-	X	-	*
4A*	6X40	0	*	*	4	3.0	-	X	-	X	-	*
6A*	6X6	300	*	*	6	-	-	X	X	X	-	*

\* Video Detection Zone

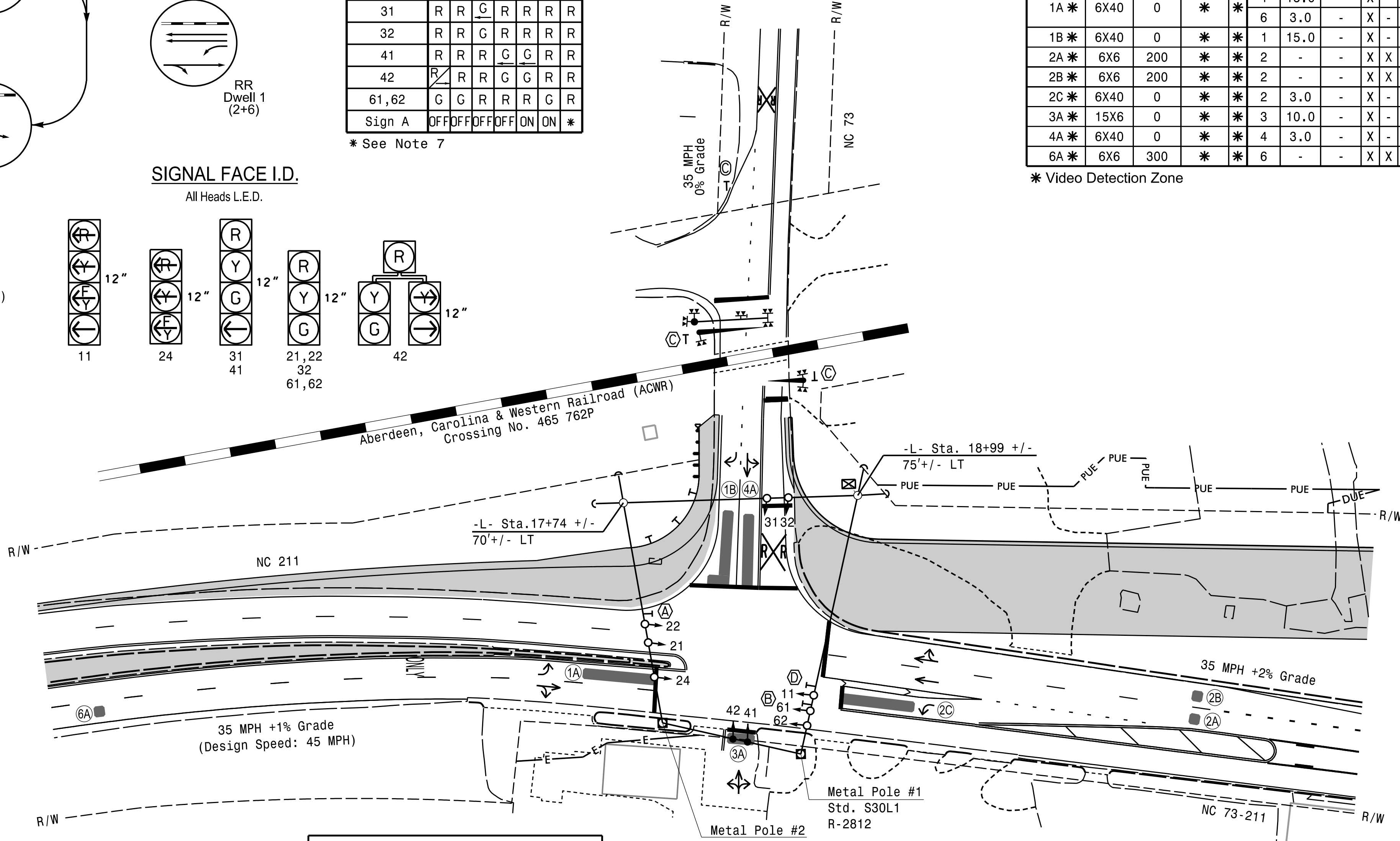
4 Phase Fully Actuated With Railroad Preemption (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- This location contains railroad preemption phasing. Do not program signal for late night phasing operation. Phase 1 may be lagged.
- The order of phase 3 and phase 4 may be reversed.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- Ensure flashing operation does not alter operation of blankout signs.
- Program phase 40 to run concurrently with all phases during normal operation. Phase 39 must be incompatible with Phase 40 and included as a track clear phase.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

LEGEND

	PROPOSED Traffic Signal Head		EXISTING Traffic Signal Head
	PROPOSED Modified Signal Head		EXISTING Modified Signal Head
	PROPOSED Pedestrian Signal Head With Push Button & Sign		EXISTING Pedestrian Signal Head With Push Button & Sign
	PROPOSED Signal Pole with Guy		EXISTING Signal Pole with Guy
	PROPOSED Signal Pole with Sidewalk Guy		EXISTING Signal Pole with Sidewalk Guy
	PROPOSED Inductive Loop Detector		EXISTING Inductive Loop Detector
	PROPOSED Controller & Cabinet		EXISTING Controller & Cabinet
	PROPOSED Junction Box		EXISTING Junction Box
	PROPOSED 2-in Underground Conduit		EXISTING 2-in Underground Conduit
	PROPOSED Right of Way		EXISTING Right of Way
	PROPOSED Permanent Utility Easement		EXISTING Permanent Utility Easement
	PROPOSED Temporary Construction Easement		EXISTING Temporary Construction Easement
	PROPOSED Permanent Drainage Easement		EXISTING Permanent Drainage Easement
	PROPOSED Temporary Drainage Easement		EXISTING Temporary Drainage Easement
	PROPOSED Permanent Drainage/Utility Easement		EXISTING Permanent Drainage/Utility Easement
	PROPOSED Directional Arrow		EXISTING Directional Arrow
	PROPOSED Railroad Cantilever		EXISTING Railroad Cantilever
	PROPOSED Railroad Gate and Flasher		EXISTING Railroad Gate and Flasher
	PROPOSED Railroad Tracks		EXISTING Railroad Tracks
	PROPOSED Guardrail		EXISTING Guardrail
	PROPOSED Construction Zone		EXISTING Construction Zone
	PROPOSED Non-Intrusive Detection Zone		EXISTING Non-Intrusive Detection Zone
	PROPOSED Metal Strain Pole		EXISTING Metal Strain Pole
	PROPOSED "NO RIGHT TURN - TRAIN" LED Blankout Sign		EXISTING "NO RIGHT TURN - TRAIN" LED Blankout Sign
	PROPOSED Left Arrow "ONLY" Sign (R3-5L)		EXISTING Left Arrow "ONLY" Sign (R3-5L)
	PROPOSED "DO NOT STOP ON TRACKS" Sign (R8-8)		EXISTING "DO NOT STOP ON TRACKS" Sign (R8-8)
	PROPOSED "U-TURN YIELD TO RIGHT TURN" Sign (R10-16)		EXISTING "U-TURN YIELD TO RIGHT TURN" Sign (R10-16)



FEATURE	MAXTIME TIMING CHART							
	PHASE						39	40
	1	2	3	4	6			
Walk *	-	-	-	-	-	-	-	
Ped Clear *	-	-	-	-	-	-	-	
Min Green *	7	10	7	7	12			
Passage *	2.0	5.0	2.0	2.0	6.0			
Max I *	20	60	15	30	60			
Yellow Change	3.0	4.4	3.8	3.8	4.4	3.8	4.4	
Red Clear	2.4	1.7	1.3	1.6	1.7	1.6	2.4	
Added Initial *	-	1.5	-	-	2.5			
Maximum Initial *	-	24	-	-	34			
Time Before Reduction *	-	15	-	-	15			
Time To Reduce *	-	34	-	-	34			
Minimum Gap	-	3.0	-	-	3.0			
Advance Walk	-	-	-	-	-			
Non Lock Detector	X	-	X	X	-			
Vehicle Recall	-	MIN RECALL	-	-	MIN RECALL		MIN RECALL	
Dual Entry	-	-	-	-	-			

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

MAXTIME PREEMPTION CHART

FUNCTION	PRE 1	
	TYPE	RAIL ROAD
Exit Phases		4
Delay		0
Max Presence		0
Enter Min Green		1
Enter Walk		0
Enter Ped Clear		0
Enter Yellow Change		4.4*
Enter Red Clear		2.4*
Track Green		27
Track Yellow Change		3.8
Track Red Clear		1.6
Dwell Green		0
Exit Min Green		255*
Exit Yellow Change		25.5*
Exit Red Clear		25.5*
Call Extend Time		1.0
Exit Type		EXIT PHASES
Ped Clear Through Yellow		-
Require All Red Entry		-

\* Directs controller to use default phase timing.

This signal was designed for advanced preemption

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Signal Upgrade Temporary Design 1 (TMP Phase I)

NC 211/NC 73-211 at NC 73 (South Intersection)		
Division 8	Moore County West End	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax	
PREPARED BY: LD Stouchko	REVIEWED BY:	
REVISIONS		
	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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NORTH CAROLINA PROFESSIONAL ENGINEERING SOCIETY  
SEAL 034437  
LD. STOUCHKO  
P.E.

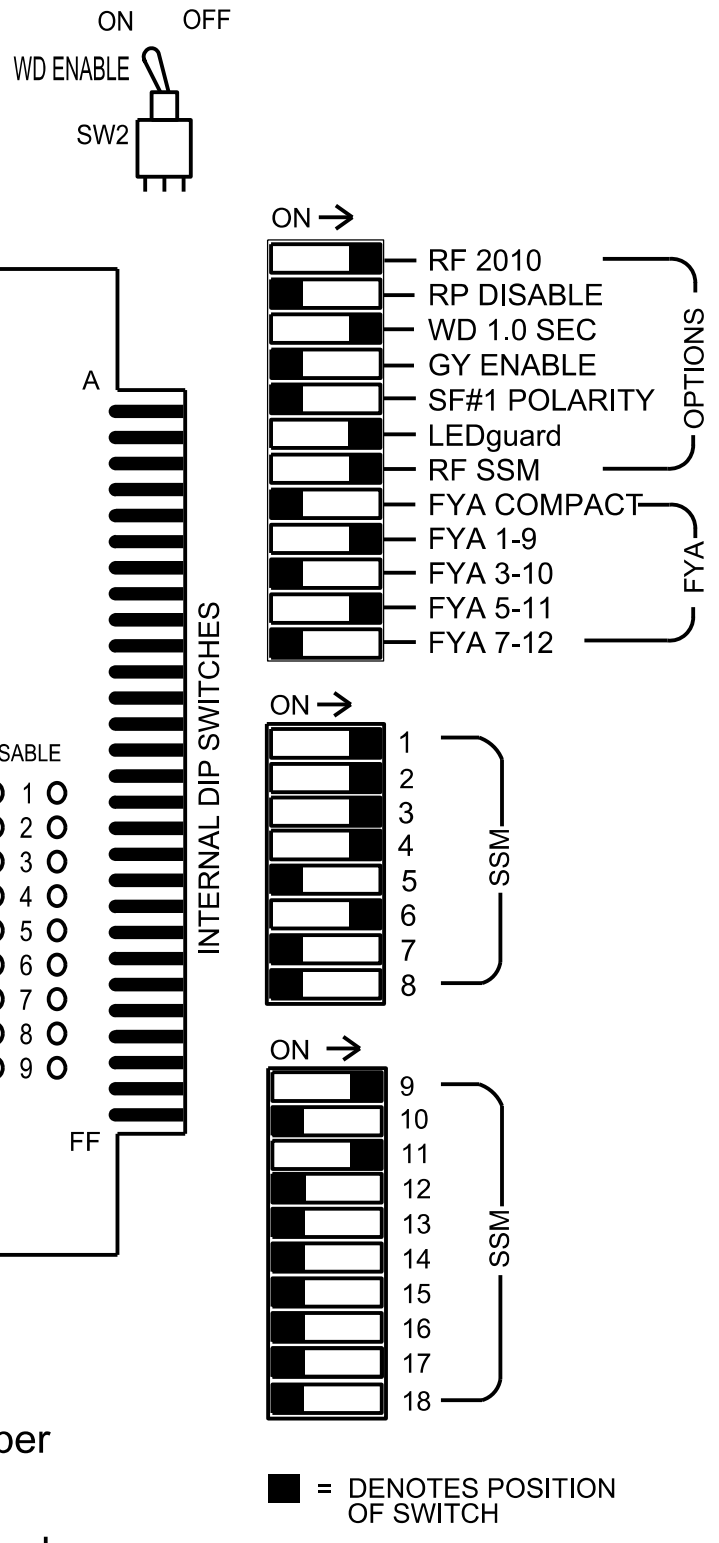
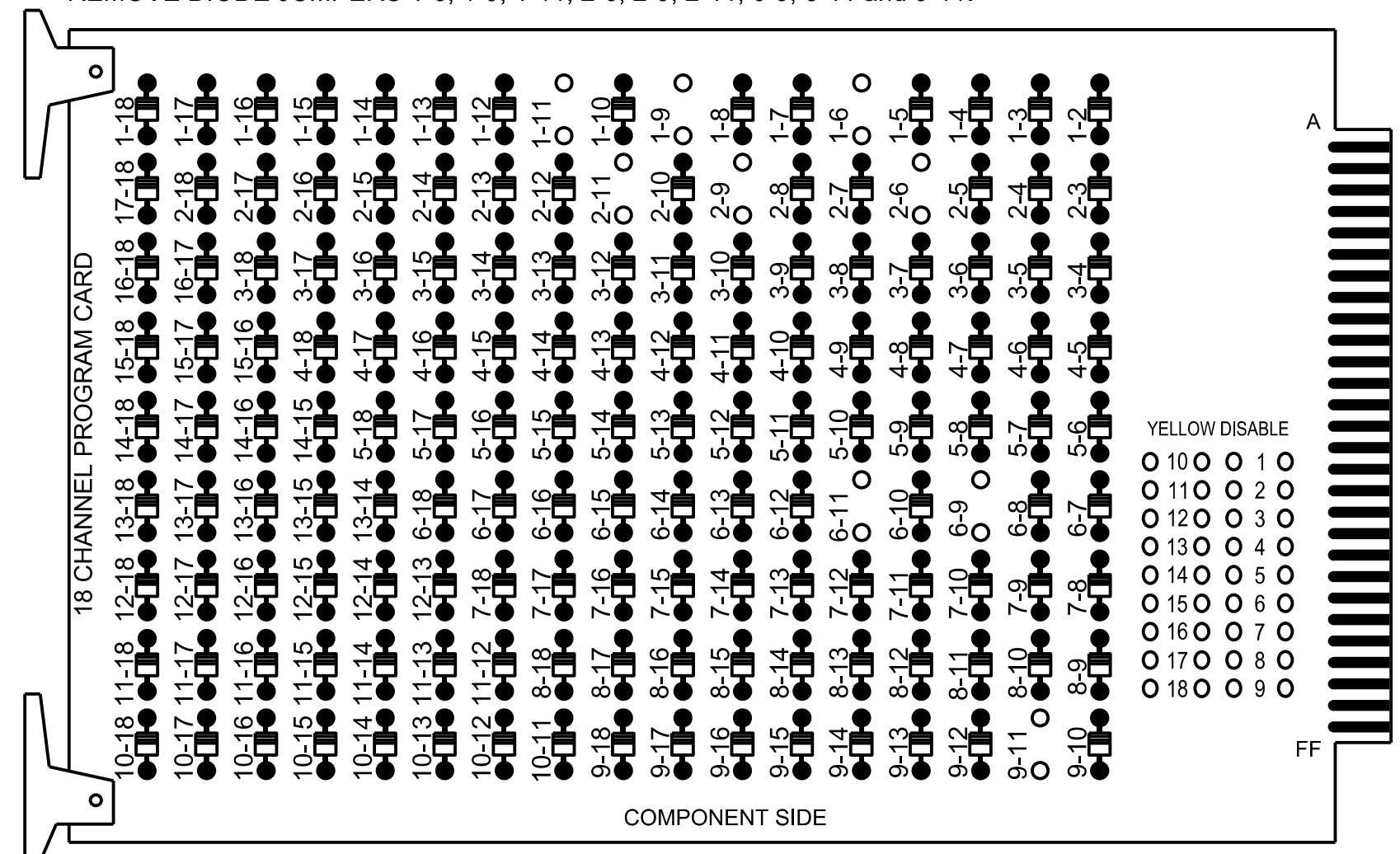
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SIG. INVENTORY NO. 08-1103T1



### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-6, 2-9, 2-11, 6-9, 6-11 and 9-11.



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

REMOVE JUMPERS AS SHOWN

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- Program Phase 39 for No Startup Veh Call.
- Program Phase 40 for Min Recall.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S1, S2, S4, S5, S8, AUX S1, AUX S4  
 Phases Used.....1, 2, 3, 4, 6, 39\*\*, 40\*\*  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....NOT USED

\*See overlap programming detail on this sheet  
 \*\*Phase used for preemption timing purposes only

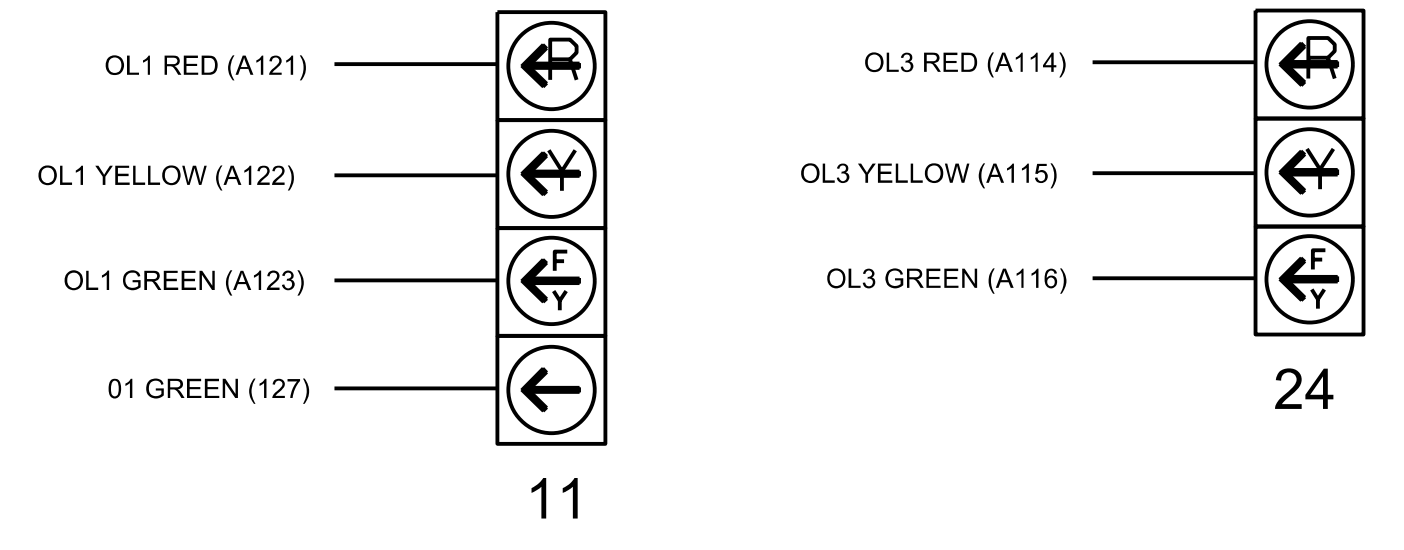
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6			
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18			
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE			
SIGNAL HEAD NO.	11*	42	21,22	NU	31	32	41	42	NU	NU	61,62	NU	NU	NU	11*	NU	NU	24*	NU	NU	
RED	*	128		116	116	101	101				134										
YELLOW		129		117	117	102	102				135										
GREEN		130		118	118	103	103				136										
RED ARROW													A121					A114			
YELLOW ARROW	126												A122					A115			
FLASHING YELLOW ARROW													A123					A116			
GREEN ARROW	127	127			118	103															

NU = Not Used  
 \*Denotes install load resistor. See load resistor installation detail this sheet.  
 \*See pictorial of head wiring in detail this sheet.

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	
U	S	S	S	S	S	S	S	S	S	S	S	S	S	S	FS
L	O	O	O	O	O	O	O	O	O	O	O	O	O	O	DC ISOLATOR
	E	E	E	E	E	E	E	E	E	E	E	E	E	E	ST
	M	M	M	M	M	M	M	M	M	M	M	M	M	M	DC ISOLATOR
	P	P	P	P	P	P	P	P	P	P	P	P	P	P	
	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	
U	S	S	S	S	S	S	S	S	S	S	S	S	S	S	PRE1
L	O	O	O	O	O	O	O	O	O	O	O	O	O	O	AC ISOLATOR
	E	E	E	E	E	E	E	E	E	E	E	E	E	E	
	M	M	M	M	M	M	M	M	M	M	M	M	M	M	
	P	P	P	P	P	P	P	P	P	P	P	P	P	P	
	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	

EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

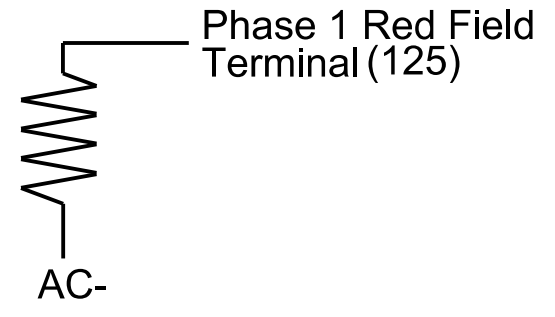
### SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



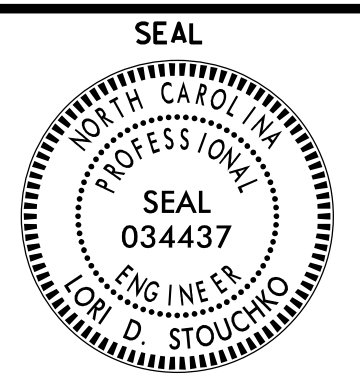
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103T1  
 DESIGNED: June 2024  
 SEALED: 7/11/2024  
 REVISED:

**M M**  
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ELECTRICAL AND PROGRAMMING DETAILS FOR:  
 Prepared for:  
  
 750 N. Greenfield Pkwy, Corner, NC 27529

NC 211/NC 73-211  
 at  
 NC 73 (South Intersection)  
 Division 8 Moore County West End  
 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:  
 REVISIONS INIT. DATE

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### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		X	X	1
NOTICE PHASE 2 FLASH RED →	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
NOTICE PHASE 6 FLASH RED →	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

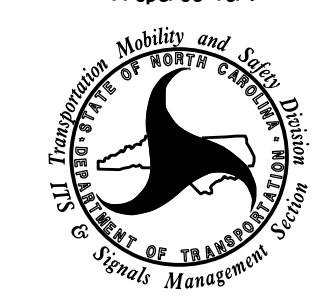
Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-1103T1  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 2 of 3  
Temporary Design 1 (TMP Phase I)

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ELECTRICAL AND PROGRAMMING  
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NC 211/NC 73-211  
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NORTH CAROLINA  
PROFESSIONAL  
ENGINEER  
SEAL  
034437  
LD D. STOUCHKO

SIG. INVENTORY NO. 08-1103T1

### SEQUENCE DETAIL

Front Panel  
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface  
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

### PREEMPTION PROGRAMMING

Front Panel  
Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

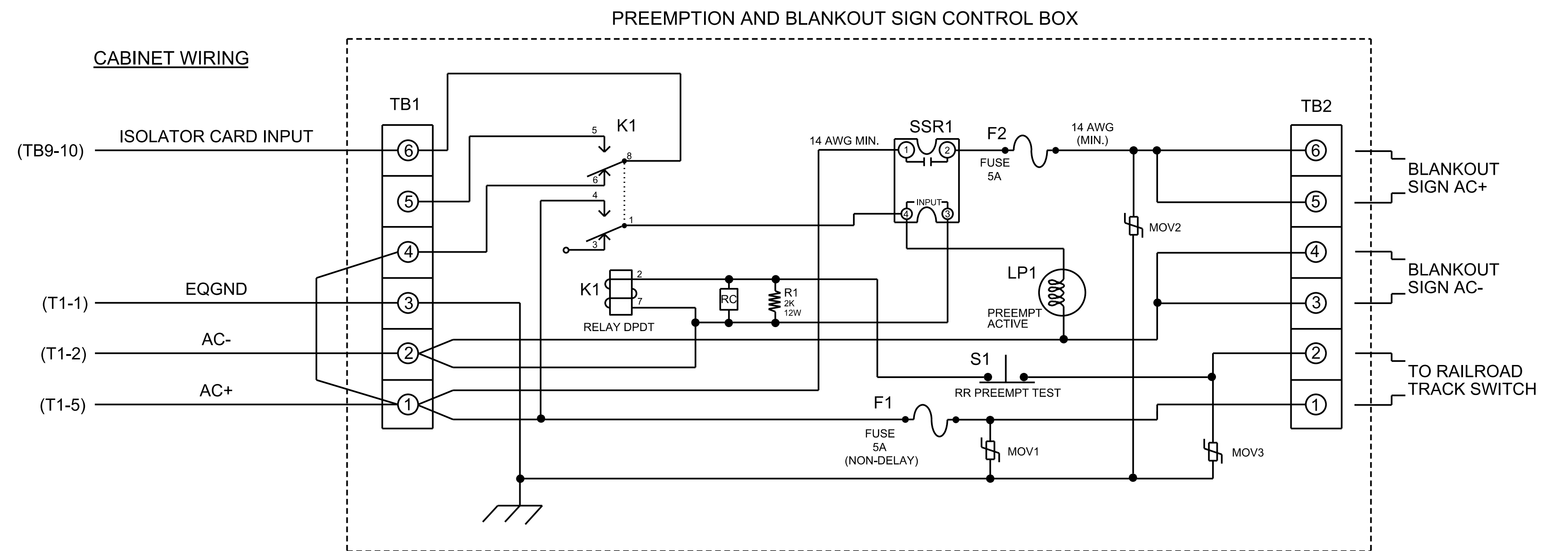
Web Interface  
Home >Controller >Preempt Configuration >Preempts

Preempt Configuration

Preempt	1
Enabled	Enabled
Type	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	-
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.4
Enter Red Clear	2.4
Track Green	27
Track Yellow Clr	3.8
Track Red Clear	1.6
Dwell Green	0
Exit Min Green	25.5
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovrdr Flash	X
Not Ovrdr Nxt Pre	-
Require All Red Entry	-
Track Clear Ovrdr	X
Ped Clear During Yellow	-
Entry Omit OLTG	X
Track Reserve	X

### RAILROAD PREEMPTION WIRING DETAIL

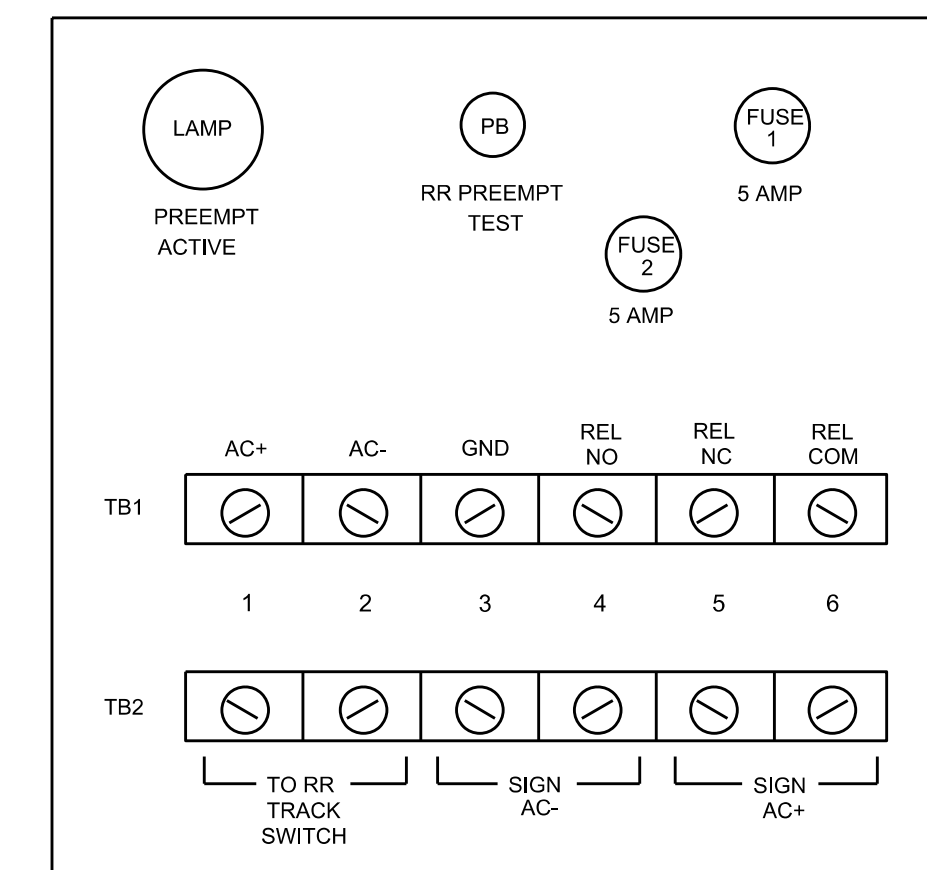
(wire as shown below)



### NOTES

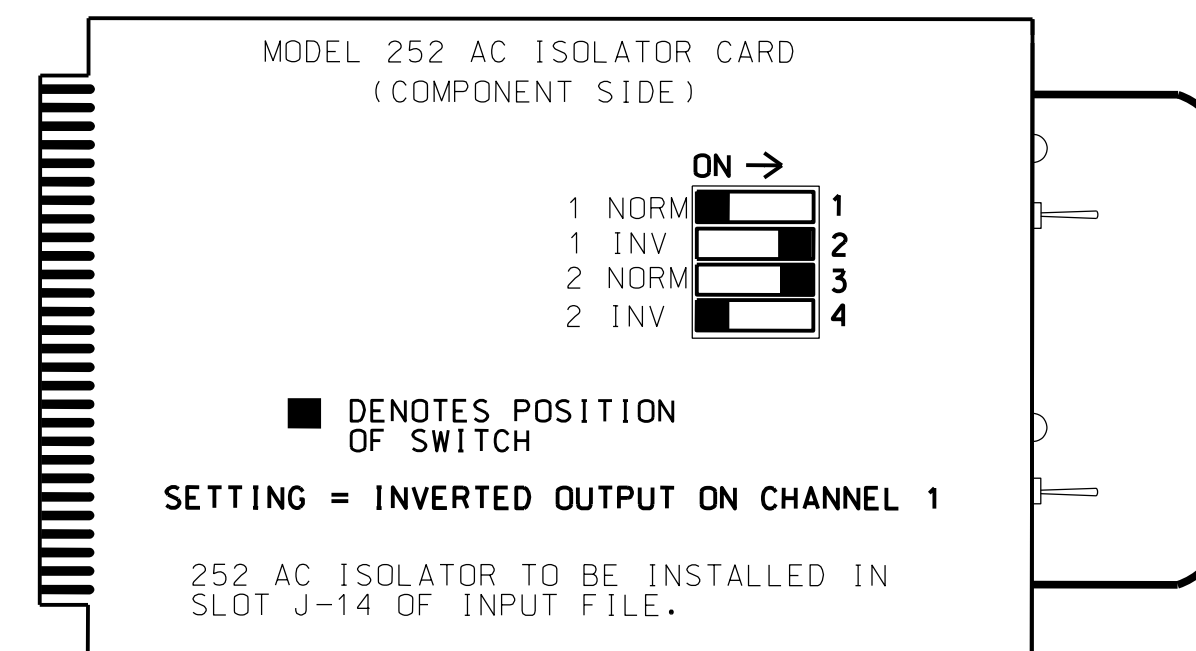
- Relay K1 is shown in the energized (Preempt not active) normal operation state.
- Relay K1 is a DPDT with 120VAC coil with octal base.
- Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- IMPORTANT!!** A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

### FRONT VIEW



### PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103T1  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3  
Temporary Design 1 (TMP Phase I)

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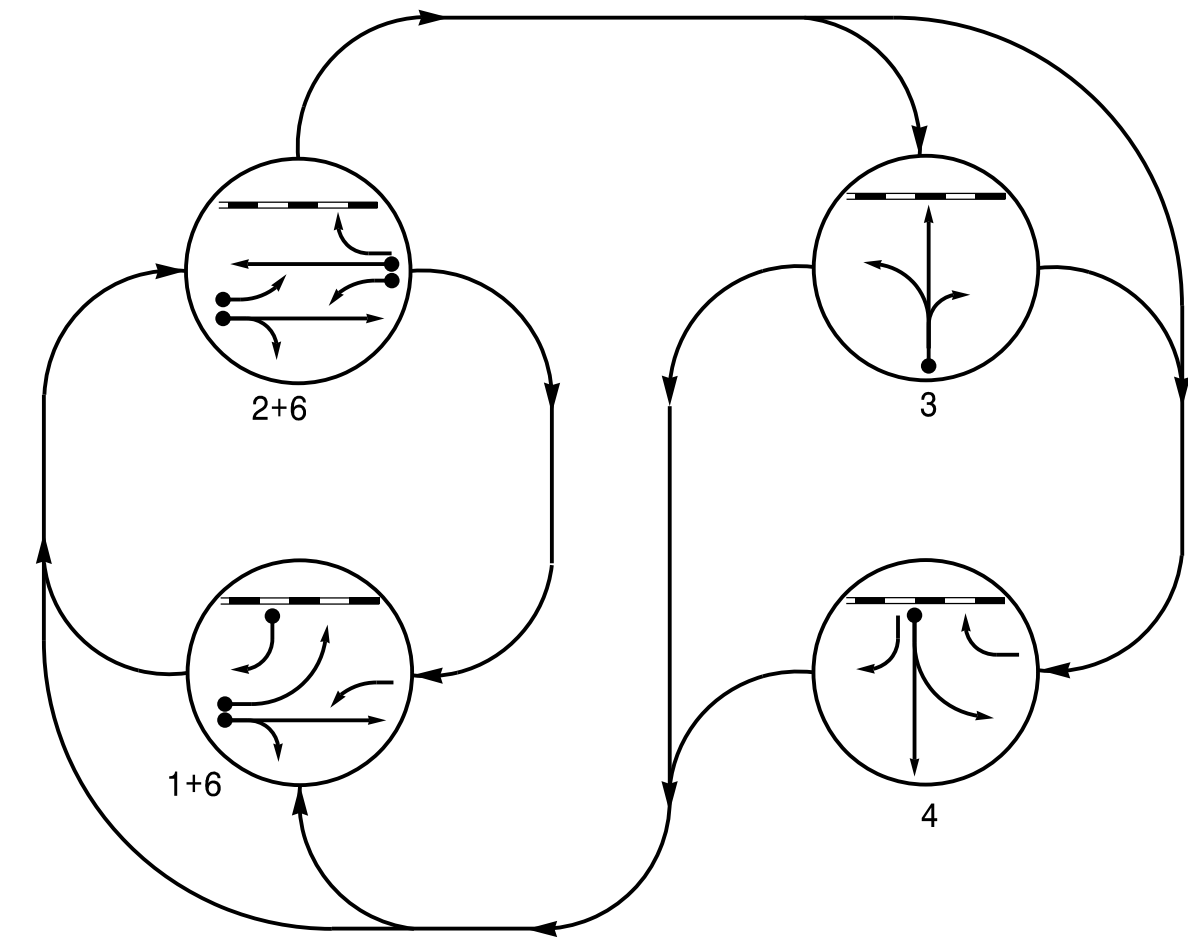
ELECTRICAL AND PROGRAMMING DETAILS FOR:  
Prepared for:  
Division 8  
Prepared By: LD Stouchko  
750 N. Greenfield Pkwy, Corner, NC 27529

NC 211/NC 73-211  
at  
NC 73 (South Intersection)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
REVISIONS  
INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 034437  
L. D. STOUCHKO  
DATE  
SIG. INVENTORY NO. 08-1103T1

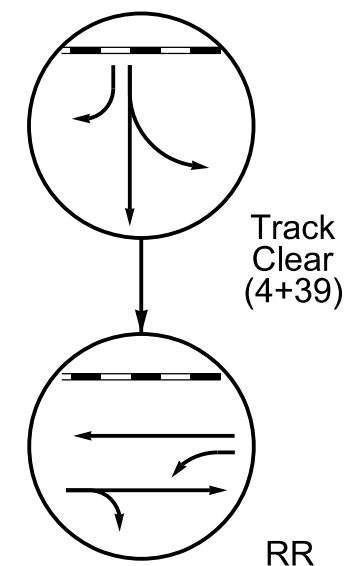
PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

RAIL PREEMPT PHASES (High Priority)



SIGNAL FACE	PHASE							
	1+6	2+6	3	4	TR	RL	AE	CA
11	Y	R	R	R	R	R	R	R
21,22	R	G	R	R	R	R	R	R
23	R	F	R	F	R	R	R	R
24	F	F	R	R	R	R	R	R
31	R	R	G	R	R	R	R	R
32	R	R	G	R	R	R	R	R
41	R	R	R	G	G	R	R	R
42	R	R	R	G	G	R	R	R
43	R	R	R	G	G	R	R	R
61,62	G	G	R	R	R	R	R	R

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	
1A*	6X40	0	*	*	1	15.0	-	X	-	X	-	*
1B*	6X40	0	*	*	6	3.0	-	X	-	X	X	*
2A*	6X6	200	*	*	2	-	-	X	X	X	-	*
2C*	6X40	0	*	*	2	3.0	-	X	-	X	X	*
3A*	15X6	0	*	*	3	10.0	-	X	-	X	-	*
4A*	6X40	0	*	*	4	3.0	-	X	-	X	X	*
6A*	6X6	300	*	*	6	-	-	X	X	X	-	*

\* Video Detection Zone

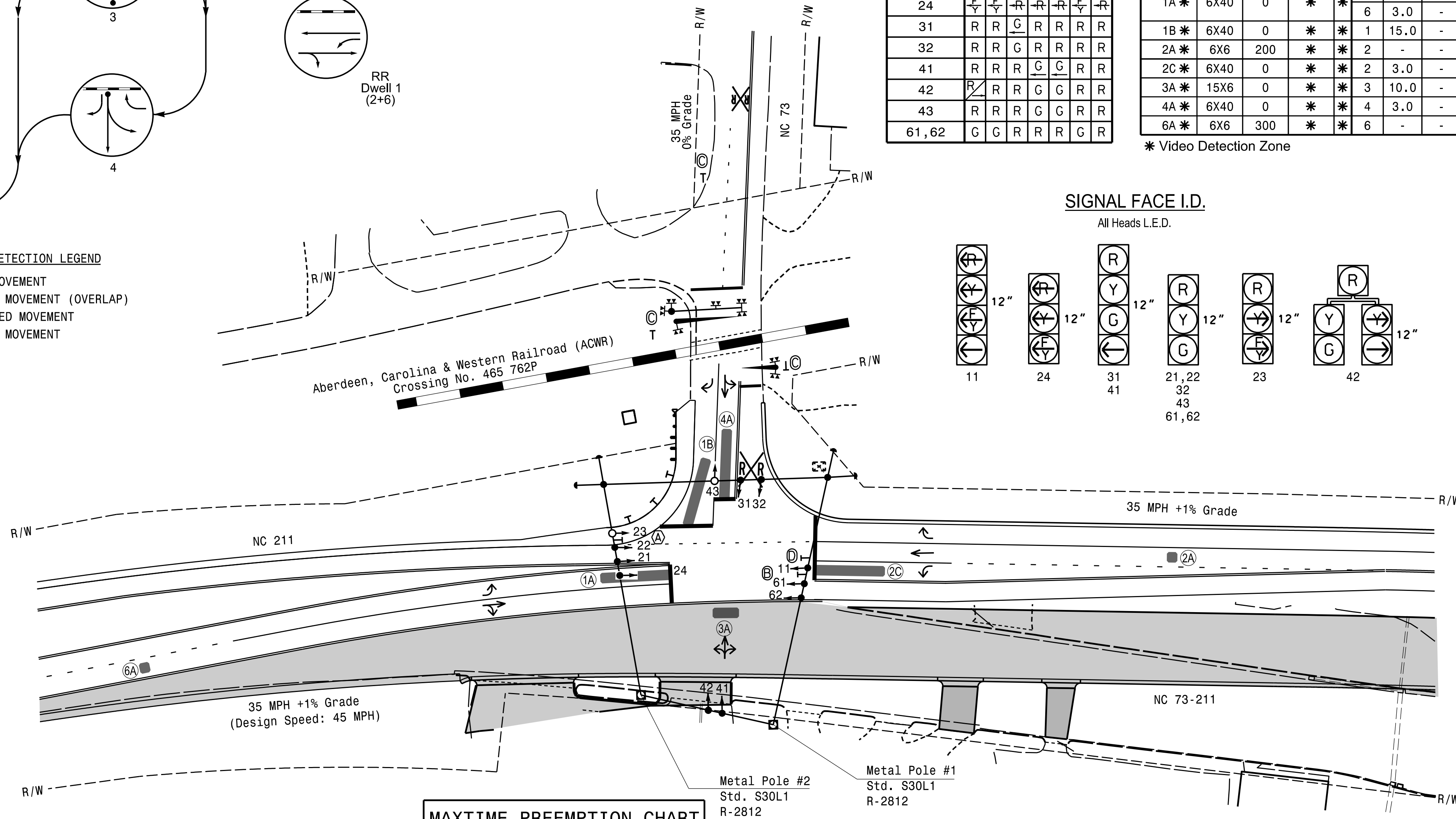
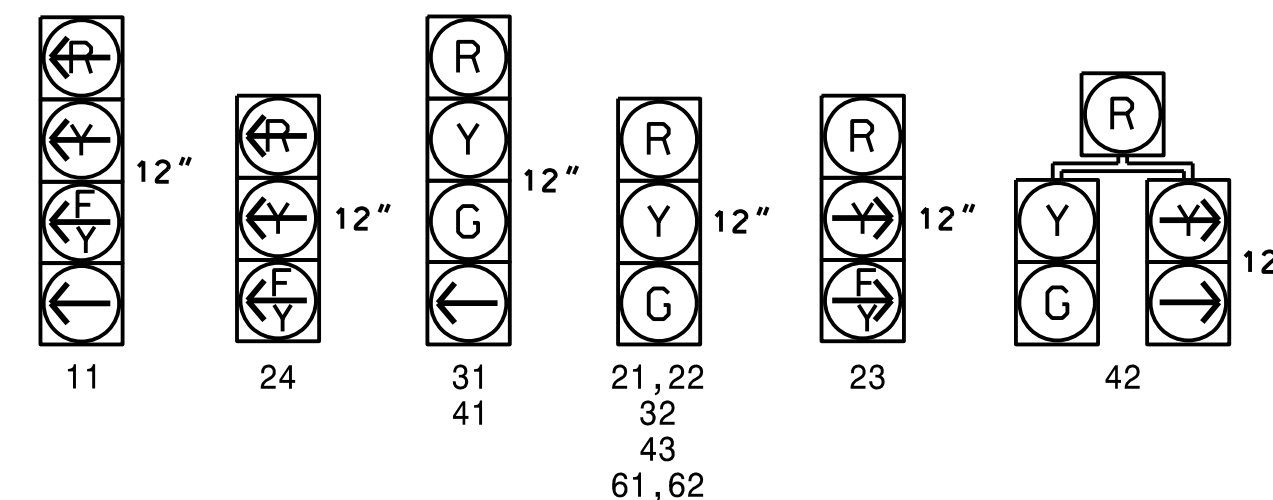
4 Phase Fully Actuated With Railroad Preemption (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- This location contains railroad preemption phasing. Do not program signal for late night flashing operation.
- Phase 1 may be lagged.
- The order of phase 3 and phase 4 may be reversed.
- Reposition existing signal heads numbered 11,21,22,24,61 and 62.
- Set all detector units to presence mode.
- Program phase 40 to run concurrently with all phases during normal operation. Phase 39 must be incompatible with Phase 40 and included as a track clear phase.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

SIGNAL FACE I.D.

All Heads L.E.D.



LEGEND

- | PROPOSED   | EXISTING   |
|--|--|
| ○ → Traffic Signal Head                            | ● → Traffic Signal Head                            |
| ○ → Modified Signal Head                           | N/A  |
| ○ → Sign   | ○ → Sign   |
| ○ → Pedestrian Signal Head With Push Button & Sign | ○ → Pedestrian Signal Head With Push Button & Sign |
| ○ → Signal Pole with Guy                           | ○ → Signal Pole with Guy                           |
| ○ → Signal Pole with Sidewalk Guy                  | ○ → Signal Pole with Sidewalk Guy                  |
| ○ → Inductive Loop Detector                        | ○ → Inductive Loop Detector                        |
| ○ → Controller & Cabinet                           | ○ → Controller & Cabinet                           |
| ○ → Junction Box                                   | ○ → Junction Box                                   |
| ○ → 2-in Underground Conduit                       | ○ → 2-in Underground Conduit                       |
| N/A  | ○ → Right of Way                                   |
| →  | → Directional Arrow                                |
| N/A  | → Railroad Cantilever                              |
| N/A  | → Railroad Gate and Flasher                        |
| N/A  | → Railroad Tracks                                  |
| N/A  | → Guardrail  |
| ■  | ■ Construction Zone                                |
| ■  | ■ Non-Intrusive Detection Zone                     |
| ○  | ○ Metal Strain Pole                                |
| ○  | ○ "RIGHT TURN SIGNAL" Sign (R10-10R)               |
| ○  | ○ Left Arrow "ONLY" Sign (R3-5L)                   |
| ○  | ○ "DO NOT STOP ON TRACKS" Sign (R8-8)              |
| ○  | ○ "U-TURN YIELD TO RIGHT TURN" Sign (R10-16)       |

FEATURE	PHASE						
	1	2	3	4	6	39	40
Walk *	-	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-	-
Min Green *	7	10	7	7	12	-	-
Passage *	2.0	5.0	2.0	2.0	6.0	-	-
Max I *	20	60	15	30	60	-	-
Yellow Change	3.0	4.4	3.8	3.8	4.4	3.8	4.4
Red Clear	1.9	1.6	1.0	1.2	1.6	1.2	1.9
Added Initial *	-	2.5	-	-	2.5	-	-
Maximum Initial *	-	24	-	-	34	-	-
Time Before Reduction *	-	15	-	-	15	-	-
Time To Reduce *	-	34	-	-	34	-	-
Minimum Gap	-	3.0	-	-	3.0	-	-
Advance Walk	-	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	-	-	-
Vehicle Recall	-	MIN RECALL	-	-	MIN RECALL	-	MIN RECALL
Dual Entry	-	-	-	-	-	-	-

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

FUNCTION	PRE 1
Type	RAIL ROAD
Exit Phases	4
Delay	0
Max Presence	0
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.4*
Enter Red Clear	1.9*
Track Green	27
Track Yellow Change	3.8
Track Red Clear	1.2
Dwell Green	0
Exit Min Green	255*
Exit Yellow Change	25.5*
Exit Red Clear	25.5*
Call Extend Time	1.0
Exit Type	EXIT PHASES
Ped Clear Through Yellow	-
Require All Red Entry	-

\* Directs controller to use default phase timing.

This signal was designed for advanced preemption

Signal Upgrade Temporary Design 2 (TMP Phase II)

**MOTT MACDONALD**  
 MOTT MACDONALD I & E, LLC  
 930 Main Campus Drive  
 Suite 200  
 RALEIGH, NC 27606  
 License No. F-0669

Prepared for: **TRANSPORTATION MOBILITY AND SAFETY DIVISION**  
 STATE OF NORTH CAROLINA  
 DEPARTMENT OF TRANSPORTATION  
 Signal Design Section

750 N. Greenfield Pkwy, Garner, NC 27529

NC 211/NC 73-211 at NC 73 (South Intersection)

Division 8 Moore County West End

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

SCALE: 1" = 40'

REVISIONS: INIT. DATE

SEAL: MOTT MACDONALD I & E, LLC, License No. F-0669, SEAL 034437

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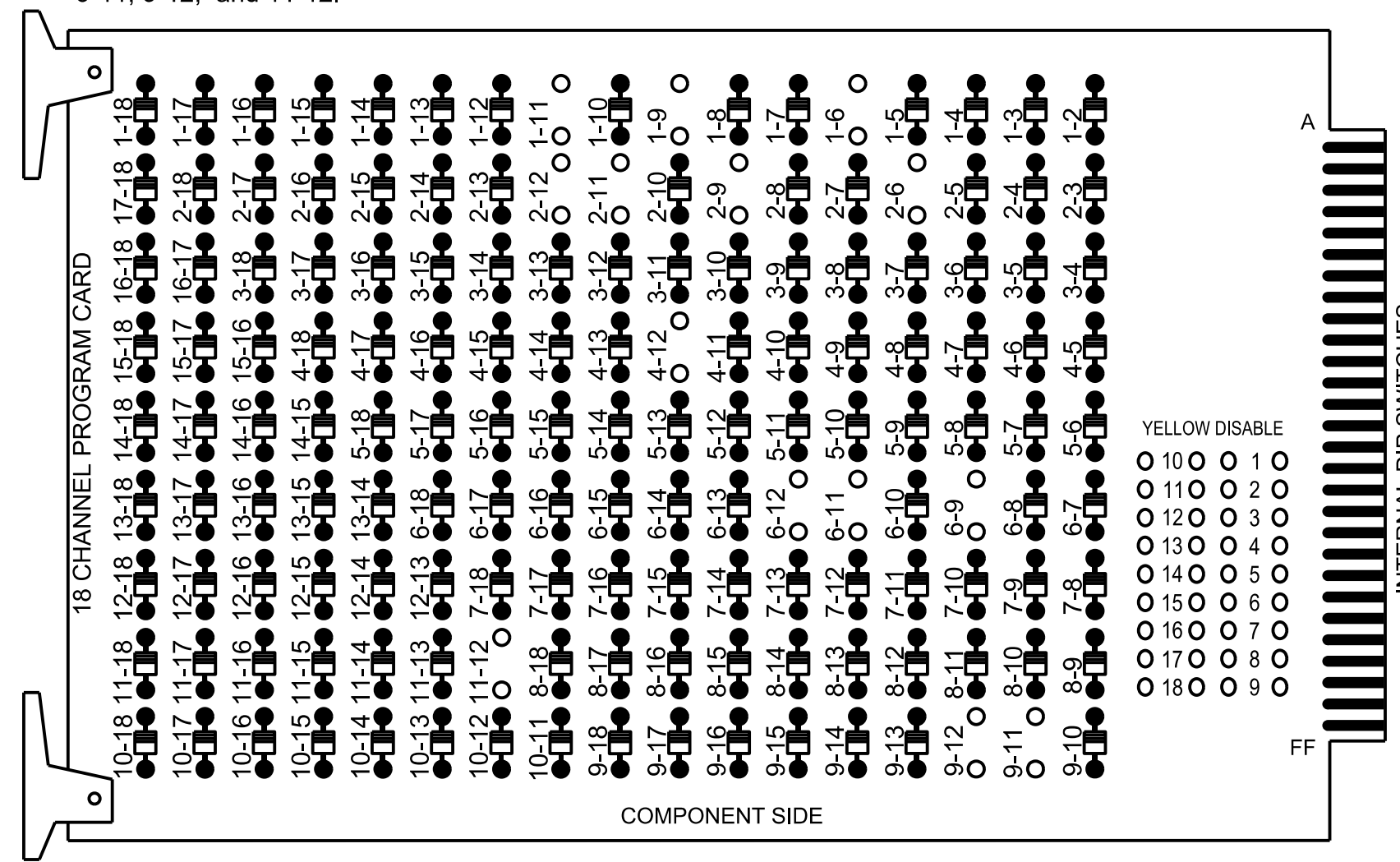
SIGNATURE: DATE: SIG. INVENTORY NO. 08-110312



### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

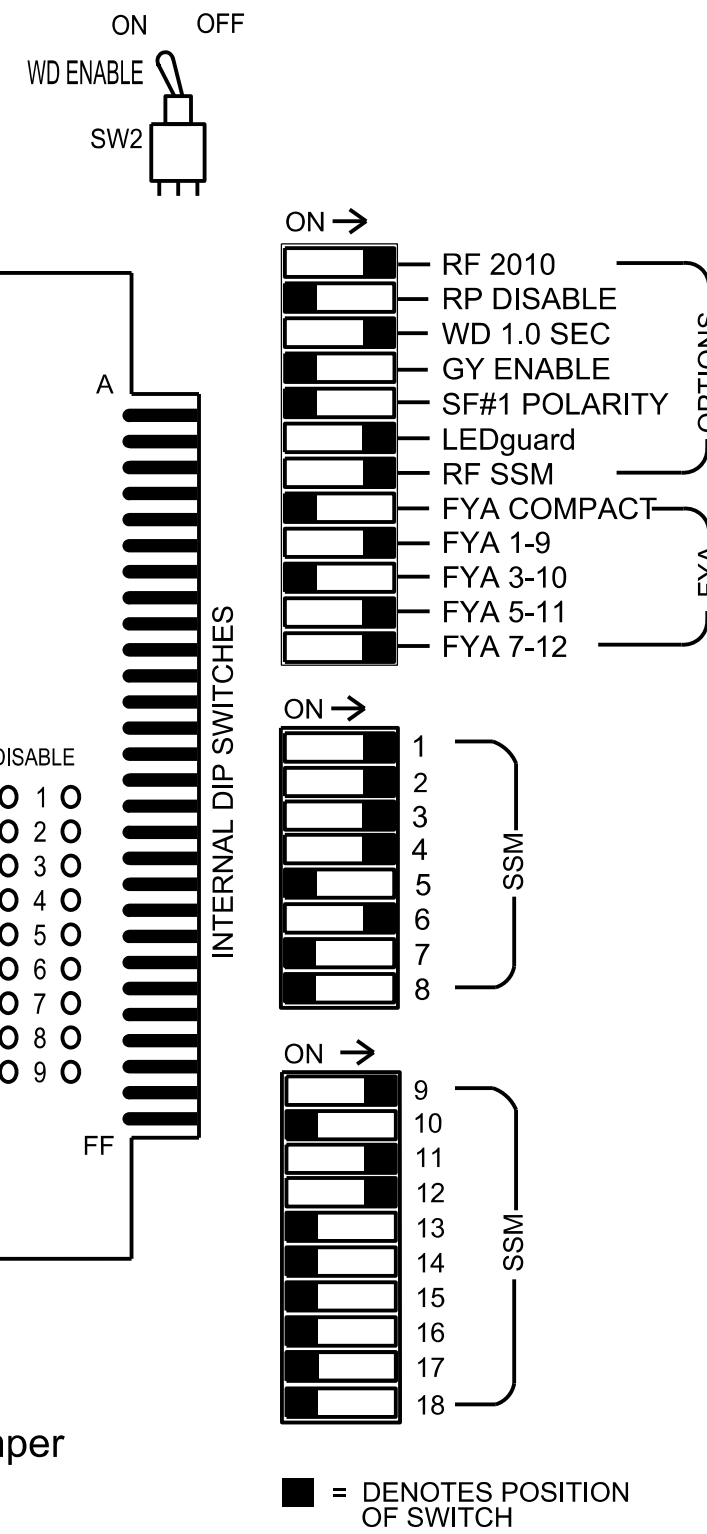
REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-6, 2-9, 2-11, 2-12, 4-12, 6-9, 6-11, 6-12, 9-11, 9-12, and 11-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- Program Phase 39 for No Startup Veh Call.
- Program Phase 40 for Min Recall.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S1, S2, S4, S5, S8, AUX S1,  
 AUX S4, AUX S5

Phases Used.....1, 2, 3, 4, 6, 39\*\*, 40\*\*  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....\*

\*See overlap programming detail on sheet 2  
 \*\*Phase used for preemption timing purposes only

### SIGNAL HEAD HOOK-UP CHART

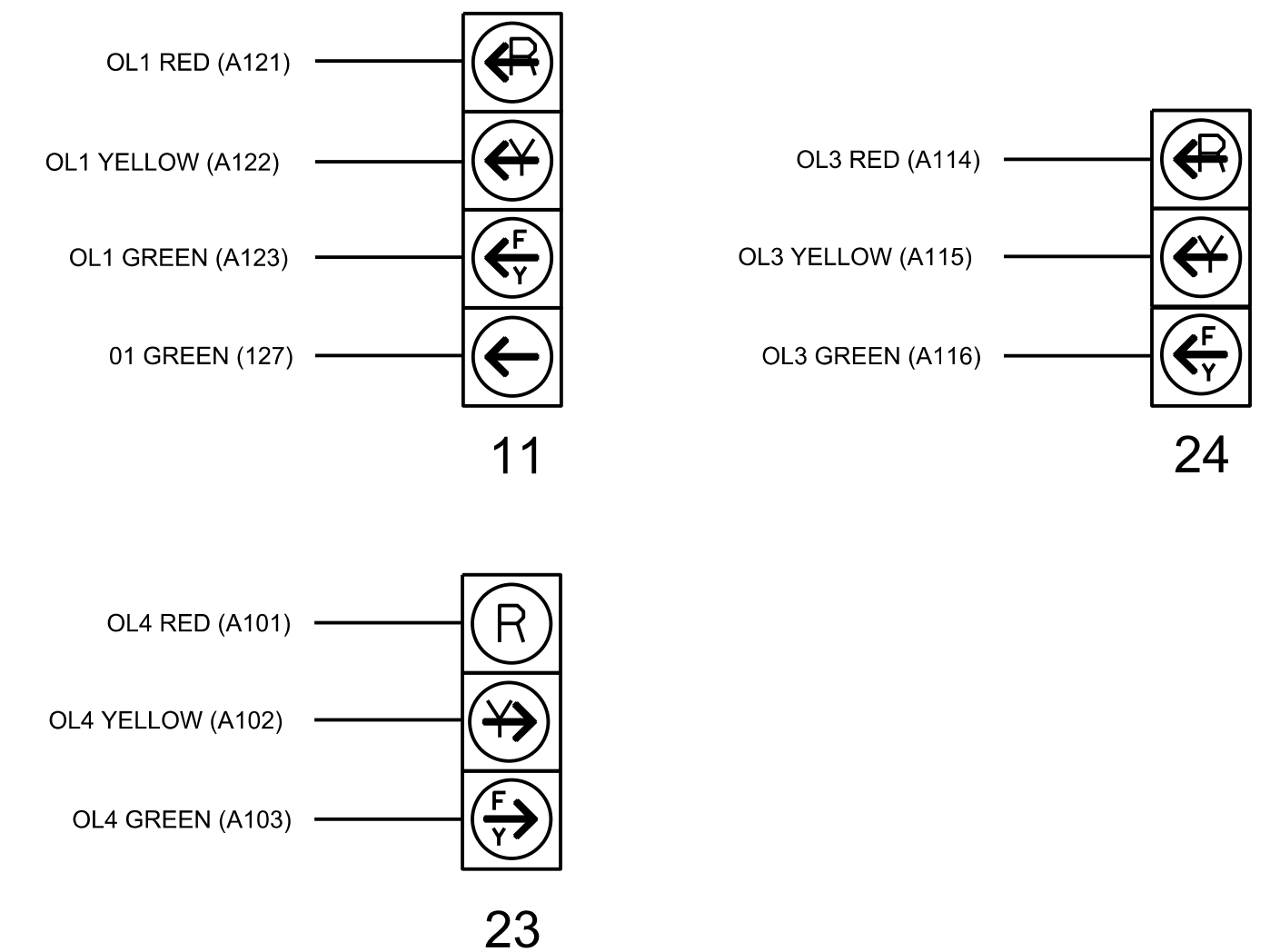
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE				
SIGNAL HEAD NO.	11*	42	21,22	NU	31	32	41	42,43	NU	NU	61,62	NU	NU	NU	11	NU	NU	24*	23*	NU		
RED		*	128	116	116	101	101				134									A101		
YELLOW			129	117	117	102	102				135											
GREEN			130	118	118	103	103				136											
RED ARROW																				A121	A114	
YELLOW ARROW		126																		A122	A115	A102
FLASHING YELLOW ARROW																				A123	A116	A103
GREEN ARROW	127	127			118	103																

NU = Not Used

\*Denotes install load resistor. See load resistor installation detail this sheet.  
 \*See pictorial of head wiring in detail this sheet.

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	S	S	S	S	S	S	S	S	S	S	S	S	FS
L	T	T	T	T	T	T	T	T	T	T	T	T	T	DC ISOLATOR
U	S	S	S	S	S	S	S	S	S	S	S	S	S	PRE1
L	T	T	T	T	T	T	T	T	T	T	T	T	T	AC ISOLATOR
														NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

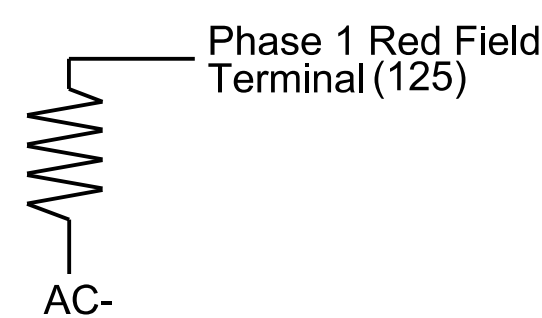
### SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

### LOAD RESISTOR INSTALLATION DETAIL

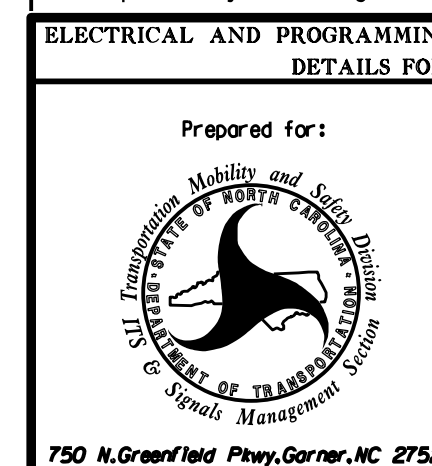
(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-110312  
 DESIGNED: June 2024  
 SEALED: 7/11/2024  
 REVISED:

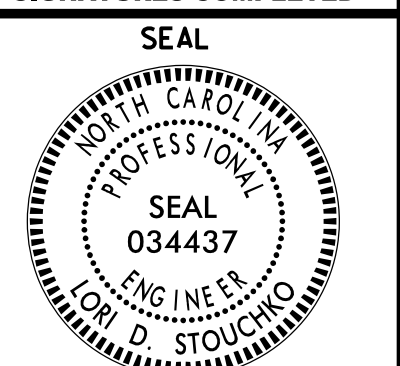
**M M**  
**MOTT MACDONALD**  
 MOTT MACDONALD I & E, LLC  
 930 Main Campus Drive  
 Suite 200  
 RALEIGH, NC 27606  
 License No. F-0669



Electrical Detail - Sheet 1 of 3  
 Temporary Design 2 (TMP Phase II)

Prepared for:		NC 211/NC 73-211 at NC 73 (South Intersection)	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax	Division 8	Moore County West End
PREPARED BY: LD Stouchko	REVIEWED BY:		
REVISIONS		INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



### OVERLAP PROGRAMMING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps  
Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	6	2,4
Modifier Phases	1	-	-	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

NOTICE  
PHASE 2  
FLASH RED →

NOTICE  
PHASE 6  
FLASH RED →

NOTICE  
OVERLAP 1  
FLASH RED →

NOTICE  
OVERLAP 3  
FLASH RED →

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

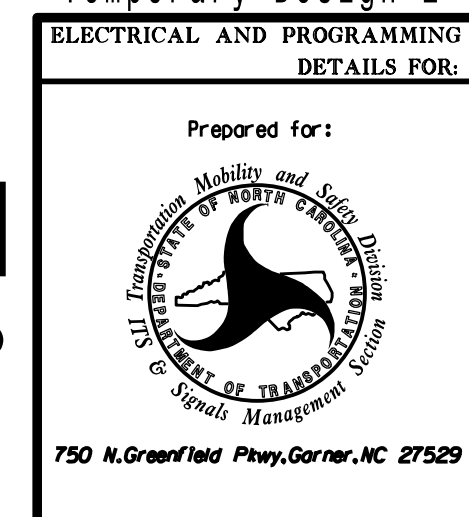
Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

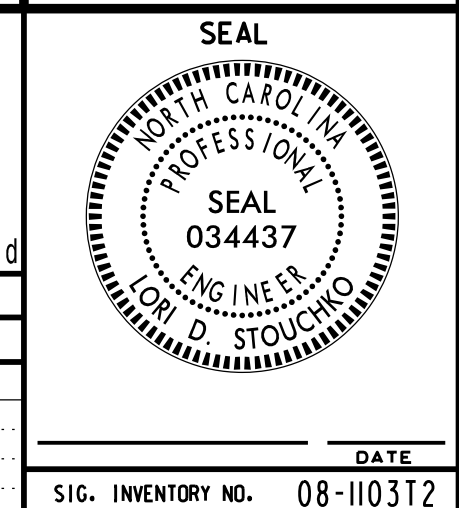
THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-1103T2  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:



Electrical Detail - Sheet 2 of 3  
Temporary Design 2 (TMP Phase II)

NC 211/NC 73-211 at NC 73 (South Intersection)	
Division 8	Moore County West End
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED



### SEQUENCE DETAIL

Front Panel  
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface  
Home >Controller >Sequence

#### Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

### PREEMPTION PROGRAMMING

Front Panel  
Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

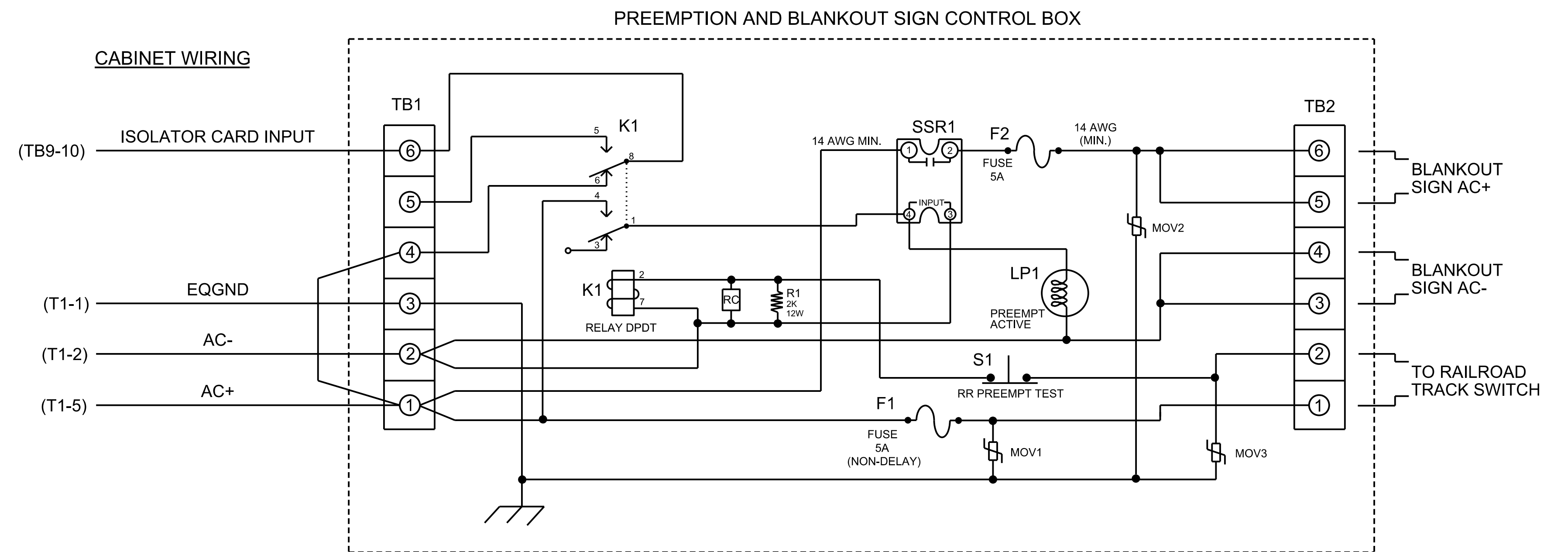
Web Interface  
Home >Controller >Preempt Configuration >Preempts

#### Preempt Configuration

Preempt	1
Enabled	Enabled
Type	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	4
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.4
Enter Red Clear	1.9
Track Green	27
Track Yellow Clr	3.8
Track Red Clear	1.2
Dwell Green	0
Exit Min Green	25.5
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovr Flash	X
Not Ovr Nxt Pre	X
Require All Red Entry	-
Track Clear Ovr	X
Ped Clear During Yellow	-
Entry Omit OLTG	X
Track Reserve	X

### RAILROAD PREEMPTION WIRING DETAIL

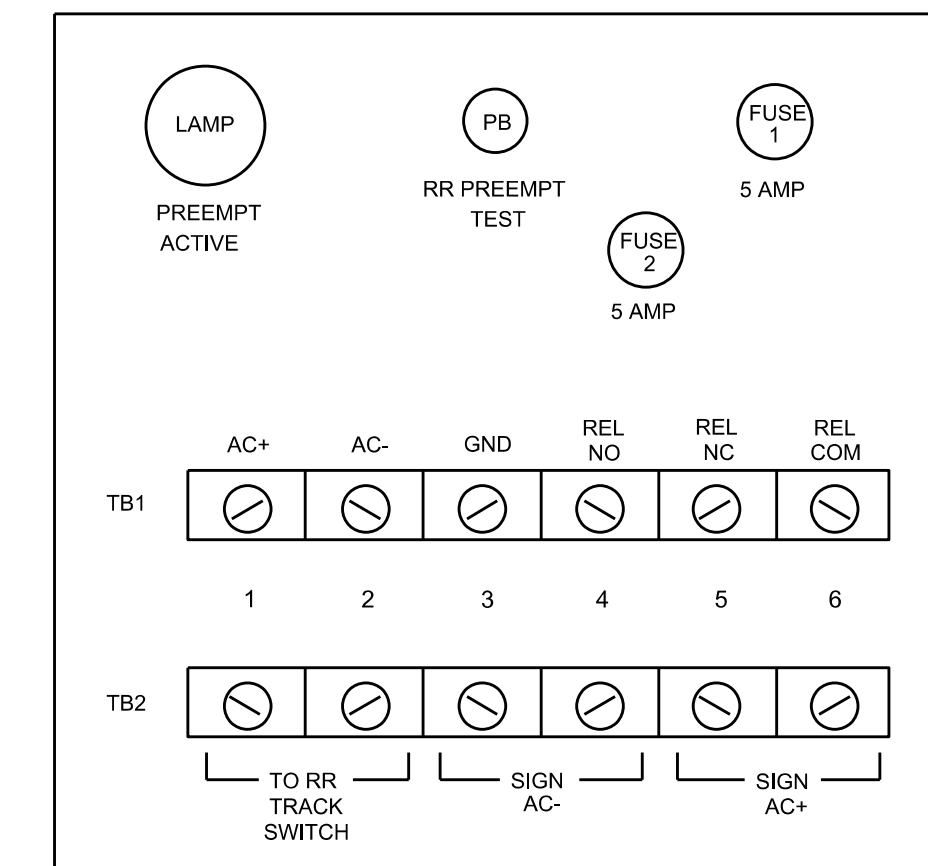
(wire as shown below)



#### NOTES

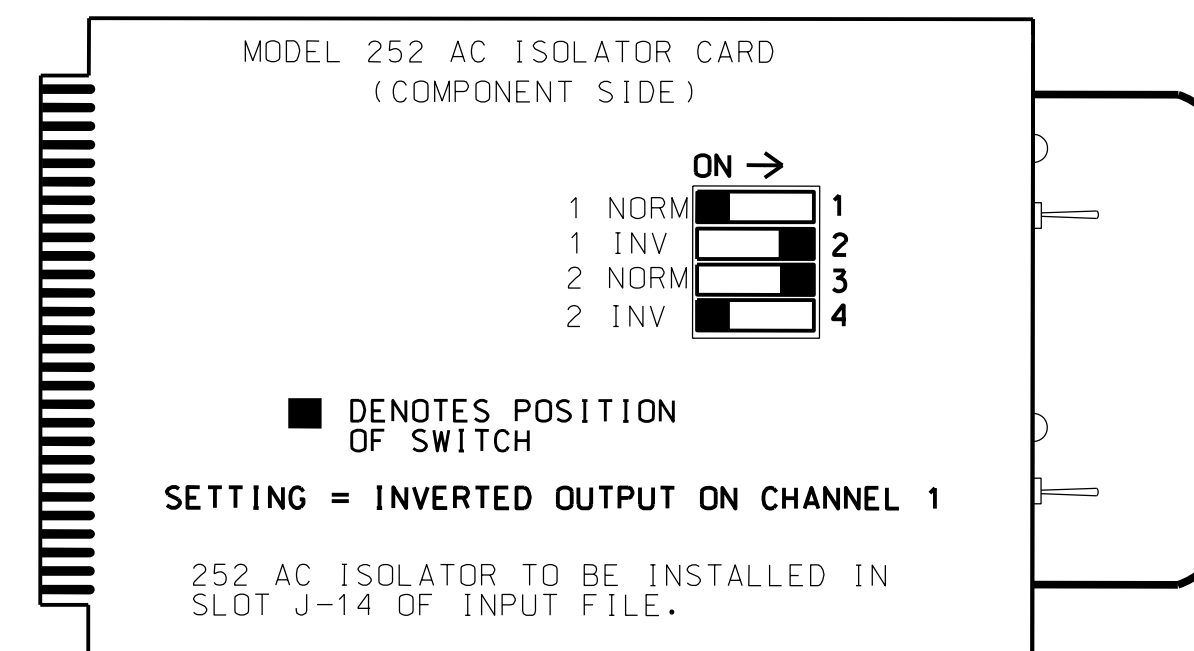
- Relay K1 is shown in the energized (Preempt not active) normal operation state.
- Relay K1 is a DPDT with 120VAC coil with octal base.
- Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- IMPORTANT!!** A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

#### FRONT VIEW



### PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103T2  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3  
Temporary Design 2 (TMP Phase II)

**M M**  
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ELECTRICAL AND PROGRAMMING DETAILS FOR:  
Prepared for:  
  
1750 N. Greenfield Pkwy, Corner, NC 27529

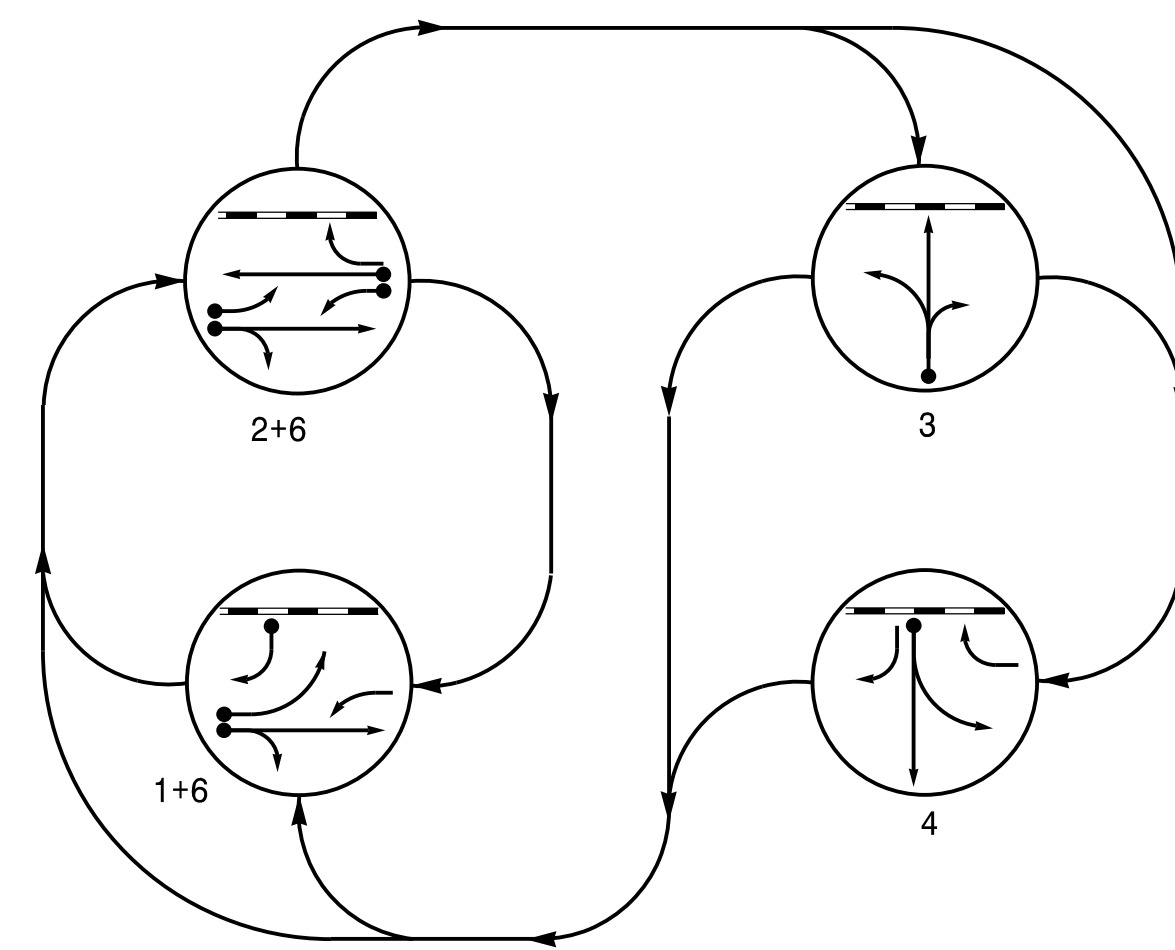
NC 211/NC 73-211  
at  
NC 73 (South Intersection)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 034437  
LD STOUCHKO  
DATE  
SIG. INVENTORY NO. 08-1103T2



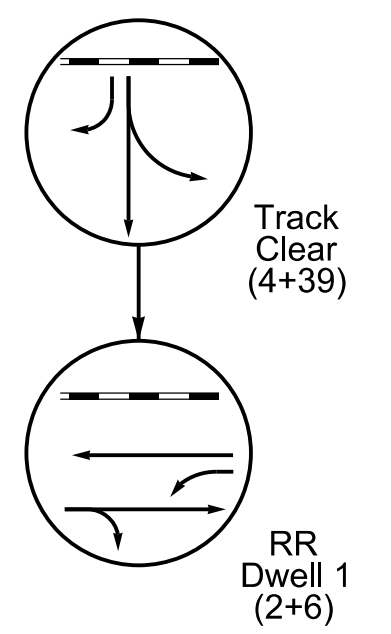
PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

- ←•→ DETECTED MOVEMENT
- ←→ UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- ←- - -→ PEDESTRIAN MOVEMENT

RAIL PREEMPT PHASES  
(High Priority)



SIGNAL FACE	PHASE						
	1 + 6	2 + 6	3	4	TRUCK CLEAR	RAILROAD	FLASH
11	---	F	F	F	F	F	F
21, 22	R	G	R	R	R	R	G
23	R	F	R	F	R	R	R
24	F	F	F	F	F	F	F
31	R	R	G	R	R	R	R
32	R	R	G	R	R	R	R
41	R	R	R	G	G	R	R
42	R	R	R	G	G	R	R
43	R	R	R	G	G	R	R
61, 62	G	G	R	R	R	G	R

MAXTIME DETECTOR INSTALLATION CHART											
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING					NEW CARD	
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL		
1A*	6X40	0	*	*	1	15.0	-	X	-	X	*
1B*	6X40	0	*	*	1	15.0	-	X	-	X	*
2A*	6X6	200	*	*	2	-	-	X	X	X	*
2C*	6X40	0	*	*	2	3.0	-	X	-	X	*
3A*	15X6	0	*	*	3	10.0	-	X	-	X	*
4A*	6X40	0	*	*	4	3.0	-	X	-	X	*
6A*	6X6	300	*	*	6	-	-	X	X	X	*

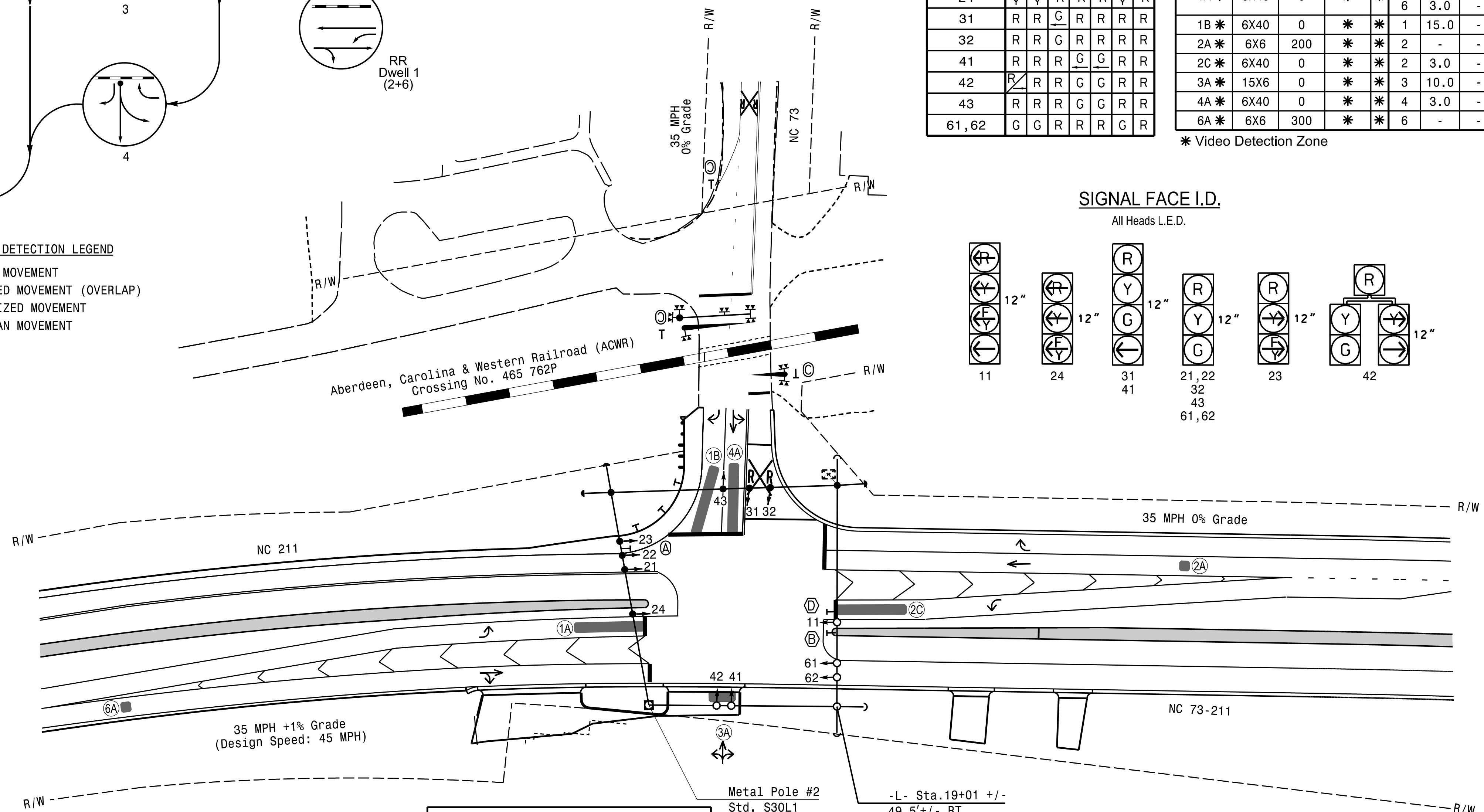
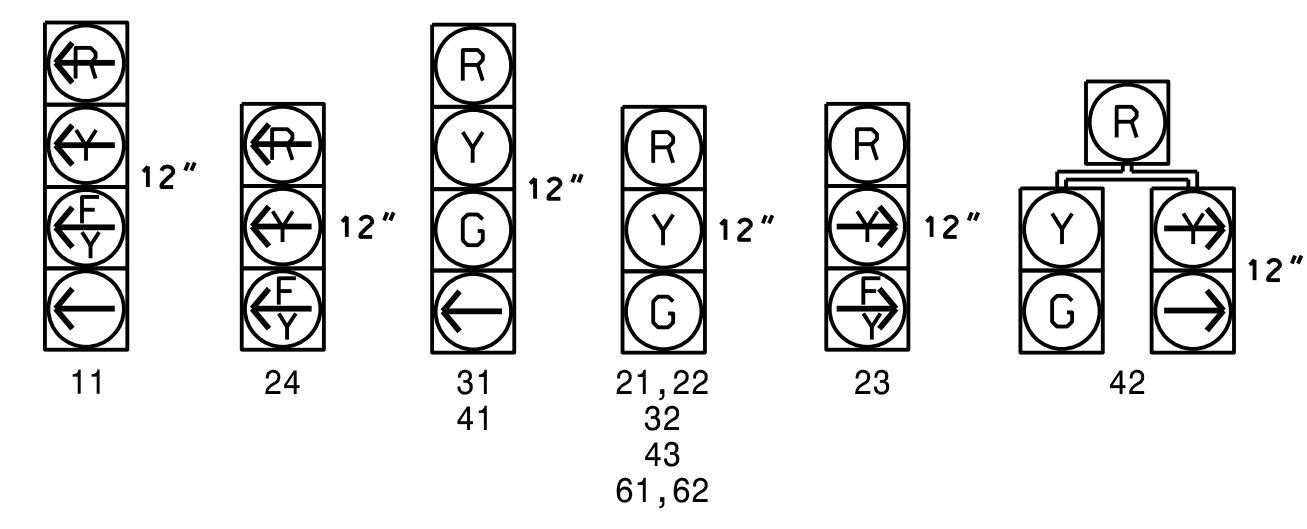
\* Video Detection Zone

4 Phase Fully Actuated With Railroad Preemption (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- This location contains railroad preemption phasing. Do not program signal for late night flashing operation.
- Phase 1 may be lagged.
- The order of phase 3 and phase 4 may be reversed.
- Reposition existing signal heads numbered 11, 21, 22, 24, 61 and 62.
- Set all detector units to presence mode.
- Program phase 40 to run concurrently with all phases during normal operation. Phase 39 must be incompatible with Phase 40 and included as a track clear phase.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

SIGNAL FACE I.D.  
All Heads L.E.D.



LEGEND

- | PROPOSED | EXISTING |
|----------|----------|
|          |          |
|          | N/A      |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |
| N/A      |          |
|          |          |
| N/A      |          |
| N/A      |          |
| N/A      |          |
| N/A      |          |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |
|          |          |

MAXTIME TIMING CHART

FEATURE	PHASE						
	1	2	3	4	6	39	40
Walk *	-	-	-	-	-		
Ped Clear *	-	-	-	-	-		
Min Green *	7	10	7	7	12		
Passage *	2.0	5.0	2.0	2.0	6.0		
Max I *	20	60	15	30	60		
Yellow Change	3.0	4.5	3.8	3.8	4.5	3.8	4.5
Red Clear	3.1	2.3	1.9	1.8	2.3	1.8	3.1
Added Initial *	-	2.5	-	-	2.5		
Maximum Initial *	-	24	-	-	34		
Time Before Reduction *	-	15	-	-	15		
Time To Reduce *	-	34	-	-	34		
Minimum Gap	-	3.0	-	-	3.0		
Advance Walk	-	-	-	-	-		
Non Lock Detector	X	-	X	X	-		
Vehicle Recall	-	MIN RECALL	-	-	MIN RECALL		MIN RECALL
Dual Entry	-	-	-	-	-		

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

MAXTIME PREEMPTION CHART

FUNCTION	PRE 1
Type	RAIL ROAD
Exit Phases	4
Delay	0
Max Presence	0
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.5"
Enter Red Clear	3.1"
Track Green	27
Track Yellow Change	3.8
Track Red Clear	1.8
Dwell Green	0
Exit Min Green	255"
Exit Yellow Change	25.5"
Exit Red Clear	25.5"
Call Extend Time	1.0
Exit Type	EXIT PHASES
Ped Clear Through Yellow	-
Require All Red Entry	-

\* Directs controller to use default phase timing.

This signal was designed for advanced preemption

**MOTT MACDONALD**  
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930 Main Campus Drive  
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License No. F-0669

Signal Upgrade  
Temporary Design 3 (TMP Phase III)

Prepared for:  
**NC 211/NC 73-211 at NC 73 (South Intersection)**  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS: INIT. DATE

SCALE: 1" = 40'

DOCUMENT NOT CONSIDERED FINAL UNTIL ALL SIGNATURES COMPLETED

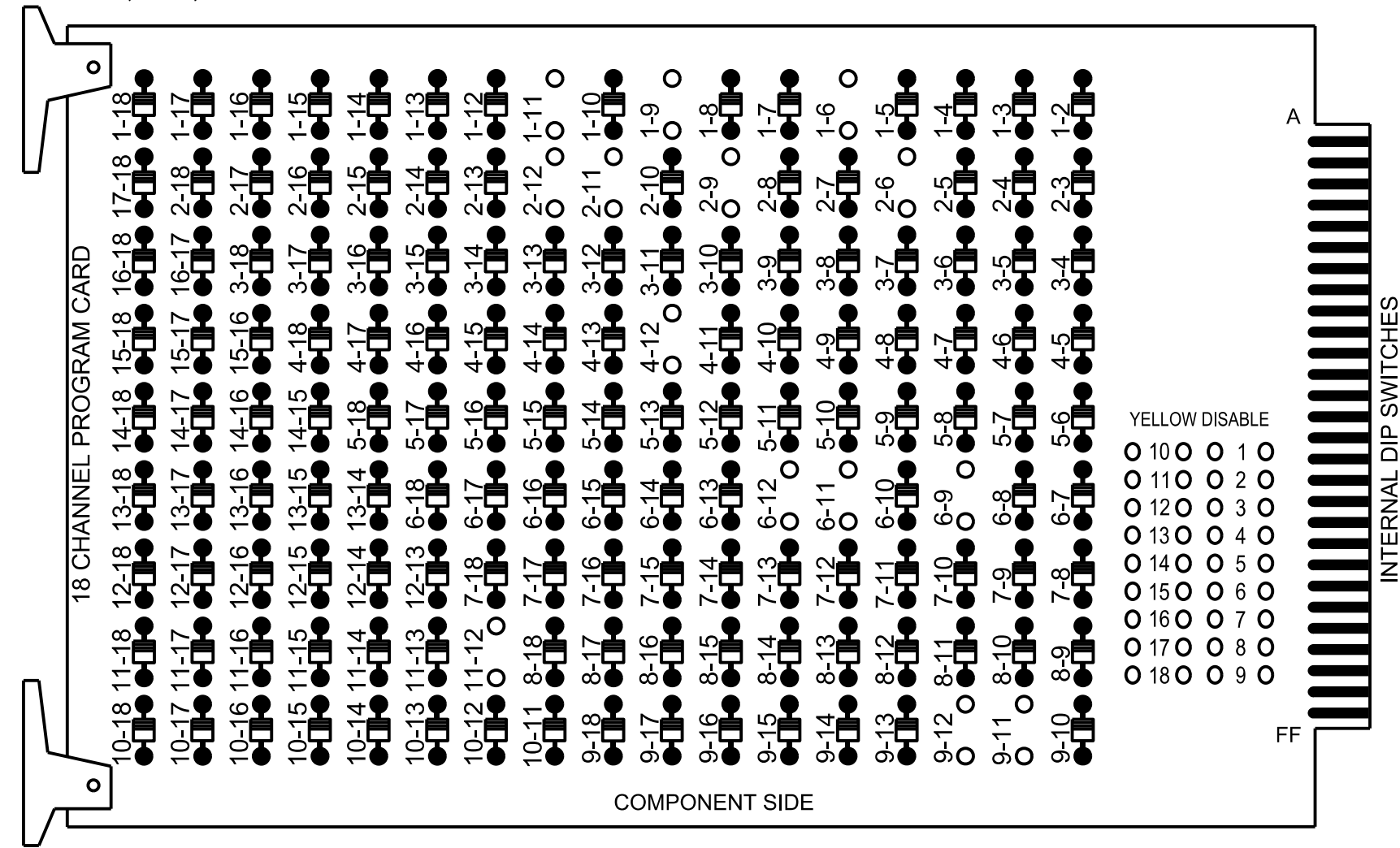
SEAL  
PROFESSIONAL ENGINEER  
SEAL 034437  
LD. D. STOUCHKO  
STATE OF NORTH CAROLINA

SIGNATURE DATE  
SIG. INVENTORY NO. 08-110313

### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

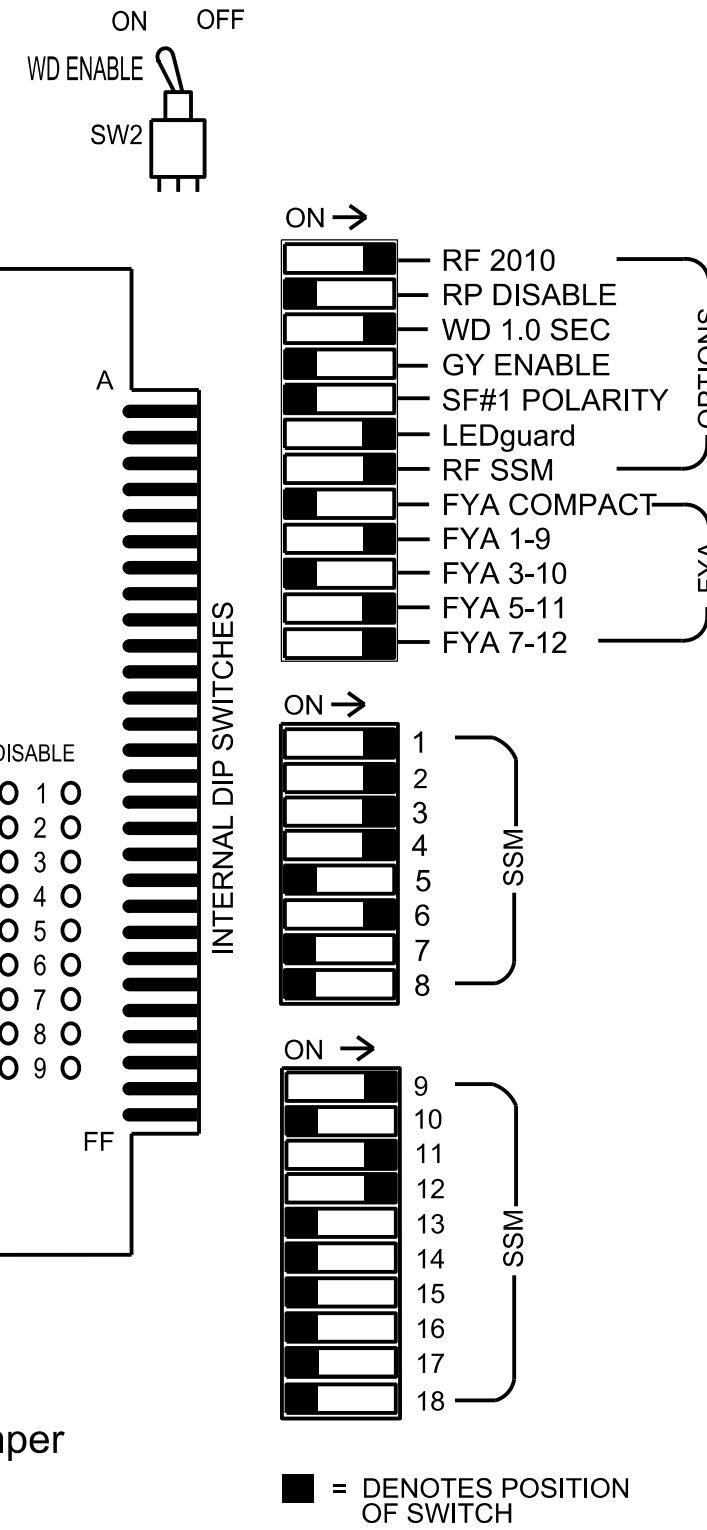
REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-6, 2-9, 2-11, 2-12, 4-12, 6-9, 6-11, 6-12, 9-11, 9-12, and 11-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- Program Phase 39 for No Startup Veh Call.
- Program Phase 40 for Min Recall.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S1, S2, S4, S5, S8, AUX S1, AUX S4, AUX S5  
 Phases Used.....1, 2, 3, 4, 6, 39\*\*, 40\*\*  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....\*

\*See overlap programming detail on sheet 2  
 \*\*Phase used for preemption timing purposes only

### SIGNAL HEAD HOOK-UP CHART

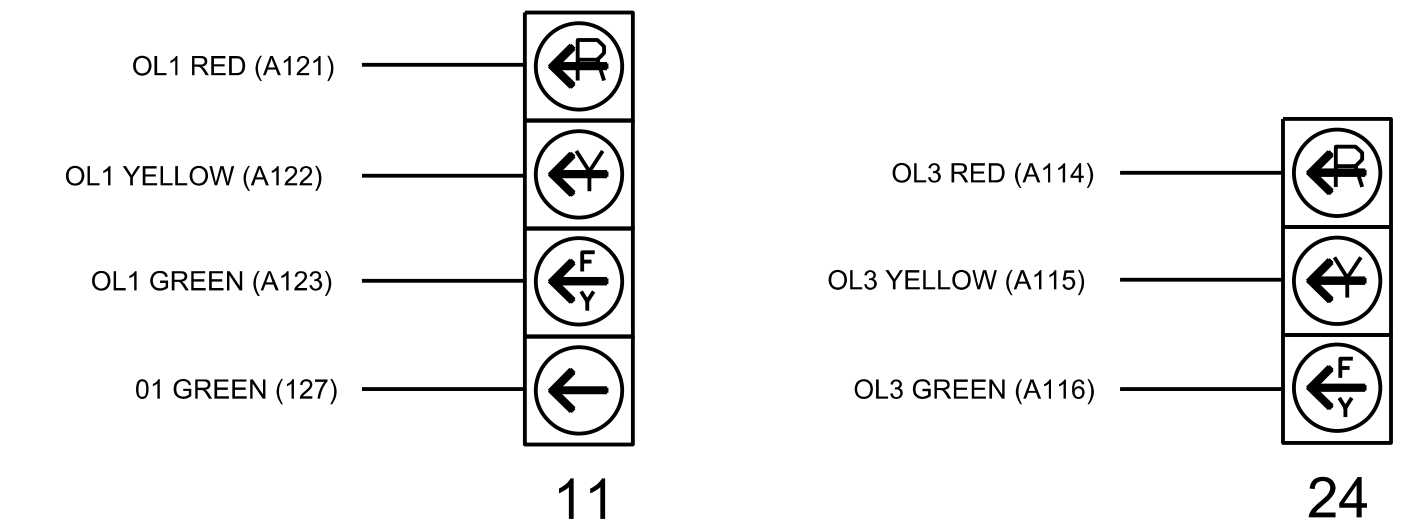
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	42	21,22	31	32	41	42,43	NU	61,62	NU	NU	NU	11	NU	NU	24	23	NU
RED	*	128		116	116	101	101		134									A101
YELLOW		129		117	117	102	102		135									
GREEN		130		118	118	103	103		136									
RED ARROW													A121					A114
YELLOW ARROW		126											A122					A115 A102
FLASHING YELLOW ARROW													A123					A116 A103
GREEN ARROW	127	127		118	103													

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	S	S	S	S	S	S	S	S	S	S	S	S	FS
"	T	T	T	T	T	T	T	T	T	T	T	T	T	DC ISOLATOR
L	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	ST
														DC ISOLATOR
U	S	S	S	S	S	S	S	S	S	S	S	S	S	PRE1
"	T	T	T	T	T	T	T	T	T	T	T	T	T	AC ISOLATOR
L	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

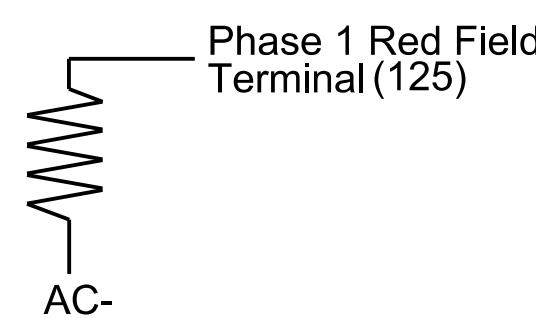
### SPECIAL DETECTOR NOTES

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3  
 Temporary Design 3 (TMP Phase III)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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 License No. F-0669

Prepared for:  
  
 750 N. Greenfield Pkwy, Corner, NC 27529

NC 211/NC 73-211  
 at  
 NC 73 (South Intersection)  
 Division 8 Moore County West End  
 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:  
 REVISIONS INIT. DATE

SEAL  
  
 DATE  
 SIG. INVENTORY NO. 08-1103T3

### OVERLAP PROGRAMMING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps  
Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	6	2,4
Modifier Phases	1	-	-	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

NOTICE PHASE 2 FLASH RED →

NOTICE PHASE 6 FLASH RED →

NOTICE OVERLAP 1 FLASH RED →

NOTICE OVERLAP 3 FLASH RED →

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

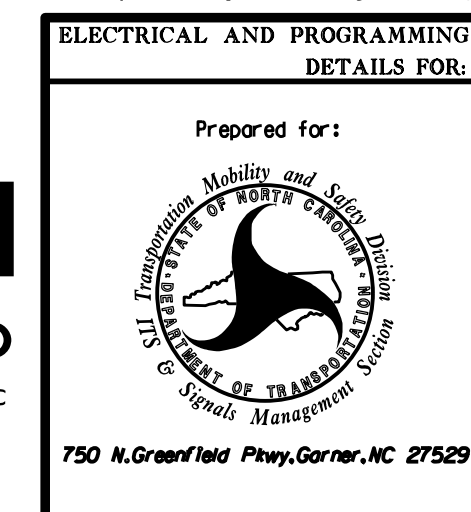
Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103T3  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:



Electrical Detail - Sheet 2 of 3  
Temporary Design 3 (TMP Phase III)



NC 211/NC 73-211 at NC 73 (South Intersection)	
Division 8	Moore County West End
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED





### SEQUENCE DETAIL

Front Panel  
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface  
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

### PREEMPTION PROGRAMMING

Front Panel  
Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

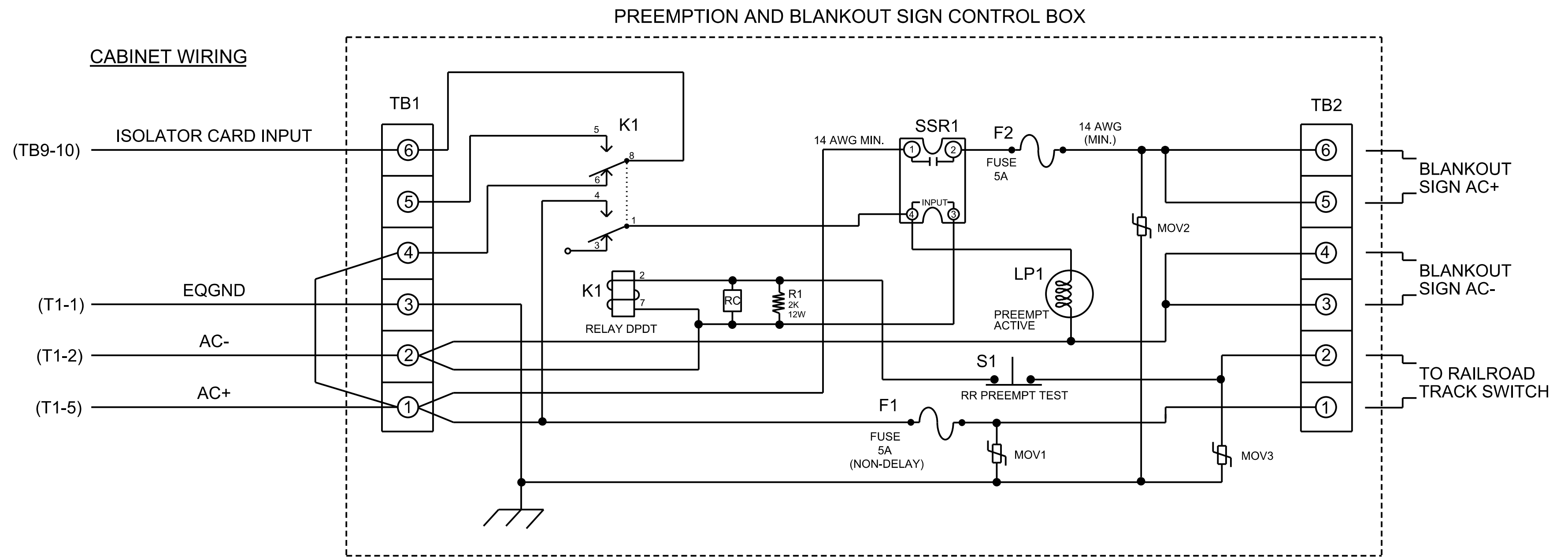
Web Interface  
Home >Controller >Preempt Configuration >Preempts

Preempt Configuration

Preempt	1
Enabled	Enabled
Type	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	4
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.5
Enter Red Clear	3.1
Track Green	27
Track Yellow Clr	3.8
Track Red Clear	1.8
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not OvrD Flash	X
Not OvrD Nxt Pre	-
Require All Red Entry	-
Track Clear OvrD	X
Ped Clear During Yellow	-
Entry Omit OLTG	X
Track Reserve	X

### RAILROAD PREEMPTION WIRING DETAIL

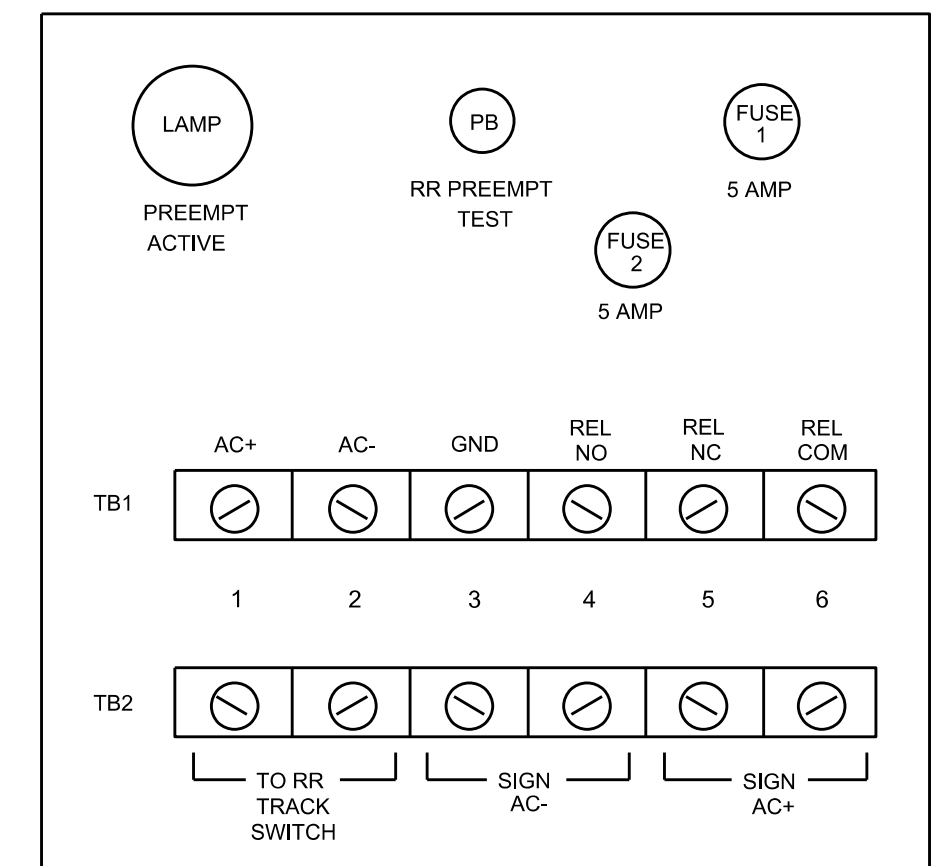
(wire as shown below)



### NOTES

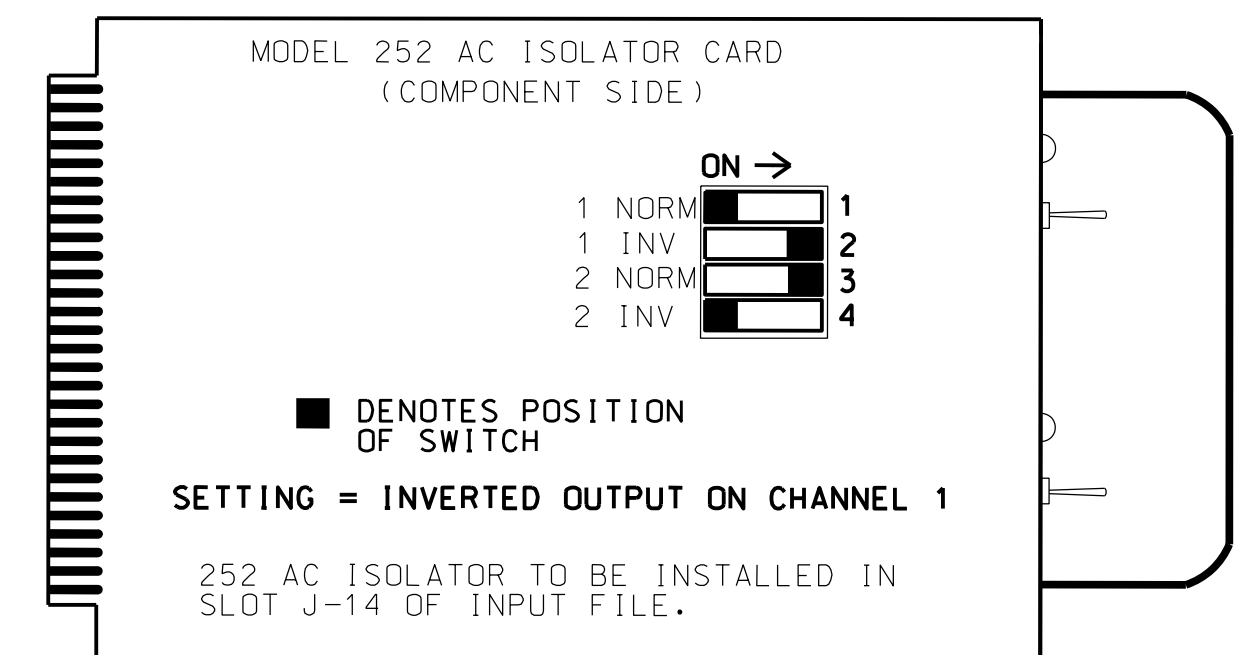
- Relay K1 is shown in the energized (Preempt not active) normal operation state.
- Relay K1 is a DPDT with 120VAC coil with octal base.
- Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- IMPORTANT!!** A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

### FRONT VIEW



### PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103T3  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3  
Temporary Design 3 (TMP Phase III)

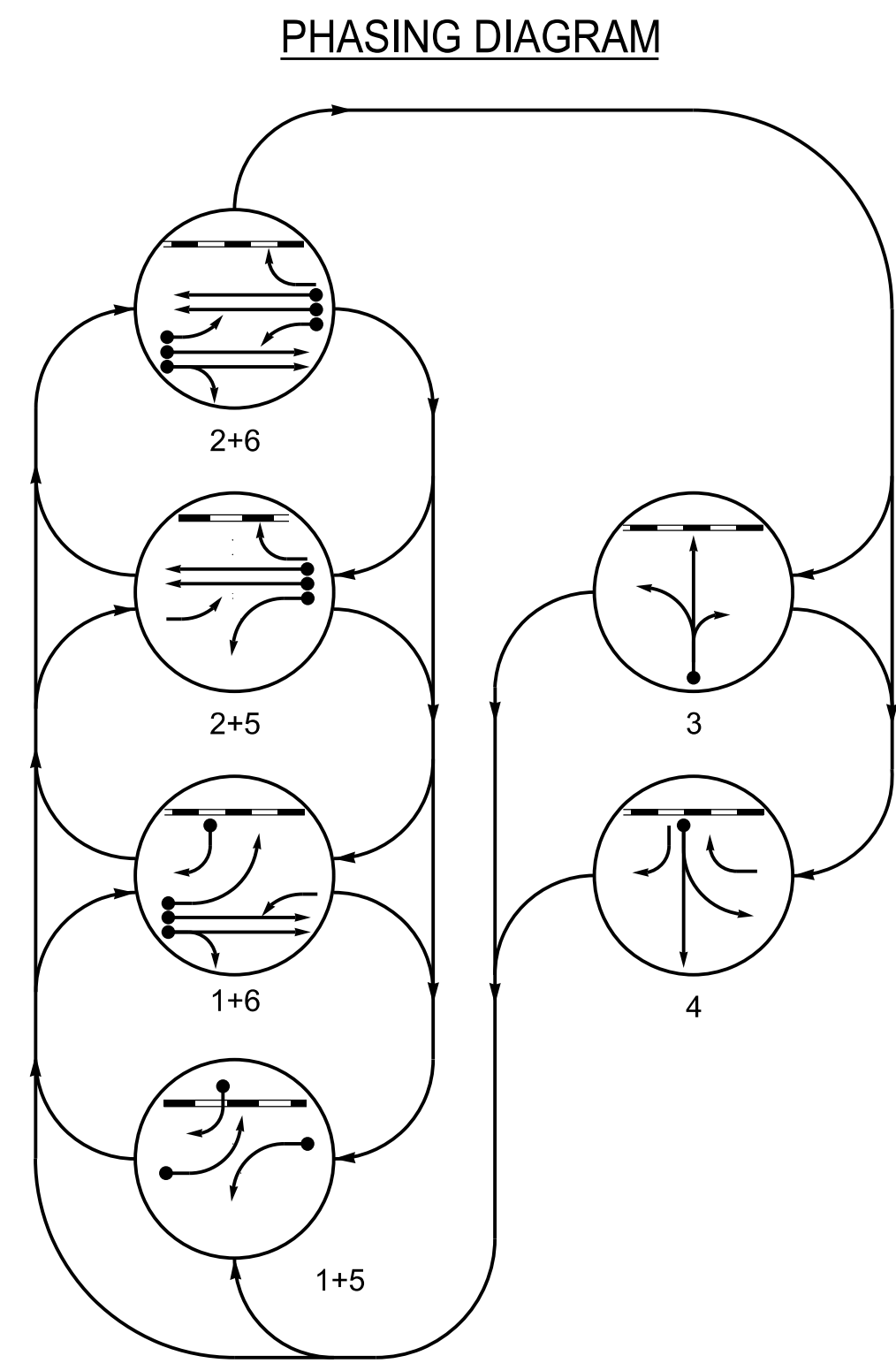
**M M**  
**MOTT MACDONALD**  
MOTT MACDONALD I & E, LLC  
930 Main Campus Drive  
Suite 200  
RALEIGH, NC 27606  
License No. F-0669

ELECTRICAL AND PROGRAMMING DETAILS FOR:  
Prepared for:

NC 211/NC 73-211  
at  
NC 73 (South Intersection)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS INIT. DATE

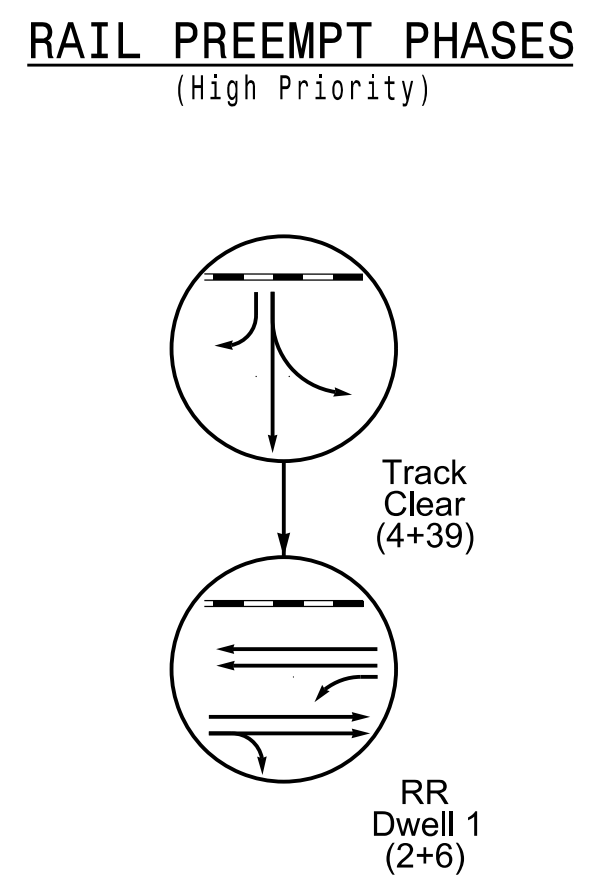
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NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 034437  
LD D. STOUCHKO  
DATE  
SIG. INVENTORY NO. 08-1103T3



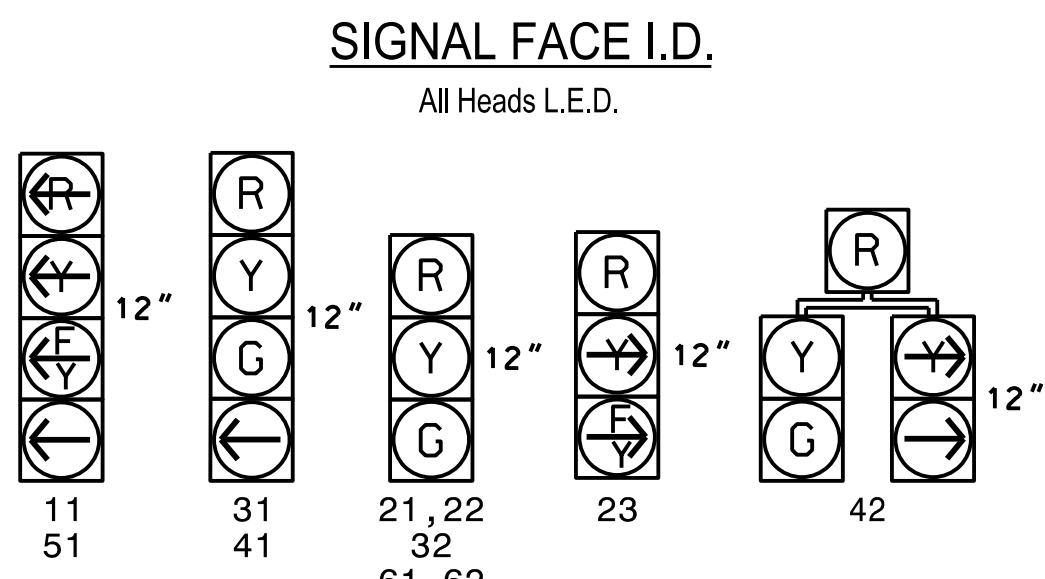
**PHASING DIAGRAM DETECTION LEGEND**

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT



**TABLE OF OPERATION**

SIGNAL FACE	PHASE										
	1+5	1+6	2+5	2+6	3	4	TRAIL	RR Dwell	FLASH	CA	KR
11											
21,22	R	R	G	G	R	R	R	R	G	R	R
23	R	R	F	F	R	R	R	R	R	R	R
31	R	R	R	R	G	R	R	R	R	R	R
32	R	R	R	R	G	R	R	R	R	R	R
41	R	R	R	R	G	R	R	R	R	R	R
42	R	R	R	R	G	R	R	R	R	R	R
51											
61,62	R	G	R	G	R	R	R	G	R	R	R

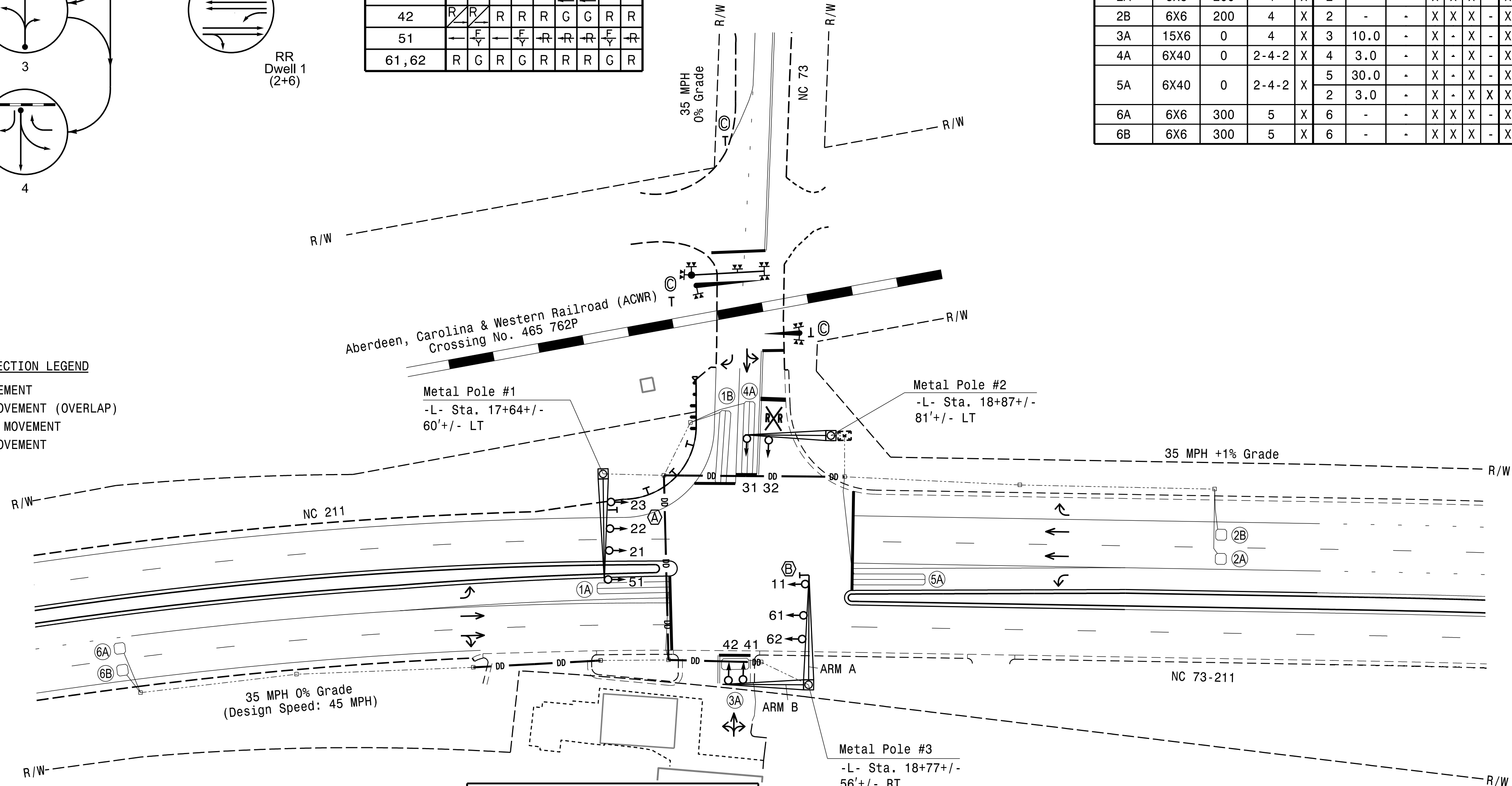


**MAXTIME DETECTOR INSTALLATION CHART**

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	
1A	6X40	0	2-4-2	X	1	15.0	-	X	-	X	-	X
1B	6X40	0	2-4-2	X	1	15.0	-	X	-	X	-	X
2A	6X6	200	4	X	2	-	-	X	X	X	-	X
2B	6X6	200	4	X	2	-	-	X	X	X	-	X
3A	15X6	0	4	X	3	10.0	-	X	-	X	-	X
4A	6X40	0	2-4-2	X	4	3.0	-	X	-	X	-	X
5A	6X40	0	2-4-2	X	5	30.0	-	X	-	X	-	X
6A	6X6	300	5	X	6	-	-	X	X	X	-	X
6B	6X6	300	5	X	6	-	-	X	X	X	-	X

**6 Phase Fully Actuated With Railroad Preemption (Isolated)**

- NOTES**
- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
  - This location contains railroad preemption phasing. Do not program signal for late night flashing operation.
  - Phase 1 and/or Phase 5 may be lagged.
  - The order of phase 3 and phase 4 may be reversed.
  - Set all detector units to presence mode.
  - Program phase 40 to run concurrently with all phases during normal operation. Phase 39 must be incompatible with Phase 40 and included as a track clear phase.

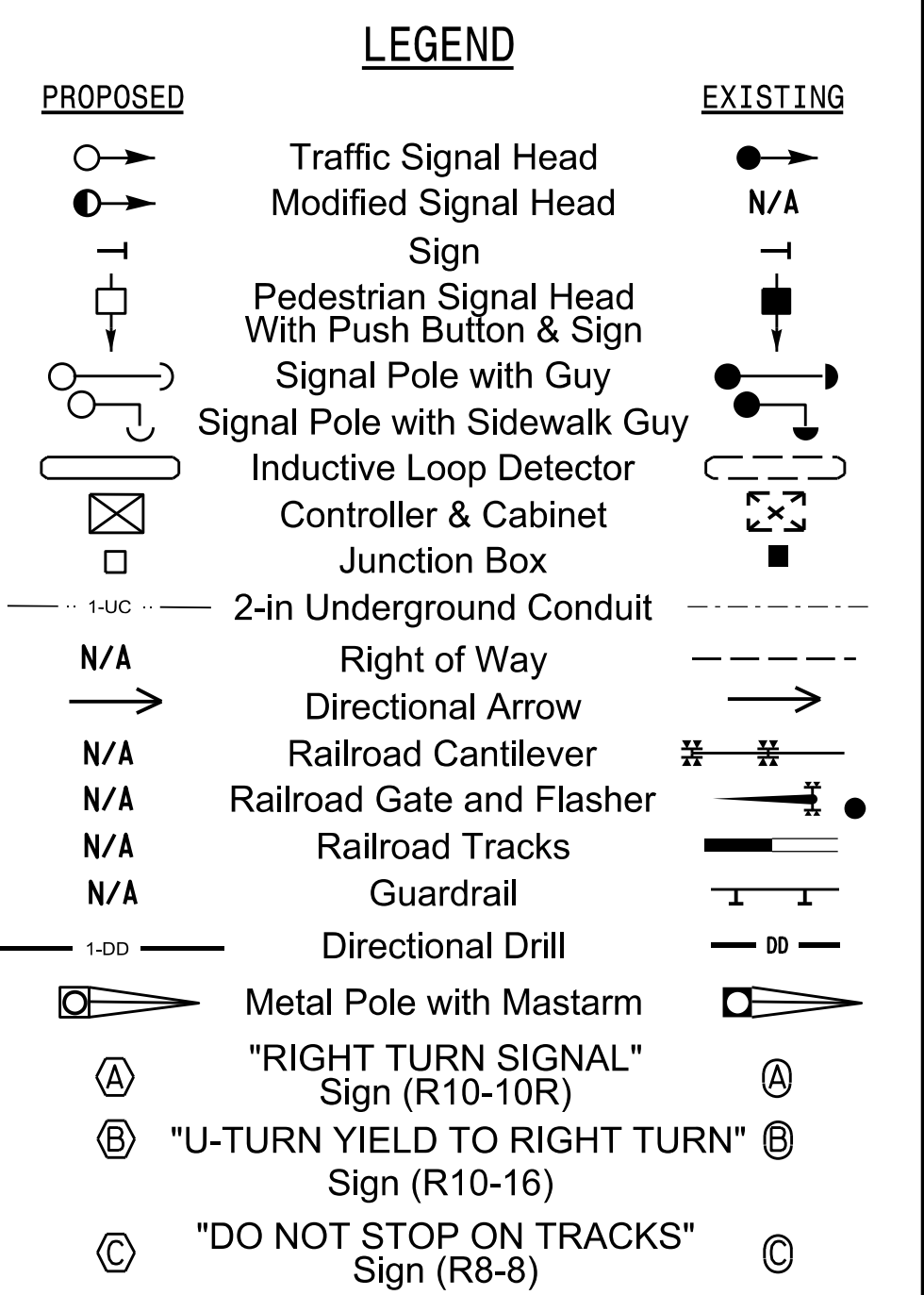


**MAXTIME TIMING CHART**

FEATURE	PHASE							
	1	2	3	4	5	6	39	40
Walk *	-	-	-	-	-	-		
Ped Clear *	-	-	-	-	-	-		
Min Green *	7	10	7	7	7	12		
Passage *	2.0	5.0	2.0	2.0	2.0	6.0		
Max I *	30	75	15	30	15	75		
Yellow Change	3.0	4.5	3.8	3.8	3.0	4.5	3.8	4.5
Red Clear	2.6	2.3	1.0	2.0	3.1	2.3	2.0	3.1
Added Initial *	-	1.5	-	-	-	1.5		
Maximum Initial *	-	24	-	-	-	34		
Time Before Reduction *	-	15	-	-	-	15		
Time To Reduce *	-	34	-	-	-	34		
Minimum Gap	-	3.0	-	-	-	3.0		
Advance Walk	-	-	-	-	-	-		
Non Lock Detector	X	-	X	X	X	-		
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL		MIN RECALL
Dual Entry	-	-	-	-	-	-		

**MAXTIME PREEMPTION CHART**

FUNCTION	PRE 1
Type	RAIL ROAD
Exit Phases	4
Delay	0
Max Presence	0
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.5*
Enter Red Clear	3.1*
Track Green	27
Track Yellow Change	3.8
Track Red Clear	2.0
Dwell Green	0
Exit Min Green	255*
Exit Yellow Change	25.5*
Exit Red Clear	25.5*
Call Extend Time	1.0
Exit Type	EXIT PHASES
Ped Clear Through Yellow	-
Require All Red Entry	-



\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

\* Directs controller to use default phase timing.

This signal was designed for advanced preemption

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Signal Upgrade - Final Design

NC 211/NC 73-211 at NC 73 (South Intersection)

Division 8 Moore County West End

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS

NO.	INIT.	DATE

SCALE: 1" = 40'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL: MOTT MACDONALD I & E, LLC, PROFESSIONAL ENGINEER, LICENSE NO. 034437, LD. D. STOUCHKO

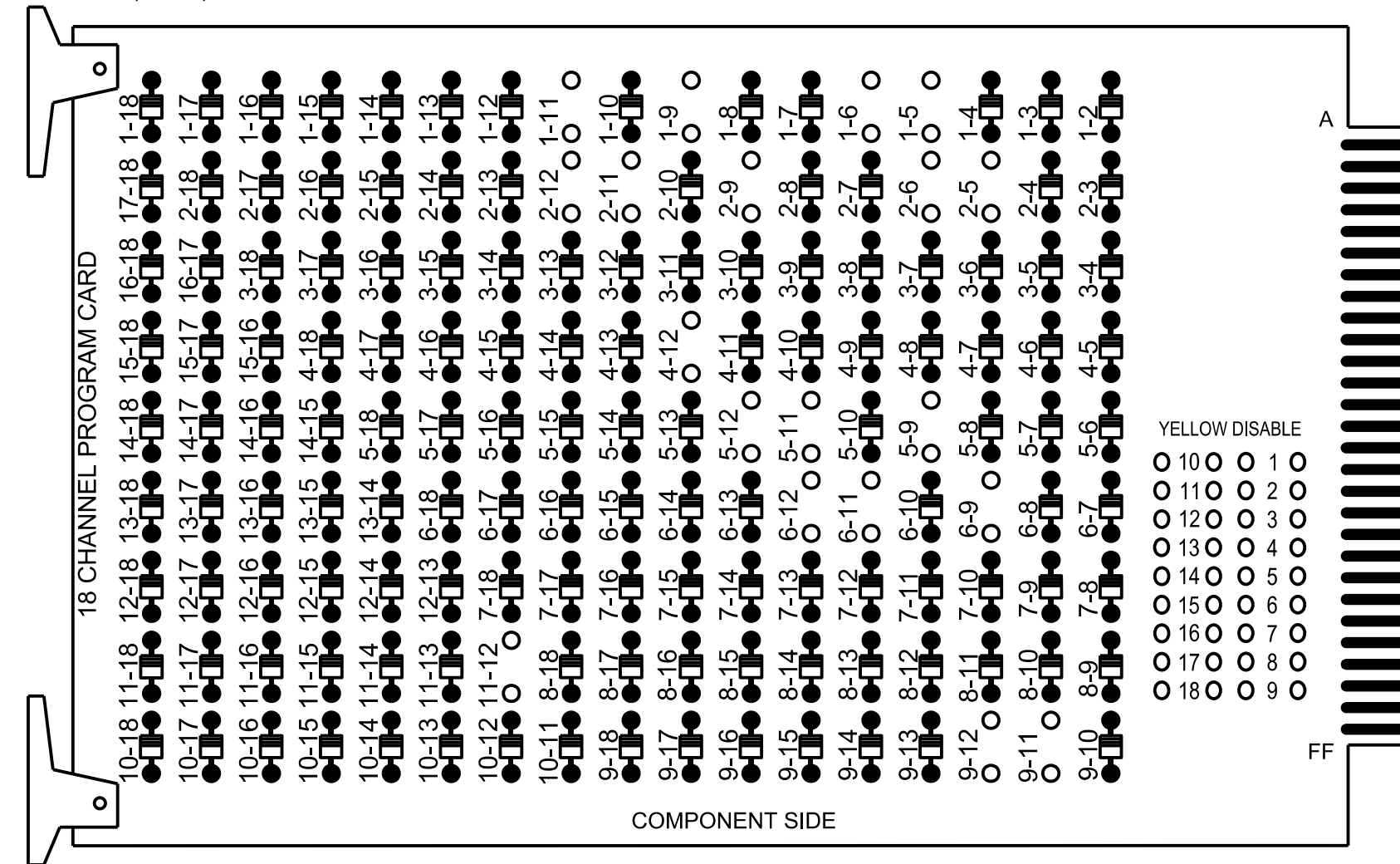
SIGNATURE: \_\_\_\_\_ DATE: \_\_\_\_\_

SIG. INVENTORY NO. 08-1103

### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

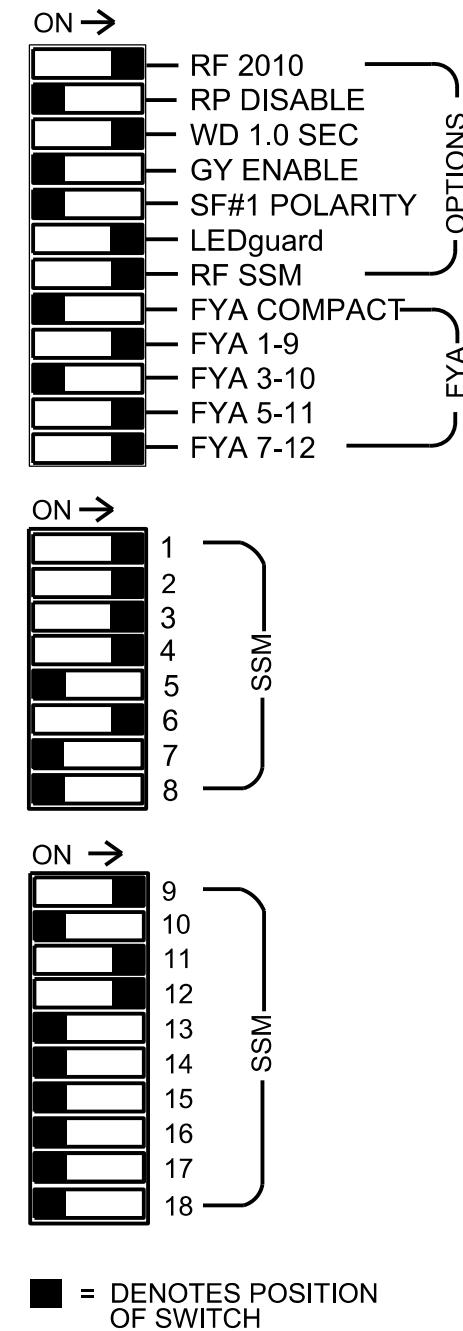
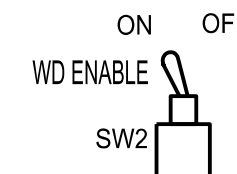
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-12, 4-12, 5-9, 5-11, 5-12, 6-9, 6-11, 6-12, 9-11, 9-12 and 11-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
- Program Phase 39 for No Startup Veh Call.
- Program Phase 40 for Min Recall.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S1, S2, S4, S5, S7, S8, AUX S1, AUX S4, AUX S5  
 Phases Used.....1, 2, 3, 4, 5, 6, 39\*\*, 40\*\*  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....\*

\*See overlap programming detail on sheet 2  
 \*\*Phase used for preemption timing purposes only

### SIGNAL HEAD HOOK-UP CHART

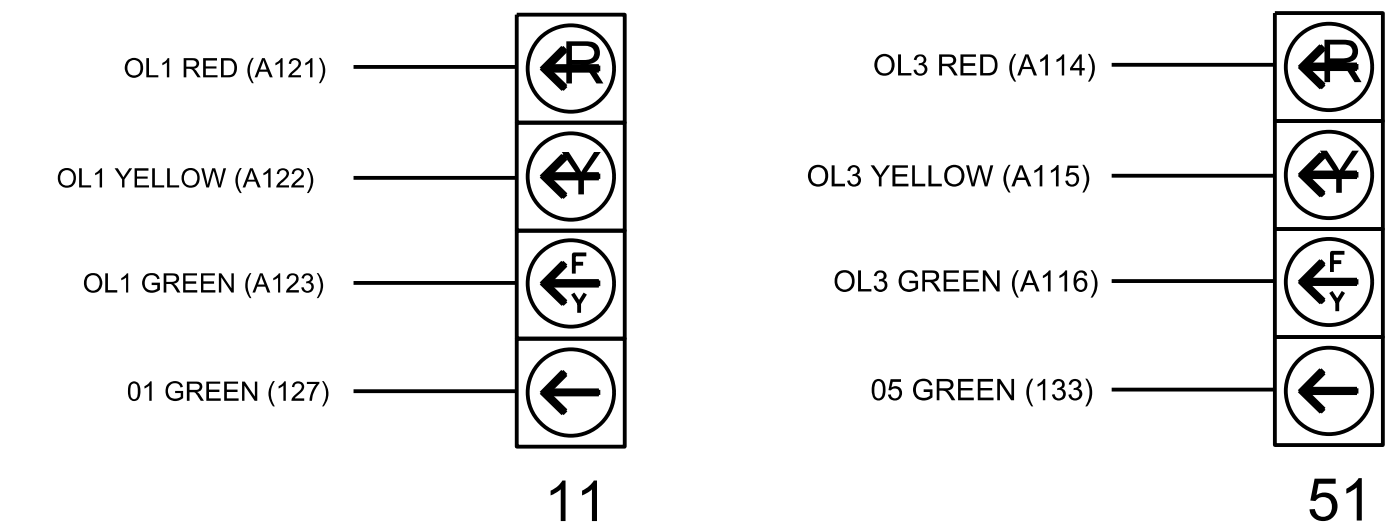
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6					
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18					
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE					
SIGNAL HEAD NO.	11	42	21,22	NU	31	32	41	42	NU	51	61,62	NU	NU	NU	NU	11	NU	NU	51	23	NU		
RED		*	128	116	116	101	101					134									A101		
YELLOW			129	117	117	102	102		*			135											
GREEN			130	118	118	103	103					136											
RED ARROW																					A121	A114	
YELLOW ARROW		126																			A122	A115	A102
FLASHING YELLOW ARROW																					A123	A116	A103
GREEN ARROW	127	127			118	103						133											

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)

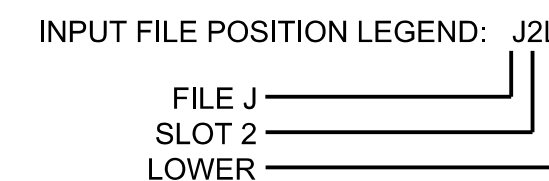
FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	∅ 1 1A	∅ 2 2A	NOT USED	∅ 3 3A	∅ 4 4A									FS DC ISOLATOR
	NOT USED	∅ 2 2B	∅ 1 1B	NOT USED	NOT USED									DC ISOLATOR
"J"	∅ 5 5A	∅ 6 6A												PRE1 AC ISOLATOR
	NOT USED	∅ 6 6B												NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

### INPUT FILE CONNECTION & PROGRAMMING CHART

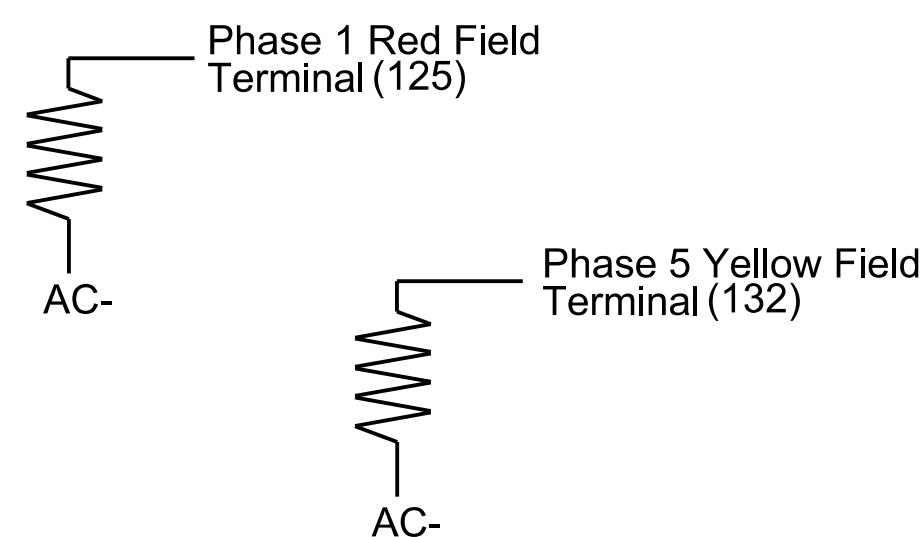
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	15.0		X		X	
1B	TB2-11,12	I3L	76	42	5	1	15.0		X		X	X
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
3A	TB4-5,6	I5U	58	20	7	3	10.0		X		X	
4A	TB4-9,10	I6U	41	3	8	4	3.0		X		X	
5A	TB3-1,2	J1U	55	17	15	5	30.0		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	X
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103  
 DESIGNED: June 2024  
 SEALED: 7/11/2024  
 REVISED:

**M M**  
**MOTT MACDONALD**  
 MOTT MACDONALD I & E, LLC  
 930 Main Campus Drive  
 Suite 200  
 RALEIGH, NC 27606  
 License No. F-0669

Electrical and Programming Details For:  
 Prepared for:  
  
 750 N. Greenfield Pkwy, Corner, NC 27529

NC 211/NC 73-211 at NC 73 (South Intersection)  
 Division 8 Moore County West End  
 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:  
 REVISIONS: INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED  
 SEAL  
  
 DATE  
 SIG. INVENTORY NO. 08-1103



### OVERLAP PROGRAMMING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps  
Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	6	2,4
Modifier Phases	1	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

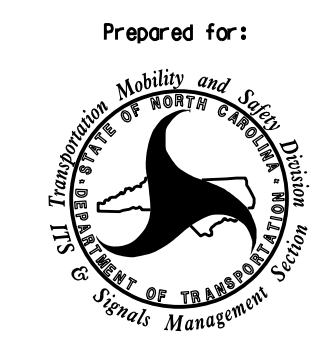
#### Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		X	X	1
NOTICE PHASE 2 FLASH RED →	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
NOTICE PHASE 6 FLASH RED →	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

**M M**  
**MOTT MACDONALD**  
MOTT MACDONALD I & E, LLC  
930 Main Campus Drive  
Suite 200  
RALEIGH, NC 27606  
License No. F-0669

Electrical Detail - Sheet 2 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:  
Prepared for:  
  
750 N. Greenfield Pkwy, Corner, NC 27529

NC 211/NC 73-211  
at  
NC 73 (South Intersection)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 034437  
LD D. STOUCHEK  
SIG. INVENTORY NO. 08-1103

**SEQUENCE DETAIL**

Front Panel  
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface  
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	39,c,40,d

**PREEMPTION PROGRAMMING**

Front Panel  
Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

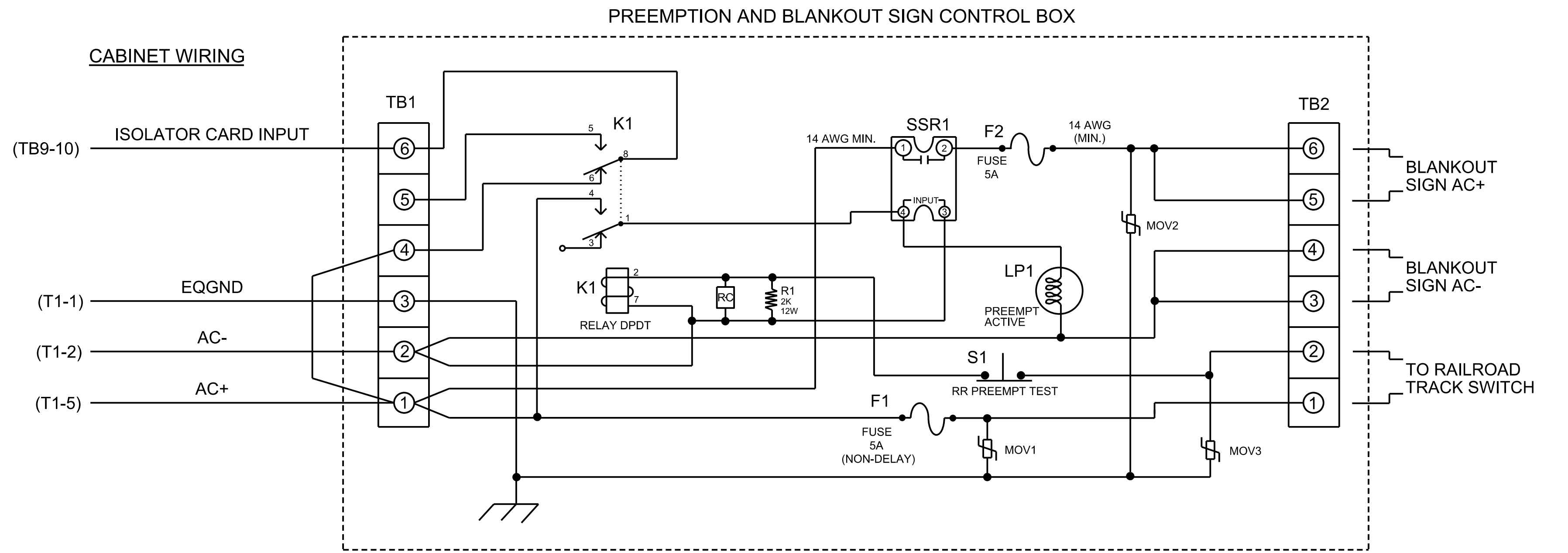
Web Interface  
Home >Controller >Preempt Configuration >Preempts

**Preempt Configuration**

Preempt	1
Enabled	Enabled
Type	Rail Road
Track Phases	4,39
Track Overlaps	-
Dwell Phases	2,6
Dwell Peds	-
Dwell Overlaps	3
Cycling Phases	-
Cycling Peds	-
Cycling Overlaps	-
Exit Phases	4
Exit Overlaps	4
Delay	0
Call Ext Time	1.0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	0
Enter Ped Clear	0
Enter Yellow Change	4.5
Enter Red Clear	3.1
Track Green	27
Track Yellow Clr	3.8
Track Red Clear	2.0
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	-
Not Ovrdr Flash	X
Not Ovrdr Nxt Pre	-
Require All Red Entry	-
Track Clear Ovrdr	X
Ped Clear During Yellow	-
Entry Omit OLTG	X
Track Reserve	X

**RAILROAD PREEMPTION WIRING DETAIL**

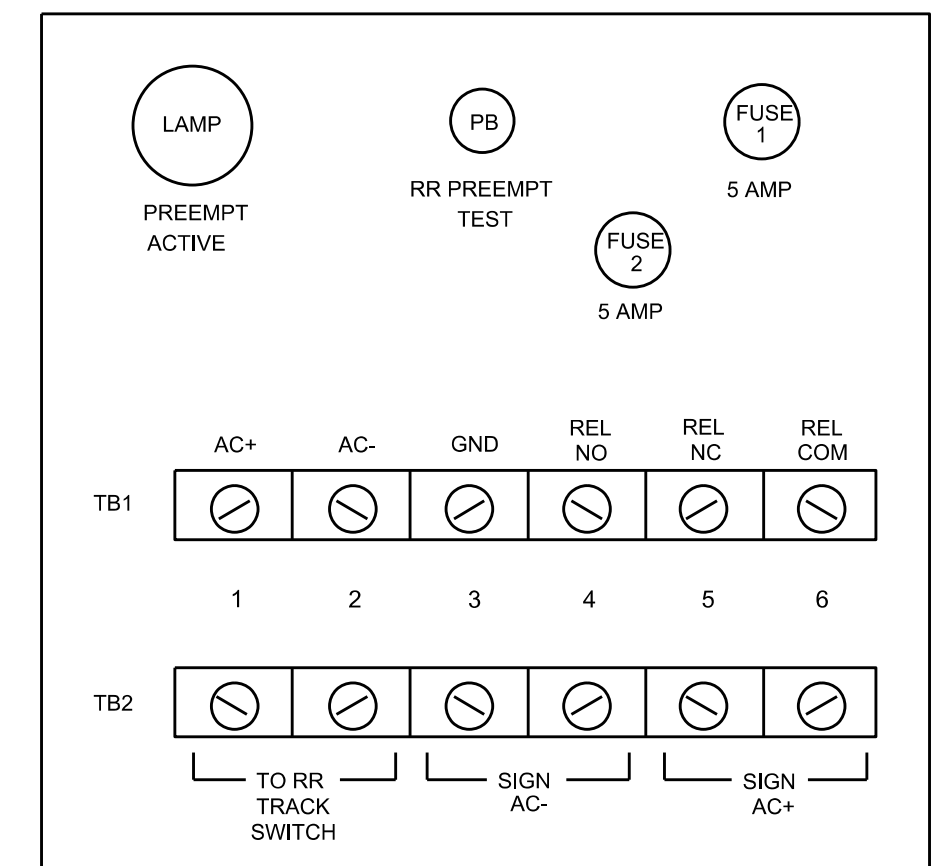
(wire as shown below)



**NOTES**

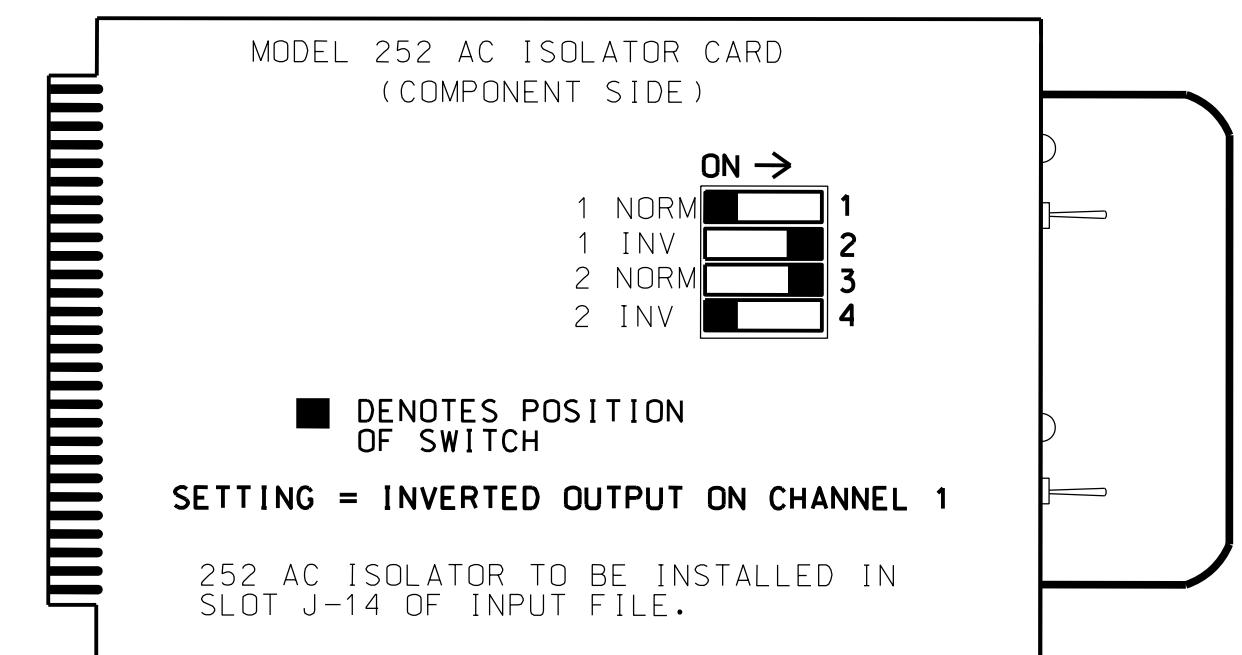
- Relay K1 is shown in the energized (Preempt not active) normal operation state.
- Relay K1 is a DPDT with 120VAC coil with octal base.
- Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- IMPORTANT!!** A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

**FRONT VIEW**



**PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL**

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3

Prepared for: **NC 211/NC 73-211 at NC 73 (South Intersection)**

Division 8 Moore County West End

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS	INIT.	DATE

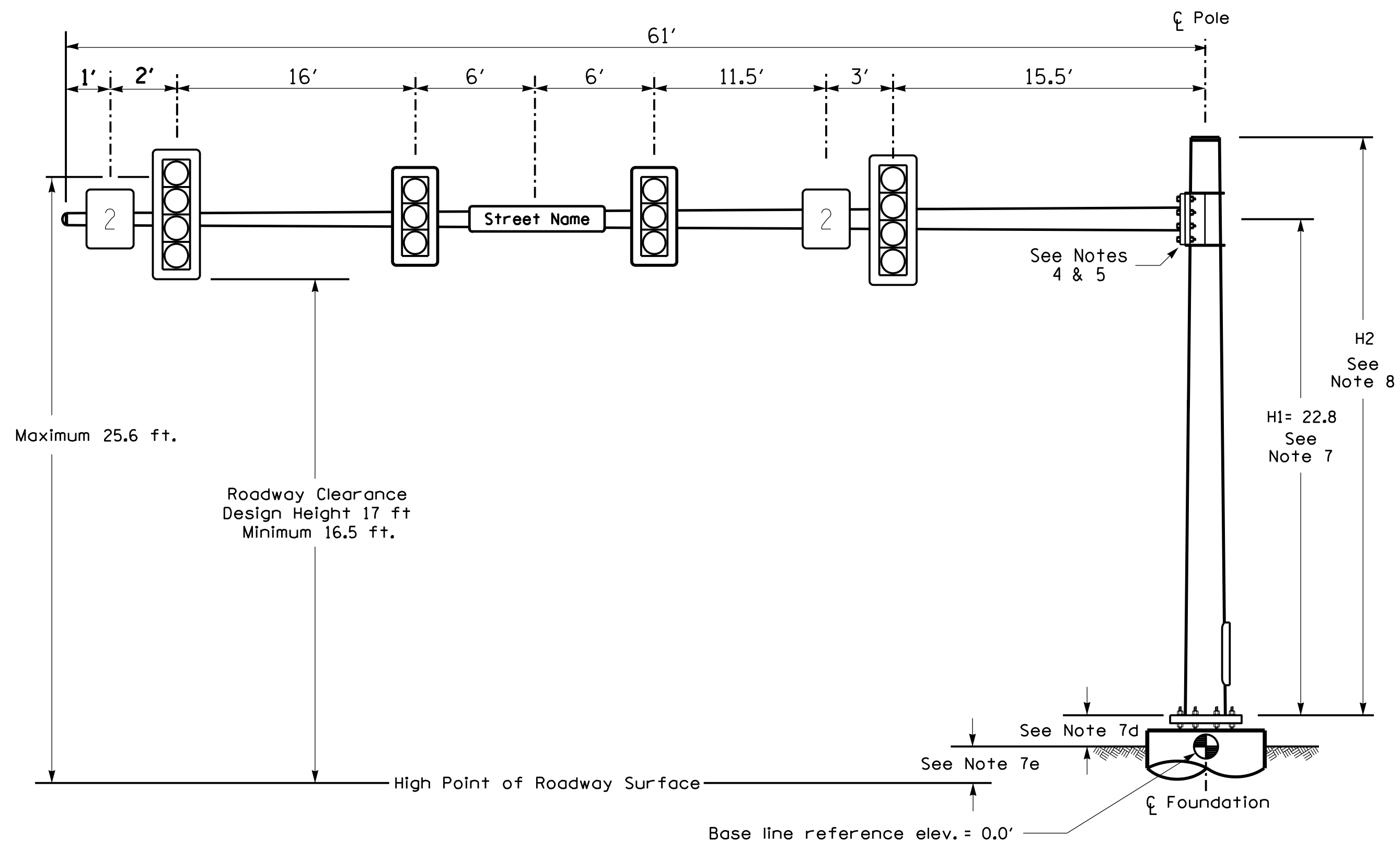
SEAL: NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 034437 LOR D. STOUCHKO

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-1103

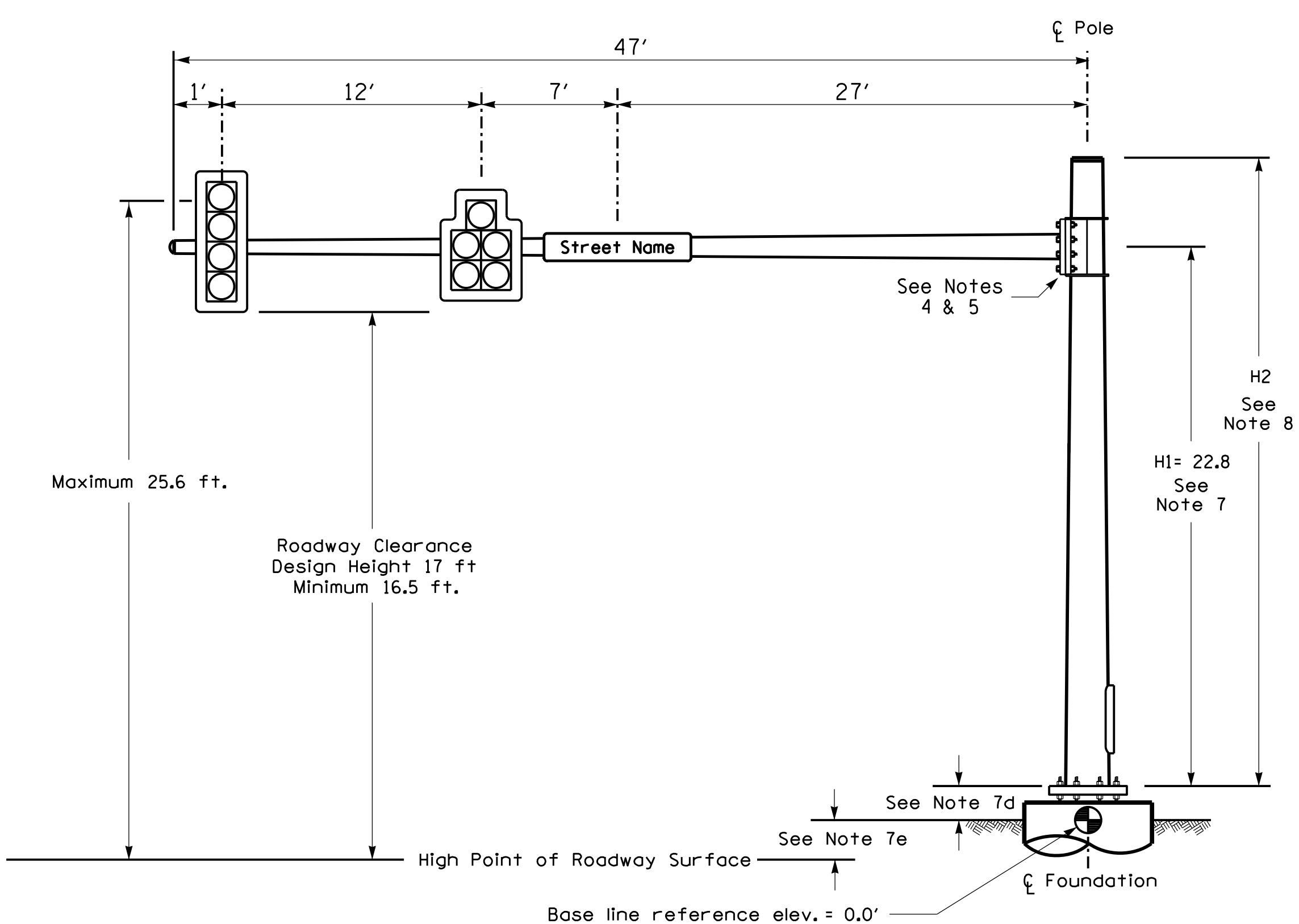
**MOTT MACDONALD**  
MOTT MACDONALD I & E, LLC  
930 Main Campus Drive  
Suite 200  
RALEIGH, NC 27606  
License No. F-0669

Design Loading for METAL POLE NO. 1



Elevation View @ 270°

Design Loading for METAL POLE NO. 2

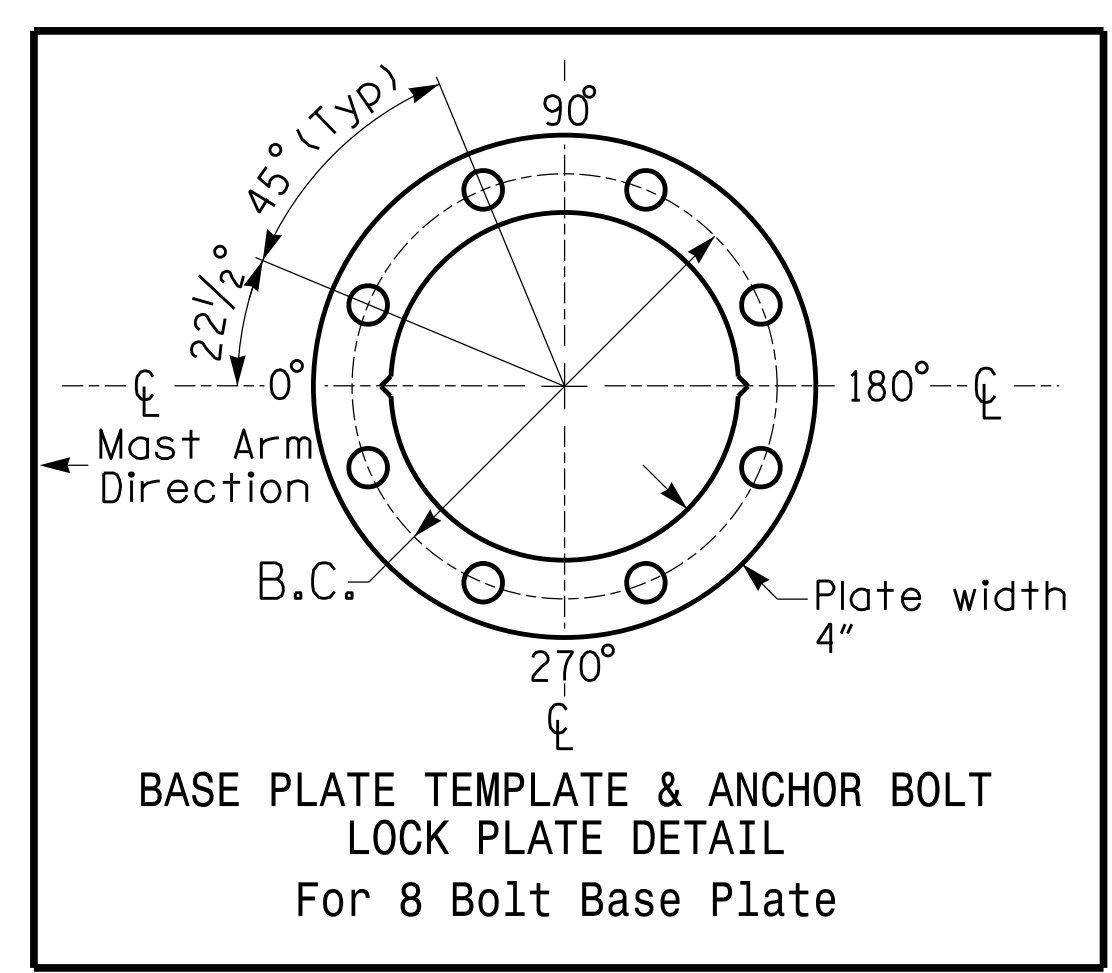
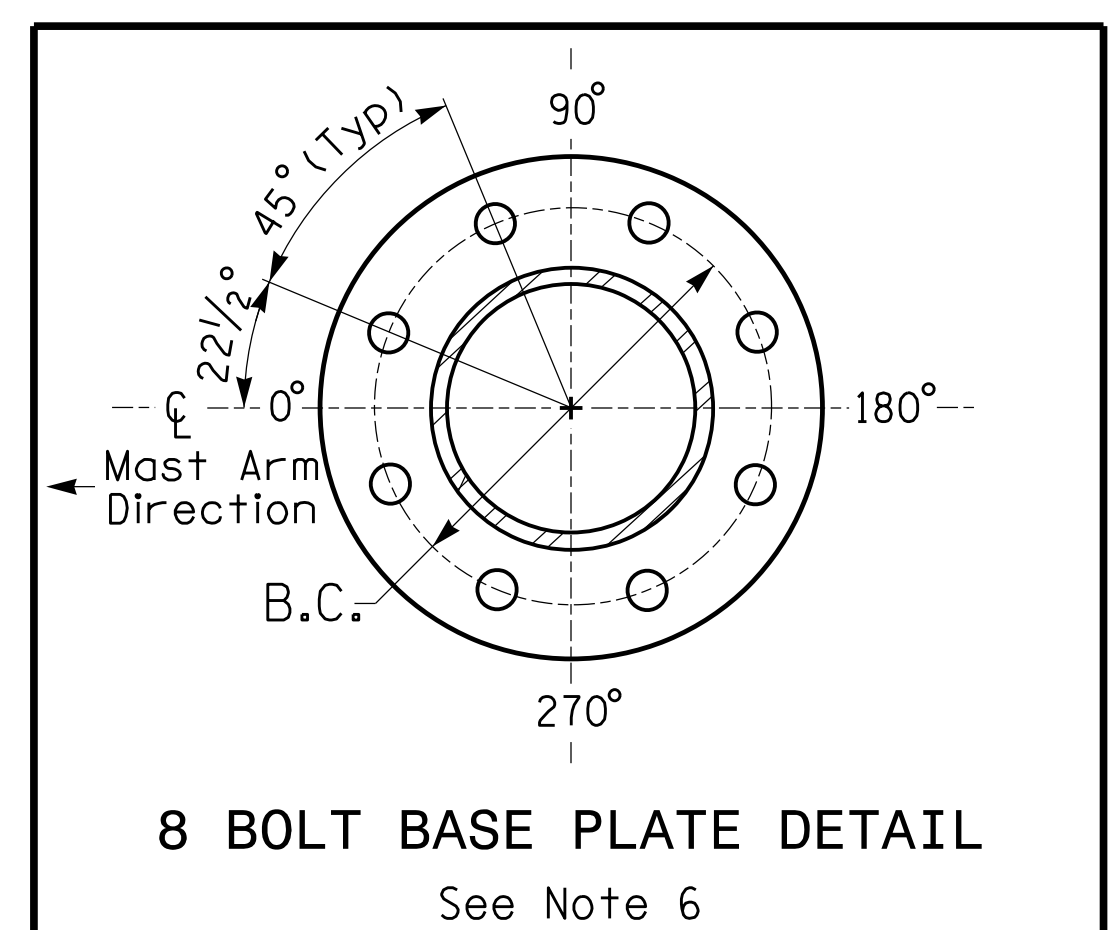
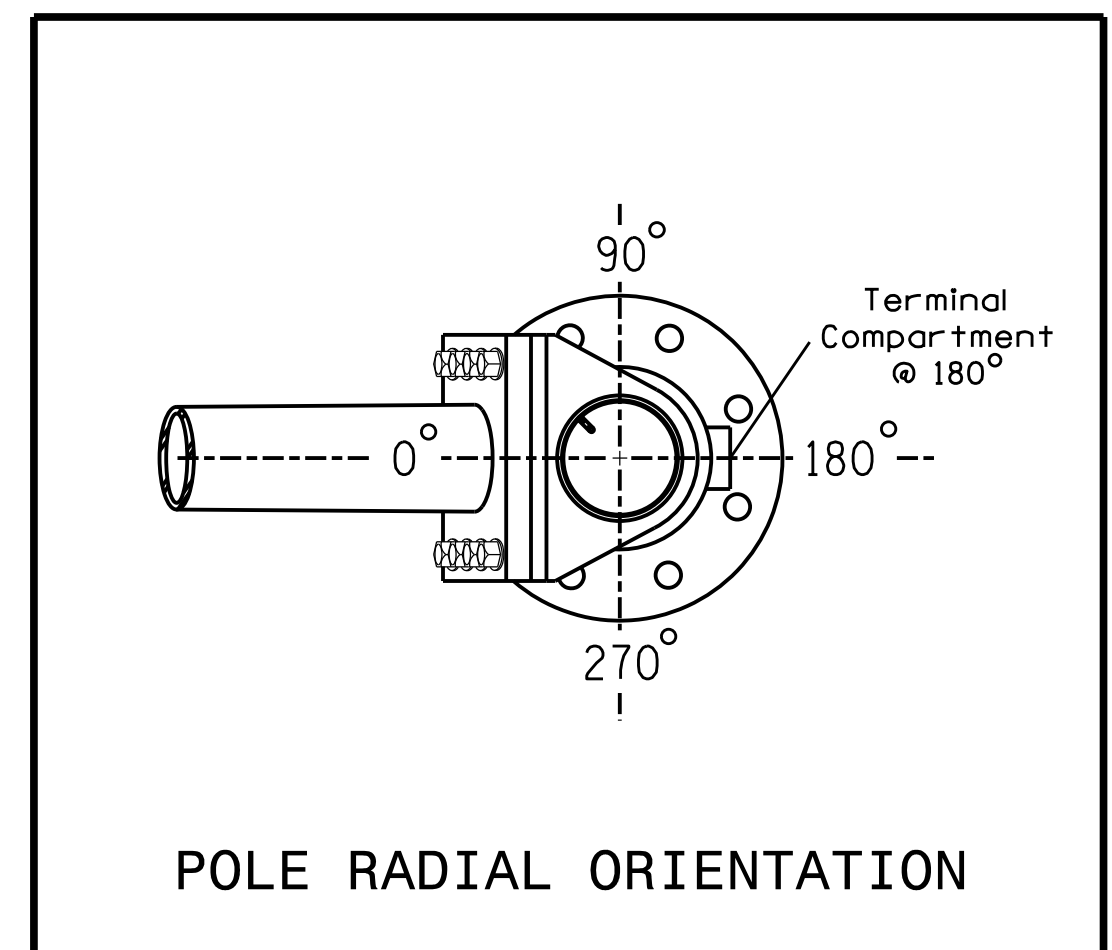


Elevation View

**SPECIAL NOTE**  
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

**Elevation Data for Mast Arm Attachment (H1)**

Elevation Differences for:	Pole 1	Pole 2
Baseline reference point at $\phi$ Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+3.7 ft.	-2.1 ft.
Elevation difference at Edge of travelway or face of curb	NA	-1.7 ft.



METAL POLE No. 1 and 2

PROJECT REFERENCE NO.	SHEET NO.
R-5726	Sig. 5.4

**MAST ARM LOADING SCHEDULE**

LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3 S.F.	42.0" W X 56.0" L	103 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5" W X 66.0" L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5" W X 52.5" L	60 LBS
	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0" L	14 LBS
	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0" W X 96.0" L	36 LBS

**MOTT MACDONALD**  
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NOTES

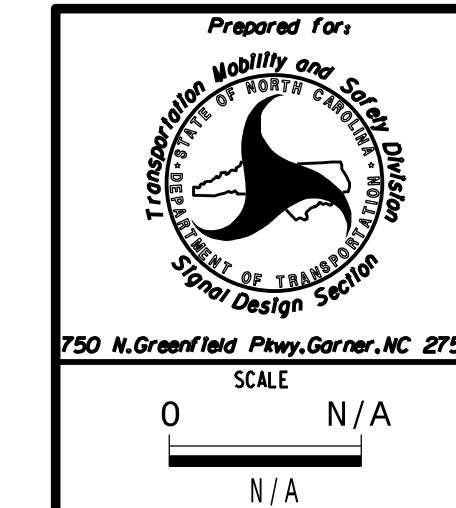
DESIGN REFERENCE MATERIAL

- Design the traffic signal structure and foundation in accordance with:
  - The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
  - The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
  - The 2018 NCDOT Roadway Standard Drawings.
  - The traffic signal project plans and special provisions.
  - The NCDOT "Metal Pole Standards" located at the following NCDOT website: <https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

DESIGN REQUIREMENTS

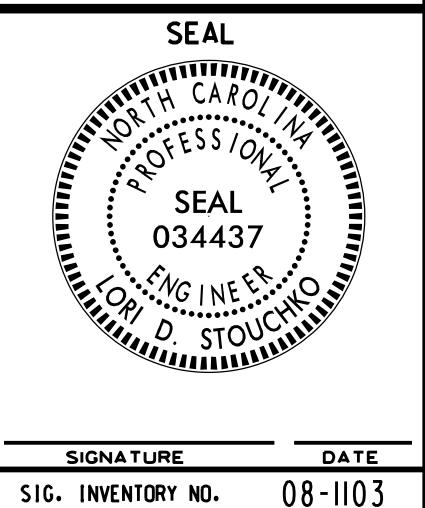
- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- Design all signal supports using stress ratios that do not exceed 0.9.
- The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- The mast arm attachment height (H1) shown is based on the following design assumptions:
  - Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
  - Signal heads are rigidly mounted and vertically centered on the mast arm.
  - The roadway clearance height for design is as shown in the elevation views.
  - The top of the pole base plate is 0.75 feet above the ground elevation.
  - Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
  - Mast arm attachment height (H1) plus 2 feet, or
  - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.

NCDOT Wind Zone 4 (90 mph)

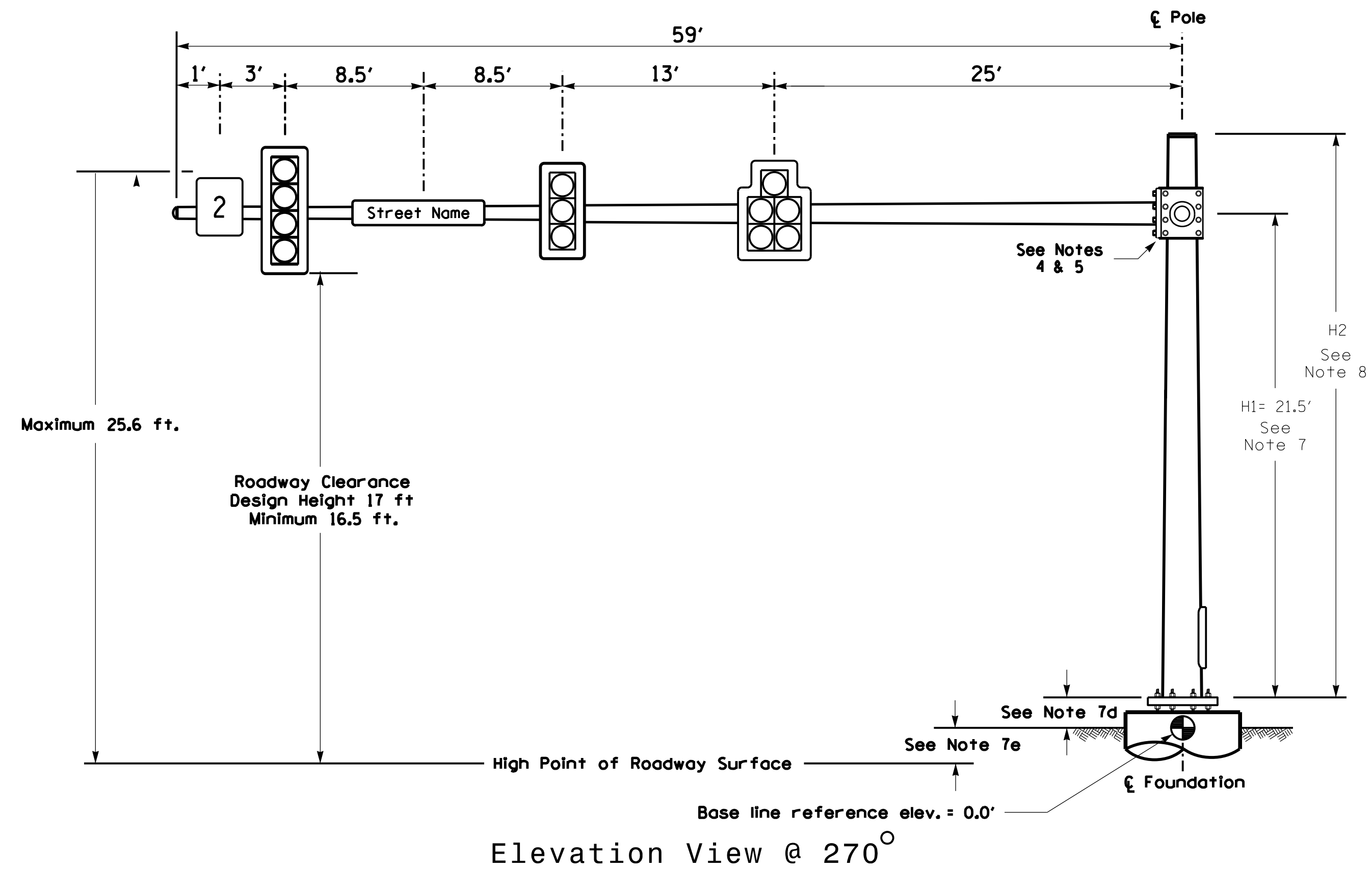


Prepared for: N.C. Department of Transportation	
NC 211/NC 73-211 at NC 73 (South Intersection)	
Division 8	Moore County
West End	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
SCALE: 0 N/A	SCALE: N/A
REVISIONS	INIT. DATE
SIGNATURE	DATE
SIG. INVENTORY NO. 08-1103	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

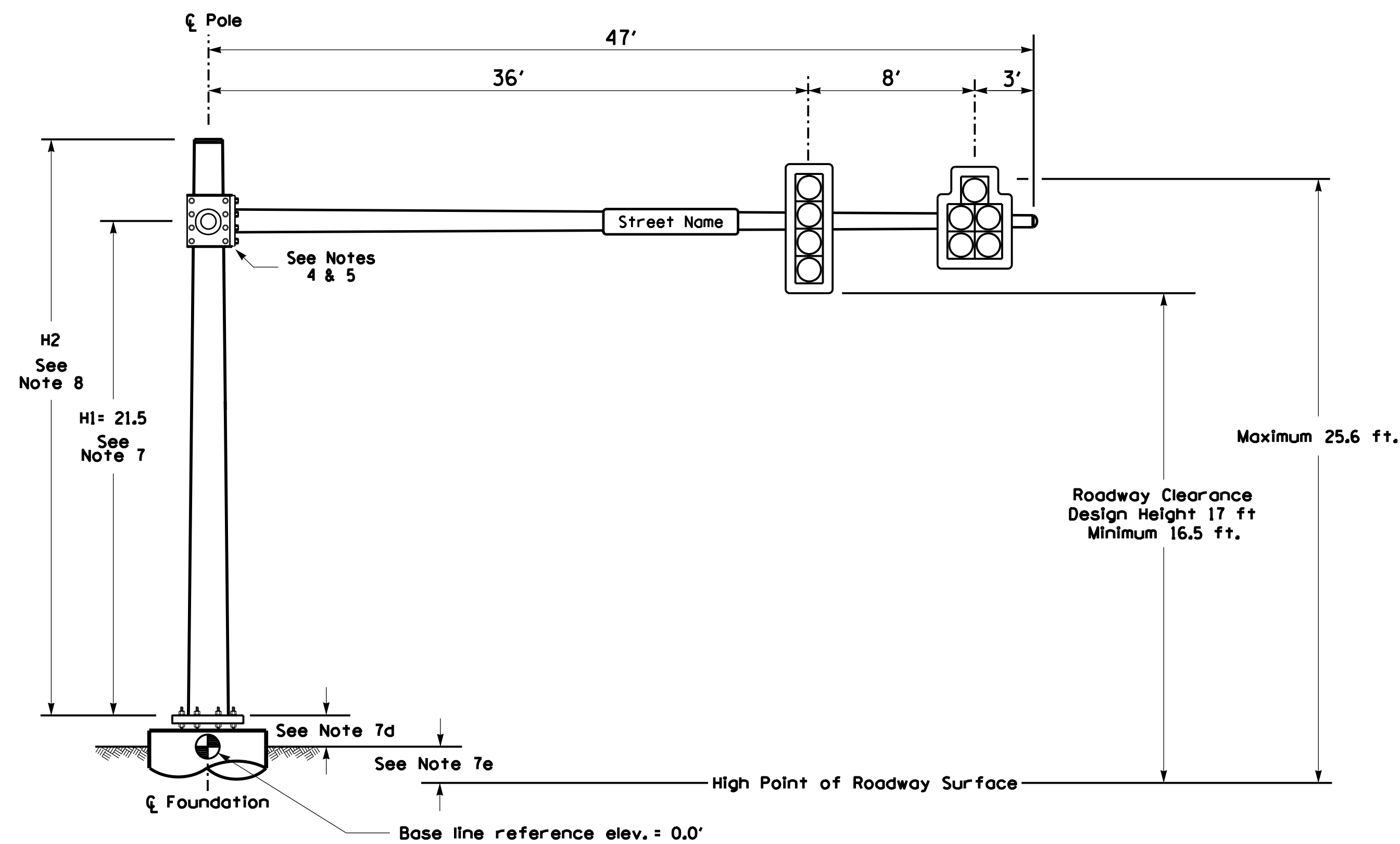


Design Loading for METAL POLE NO. 3, MAST ARM A



Elevation View @ 270°

Design Loading for METAL POLE NO. 3, MAST ARM B



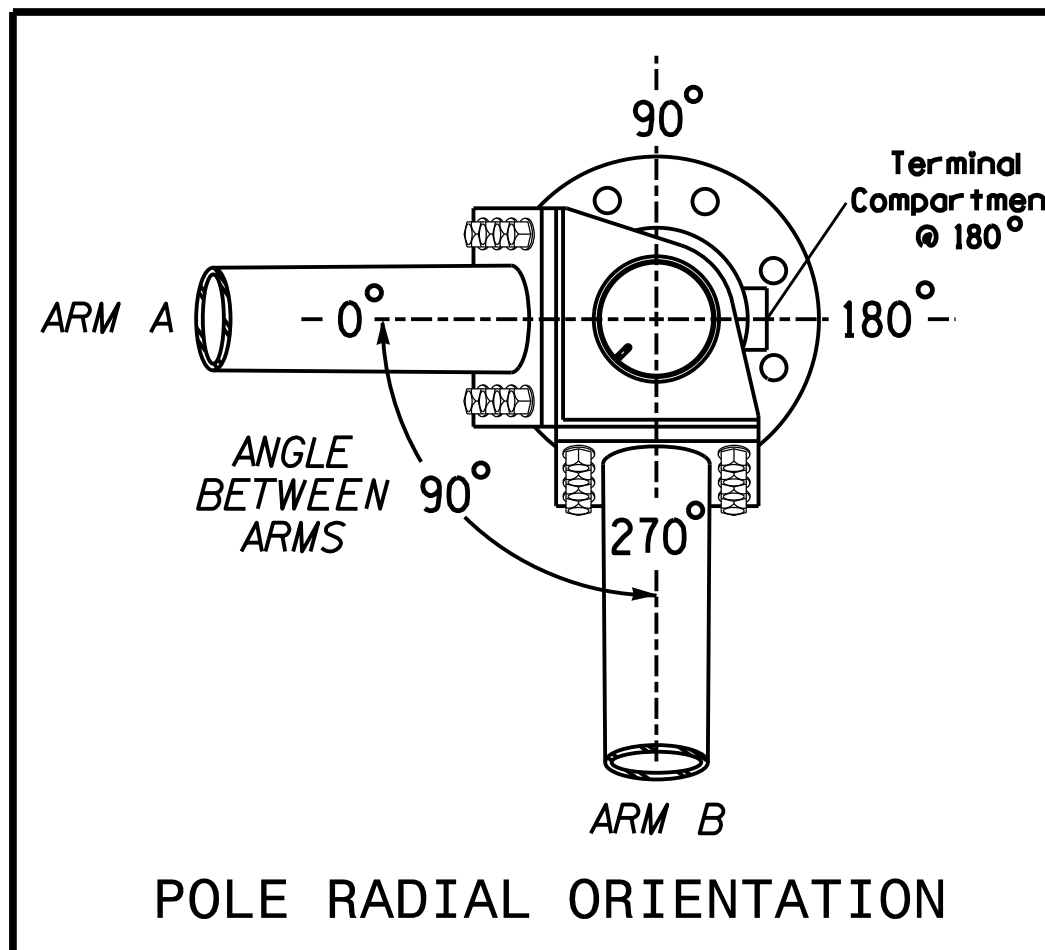
Elevation View @ 0°

SPECIAL NOTE

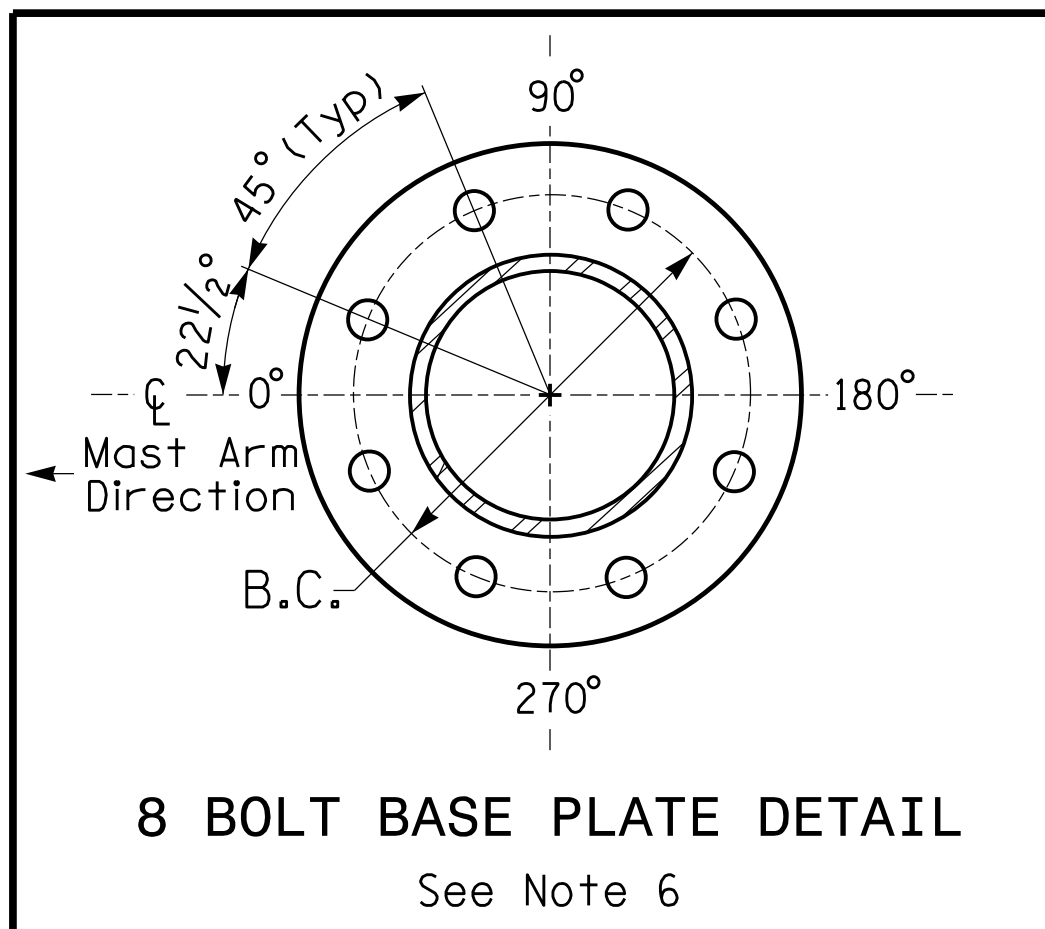
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:		Pole 3
Foundation @ ground level	☉	0.0 ft.
Elevation difference at High point of roadway surface		-2.5 ft.
Elevation difference at Edge of travelway or face of curb		+/-0.0 ft.

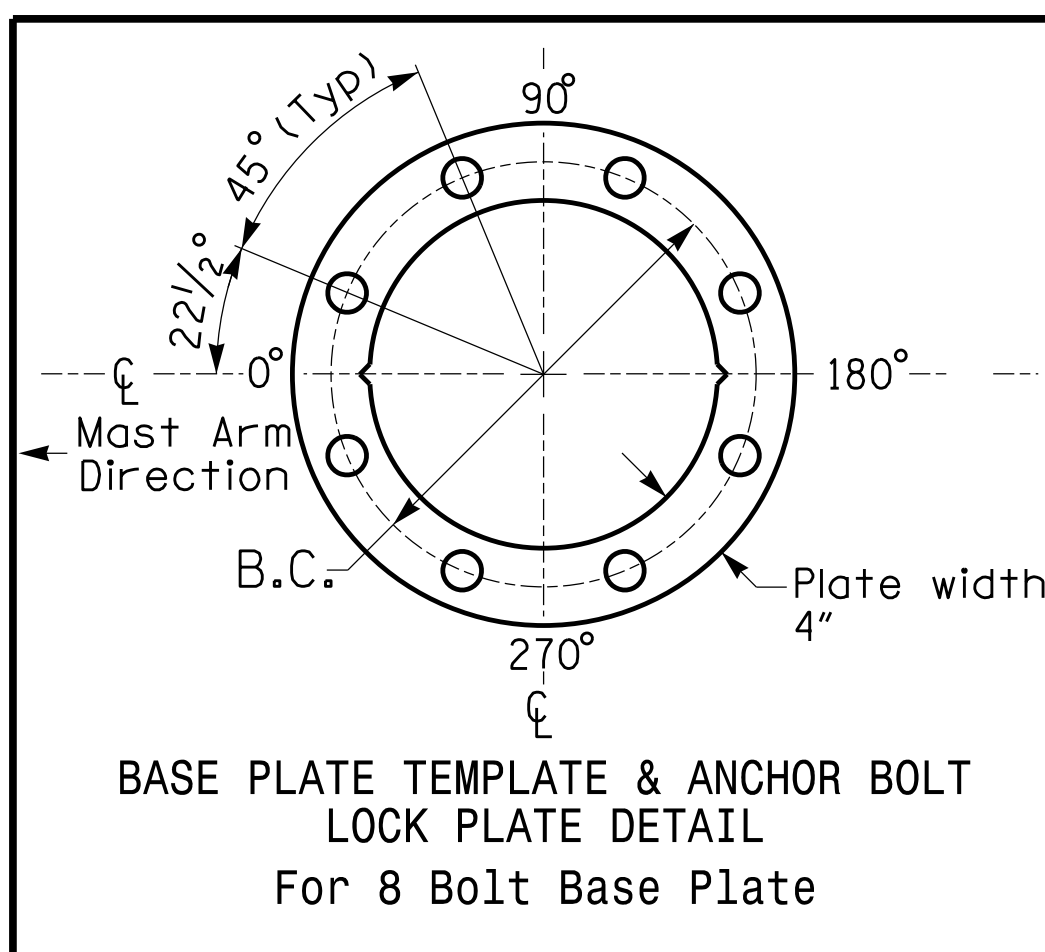


POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL

See Note 6



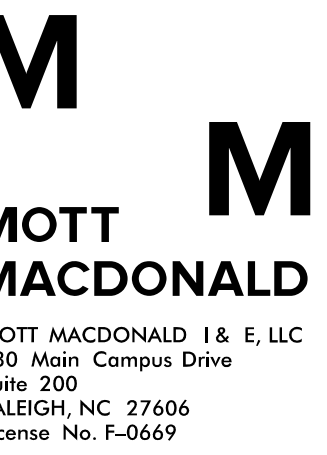
BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL For 8 Bolt Base Plate

METAL POLE No. 3

PROJECT REFERENCE NO.	SHEET NO.
R-5726	Sig. 5.5

MAST ARM LOADING SCHEDULE

LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
☉	RIGID MOUNTED SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE	16.3 S.F.	42.0" W X 56.0" L	103 LBS
☉	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5" W X 66.0" L	74 LBS
☉	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5" W X 52.5" L	60 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0" L	14 LBS



NOTES

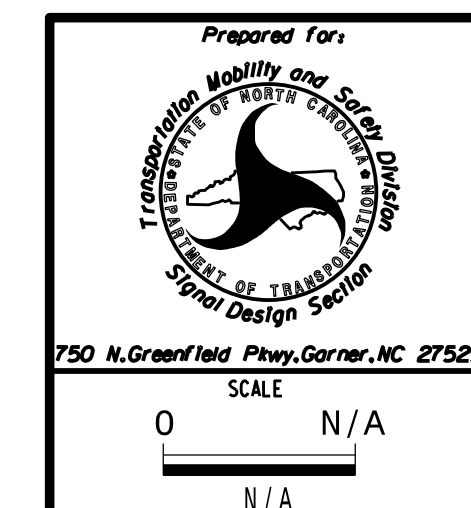
DESIGN REFERENCE MATERIAL

- Design the traffic signal structure and foundation in accordance with: The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions. The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions. The 2018 NCDOT Roadway Standard Drawings. The traffic signal project plans and special provisions. The NCDOT "Metal Pole Standards" located at the following NCDOT website: <https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

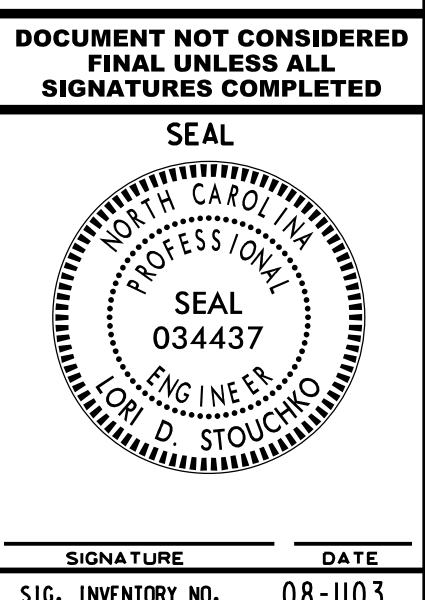
DESIGN REQUIREMENTS

- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- Design all signal supports using stress ratios that do not exceed 0.9.
- The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- The mast arm attachment height (H1) shown is based on the following design assumptions:
  - Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
  - Signal heads are rigidly mounted and vertically centered on the mast arm.
  - The roadway clearance height for design is as shown in the elevation views.
  - The top of the pole base plate is 0.75 feet above the ground elevation.
  - Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
  - Mast arm attachment height (H1) plus 2 feet, or
  - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.

NCDOT Wind Zone 4 (90 mph)



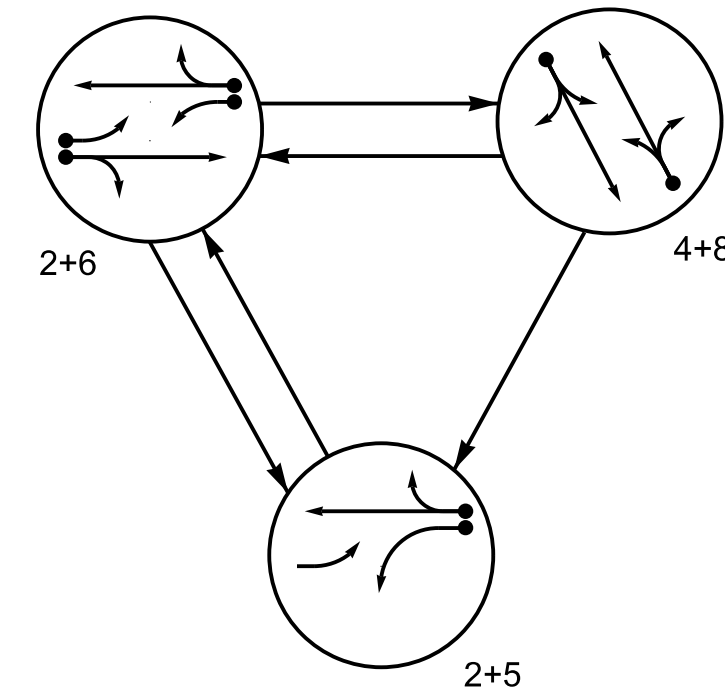
Prepared For:		NC 211/NC 73-211 at NC 73 (South Intersection)	
Division 8 Moore County		West End	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax		
PREPARED BY: LD Stouchko	REVIEWED BY:		
SCALE: 0 N/A		SIGNATURE: _____ DATE: _____	



SIG. INVENTORY NO. 08-1103

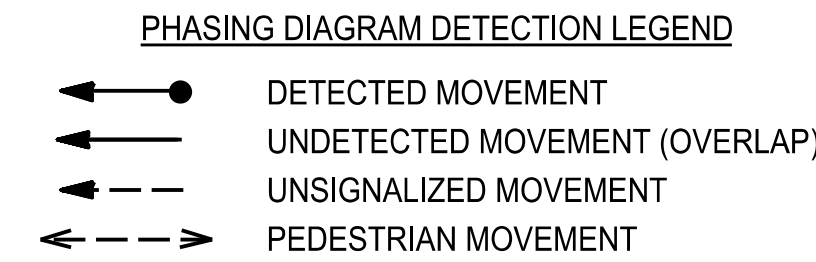
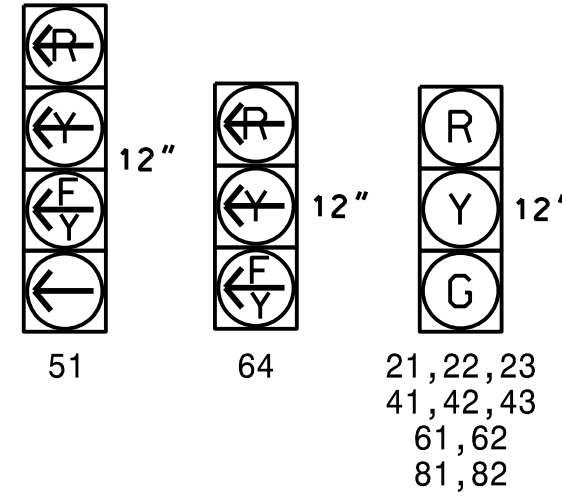


DEFAULT PHASING DIAGRAM

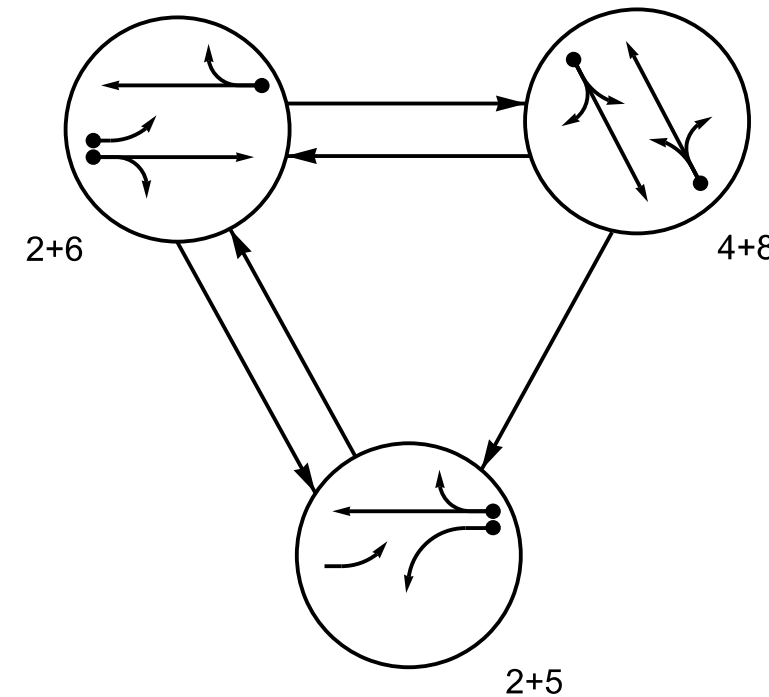


SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	F	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

SIGNAL FACE I.D.  
All Heads L.E.D.



ALTERNATE PHASING DIAGRAM



SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	R	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

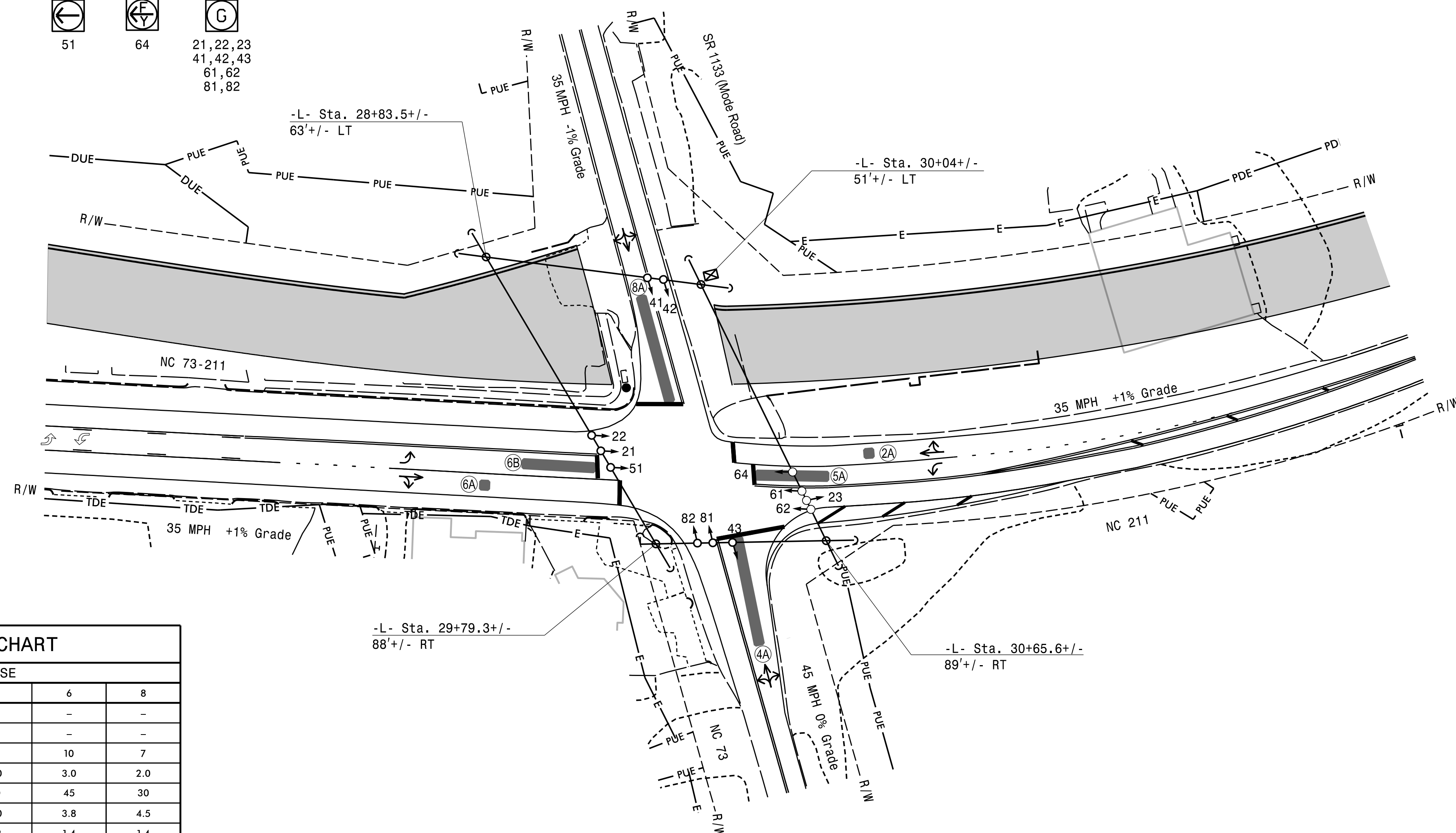
MAXTIME DETECTOR INSTALLATION CHART											
DETECTOR				PROGRAMMING							
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A*	6X6	70	*	*	2	-	-	X	-	X	*
4A*	6X60	0	*	*	4	10.0	-	X	-	X	*
5A*	6X40	0	*	*	5	15.0#	-	X	-	X	*
					2#	-	-	X	-	X	*
6A*	6X6	70	*	*	6	-	-	X	-	X	*
6B*	6X40	0	*	*	6	-	-	X	-	X	*
8A*	6X60	0	*	*	8	10.0	-	X	-	X	*

# Disable Delay During Alternate Phasing Operation  
 ## Disable Phase Call for Loop During Alternate Phasing Operation  
 \* Video Detection Zone

3 Phase Fully Actuated (Isolated)

NOTES

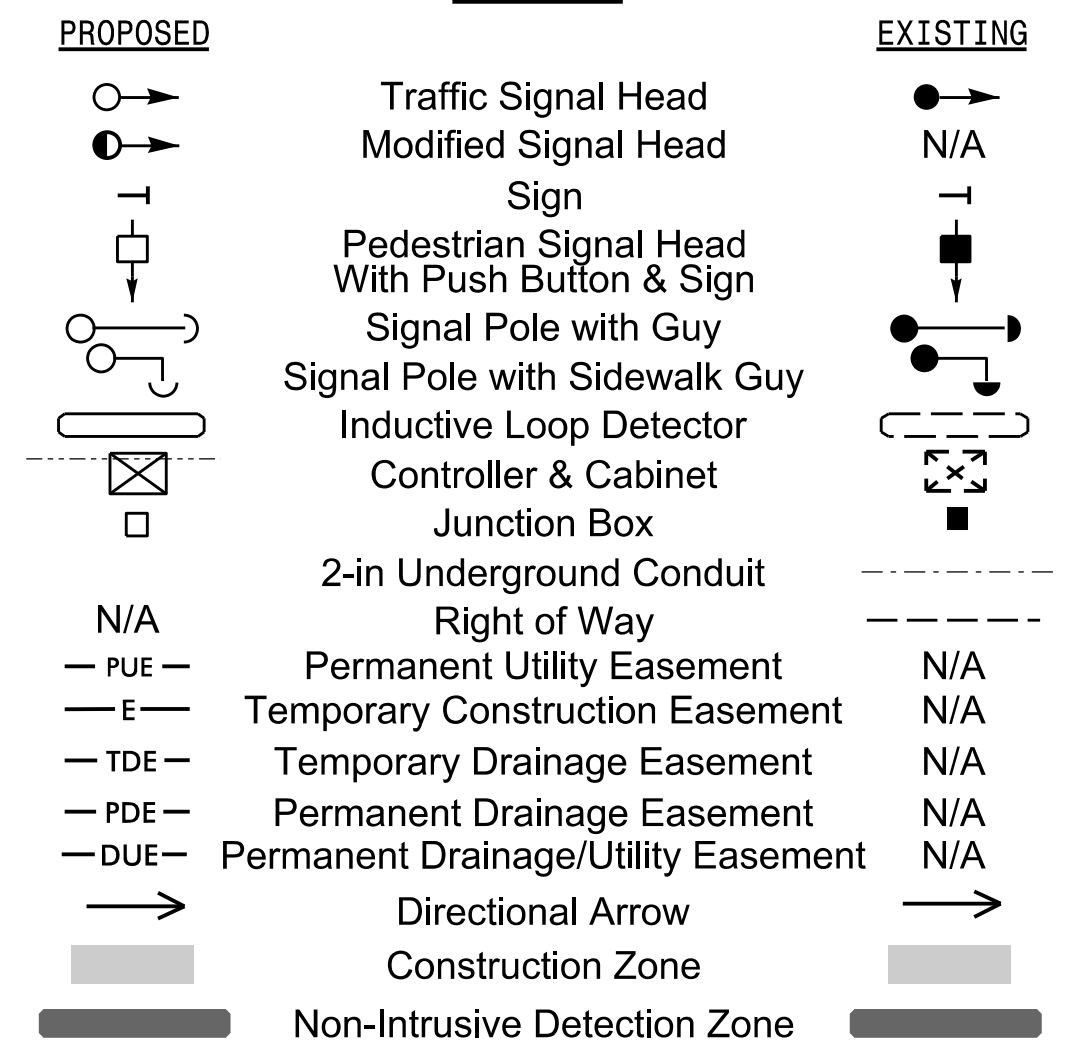
- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 5 may be lagged.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.



FEATURE	PHASE				
	2	4	5	6	8
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green *	10	7	7	10	7
Passage *	3.0	2.0	2.0	3.0	2.0
Max 1 *	45	30	20	45	30
Yellow Change	3.8	4.5	3.0	3.8	4.5
Red Clear	1.4	1.4	1.2	1.4	1.4
Added Initial *	-	-	-	-	-
Maximum Initial *	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-
Time To Reduce *	-	-	-	-	-
Minimum Gap	-	-	-	-	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	X
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	X	-	-	X

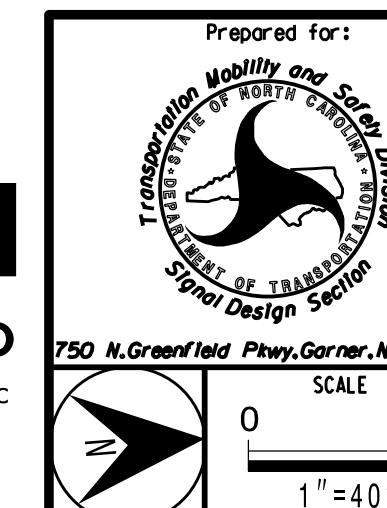
\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



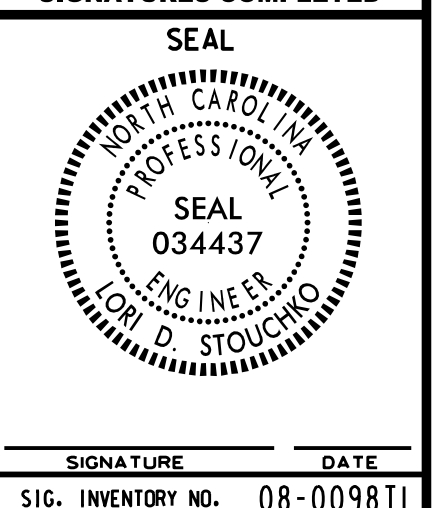
Signal Upgrade  
Temporary Design 1 (TMP Phase I)

**M M**  
**MOTT MACDONALD**  
 MOTT MACDONALD I & E, LLC  
 930 Main Campus Drive  
 Suite 200  
 RALEIGH, NC 27606  
 License No. F-0669



NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8	Moore County West End
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



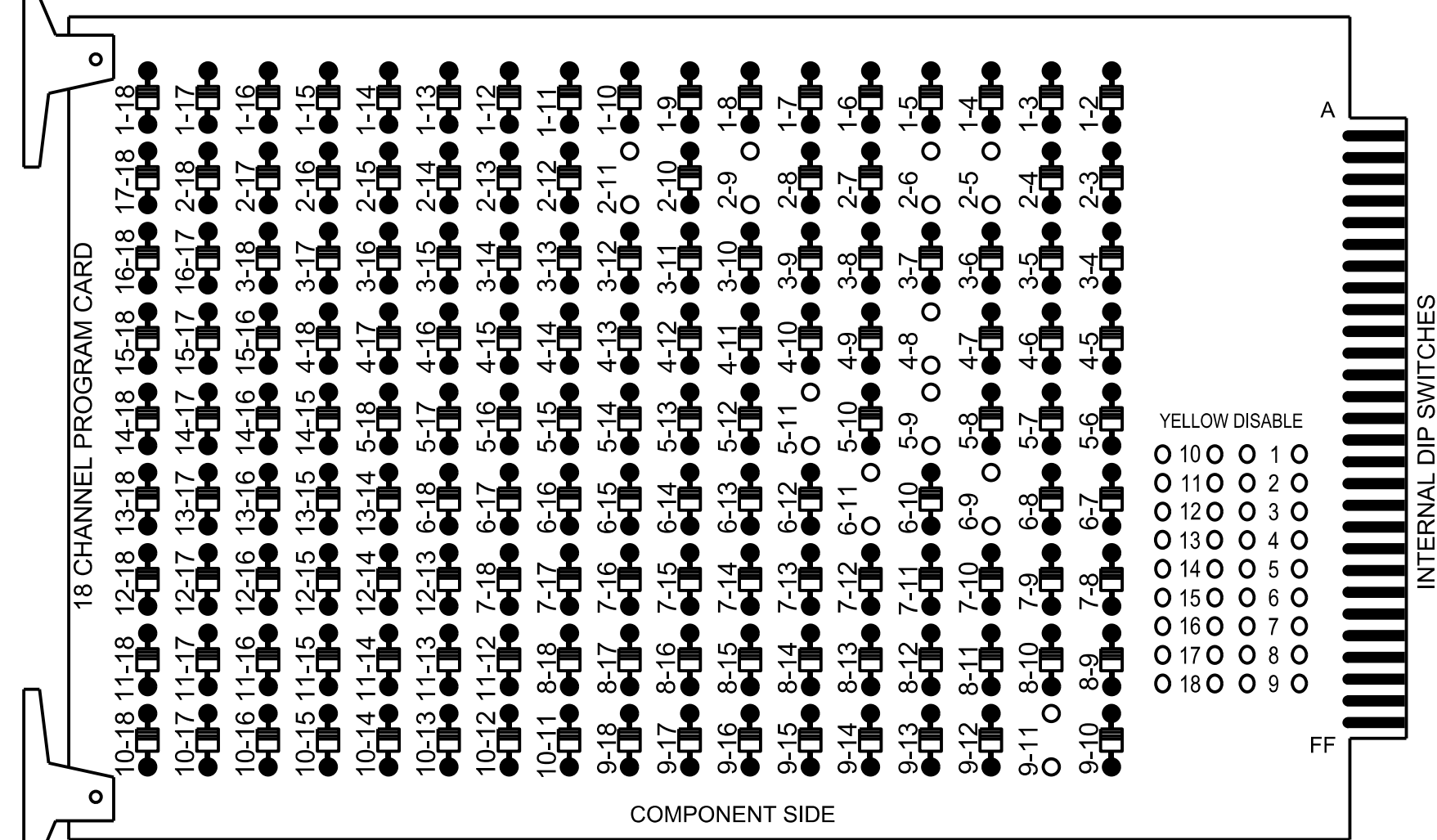
SIGNATURE DATE  
 SIG. INVENTORY NO. 08-009811

**18 CHANNEL CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

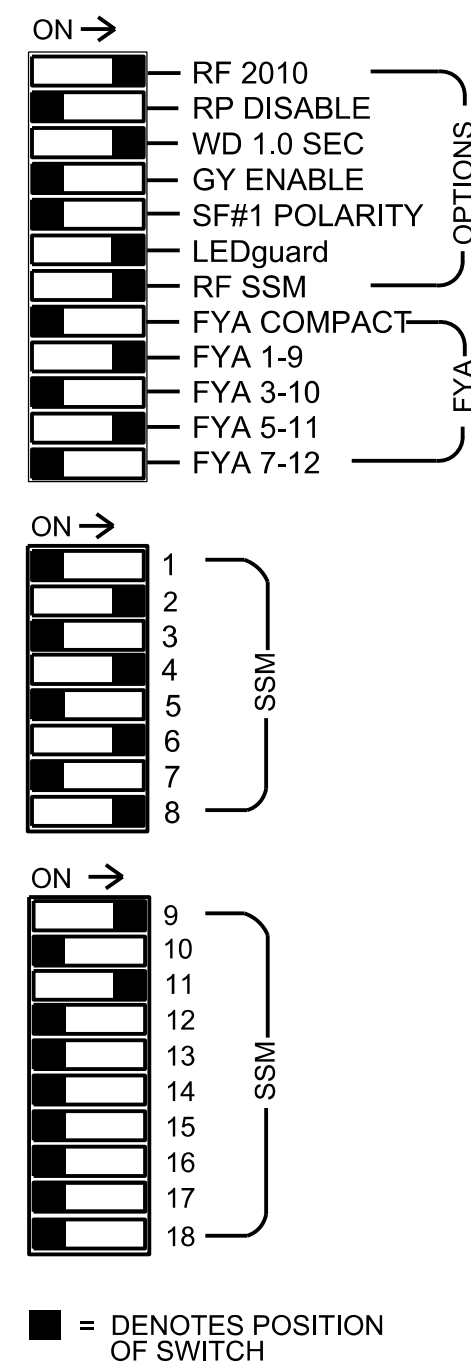
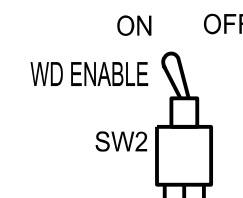
REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

**EQUIPMENT INFORMATION**

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S2, S5, S7, S8, S11, AUX S1, AUX S4  
 Phases Used.....2, 4, 5, 6, 8  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....NOT USED

\*See overlap programming detail on sheet 2

**SIGNAL HEAD HOOK-UP CHART**

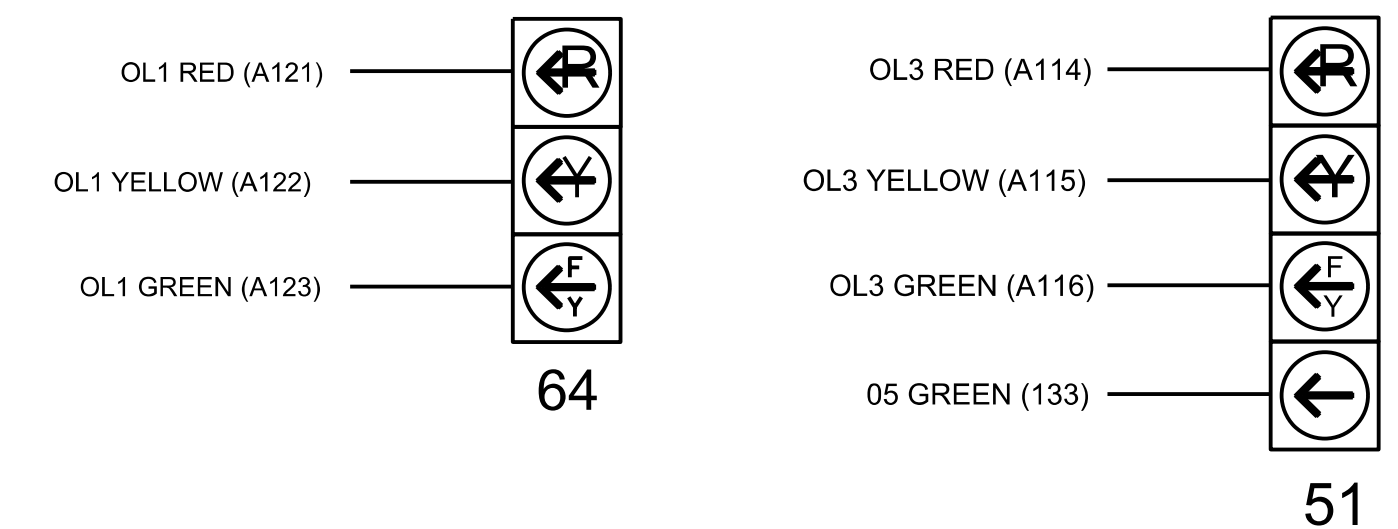
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	41,42 43	NU	51	61,62	NU	NU	81,82	NU	64	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW								133										

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

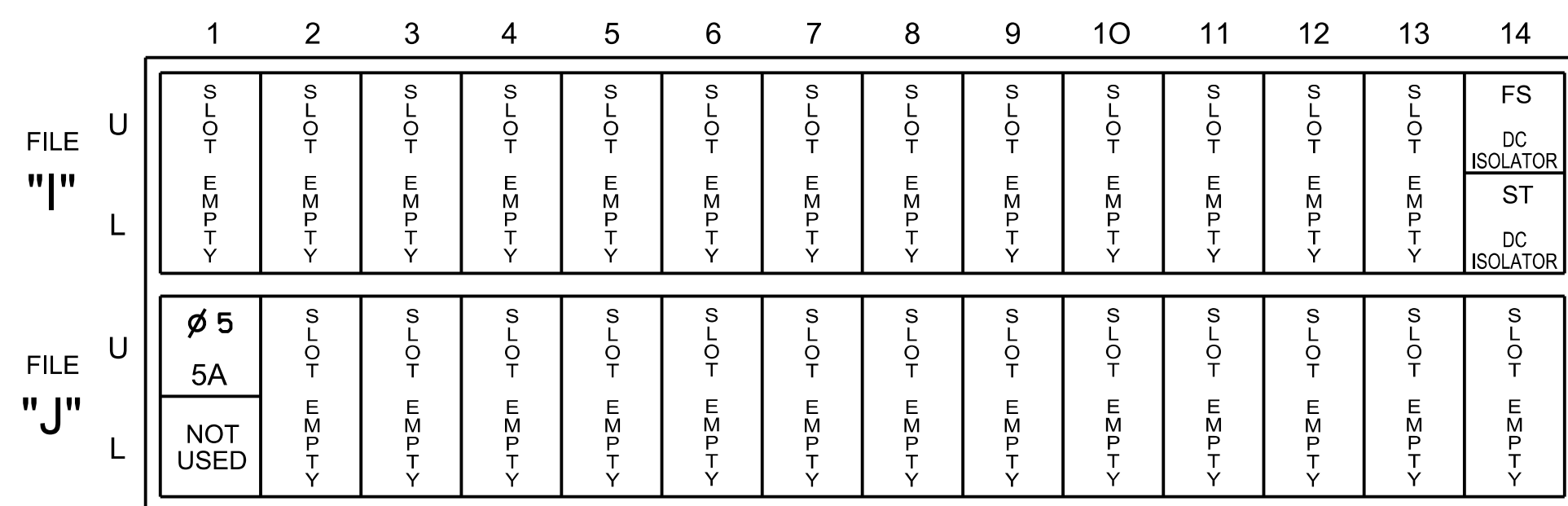
**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**INPUT FILE POSITION LAYOUT**

(front view)



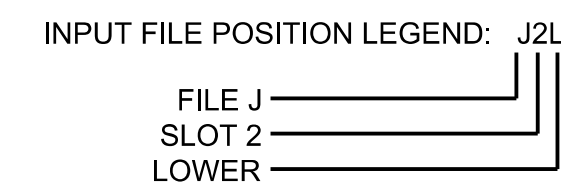
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-1,2	J1U	55	17	15 *	5	15.0		X		X	
				-	31 *	2			X		X	

\* For the detectors to work as shown on the signal design plan see the Vehicle Detector Setup Programming Detail for Alternate phasing on sheet 2 of 3



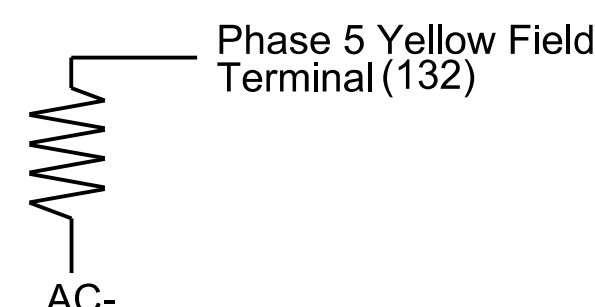
**SPECIAL DETECTOR NOTES**

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3  
 Temporary Design 1 (TMP Phase I)

**MOTT MACDONALD**  
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Prepared for:  
  
 150 N. Greenfield Pkwy, Garner, NC 27529

REVISIONS		INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
  
 LD D. STOUCHKO  
 ENGINEER  
 No. 034437

NC 73-211/NC 211  
 at  
 NC 73/SR 1133 (Mode Rd)

Division 8 Moore County West End

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax

PREPARED BY: LD Stouchko REVIEWED BY:

DATE  
 SIG. INVENTORY NO. 08-0098T1

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	6	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
15	5	-
31	0	-

5A

### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	-	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO <u>RUN DEFAULT PHASING</u>	1	1
ACTIVE PLAN REQUIRED TO <u>RUN ALTERNATE PHASING</u>	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

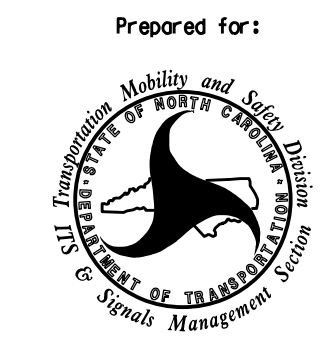
THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-0098T1  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

**M M**  
**MOTT MACDONALD**  
MOTT MACDONALD I & E, LLC  
930 Main Campus Drive  
Suite 200  
RALEIGH, NC 27606  
License No. F-0669

ELECTRICAL AND PROGRAMMING  
DETAILS FOR:  
Prepared for:  
  
750 N. Greenfield Pkwy, Garner, NC 27529

Electrical Detail - Sheet 2 of 3  
Temporary Design 1 (TMP Phase I)

NC 73-211/NC 211  
at  
NC 73/SR 1133 (Mode Rd)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS  
INIT. DATE

DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA  
PROFESSIONAL  
ENGINEER  
LD D. STOUCHKO  
034437  
DATE  
SIG. INVENTORY NO. 08-0098T1

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

NOTICE  
PHASE 2  
FLASH RED →

NOTICE  
PHASE 6  
FLASH RED →

NOTICE  
OVERLAP 1  
FLASH RED →

NOTICE  
OVERLAP 3  
FLASH RED →

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

Electrical Detail - Sheet 3 of 3  
Temporary Design 1 (TMP Phase I)

DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-0098T1  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:


M M

**MOTT  
MACDONALD**

MOTT MACDONALD I & E, LLC  
930 Main Campus Drive  
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ELECTRICAL AND PROGRAMMING  
DETAILS FOR:

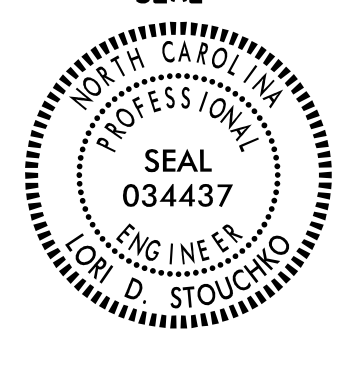
Prepared for:



750 N. Greenfield Pkwy, Corner, NC 27529

NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8 Moore County West End	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

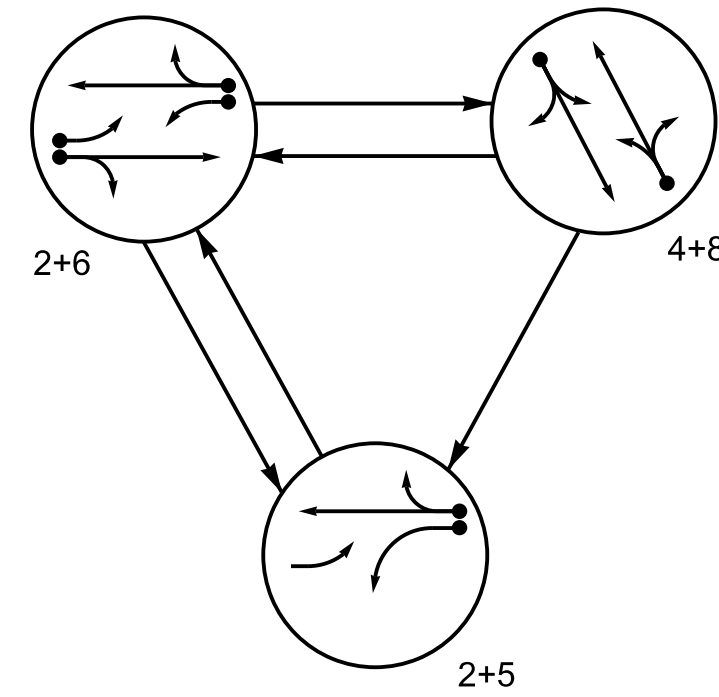
SEAL



SIGNATURE DATE  
SIG. INVENTORY NO. 08-0098T1

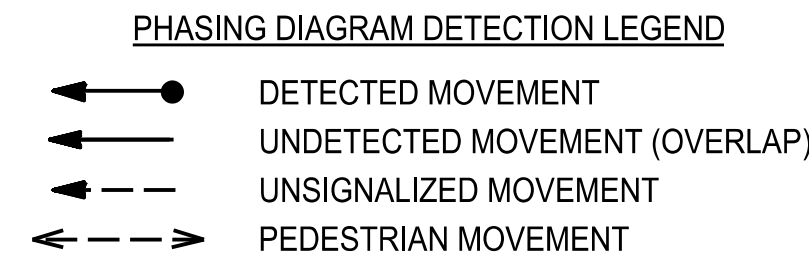
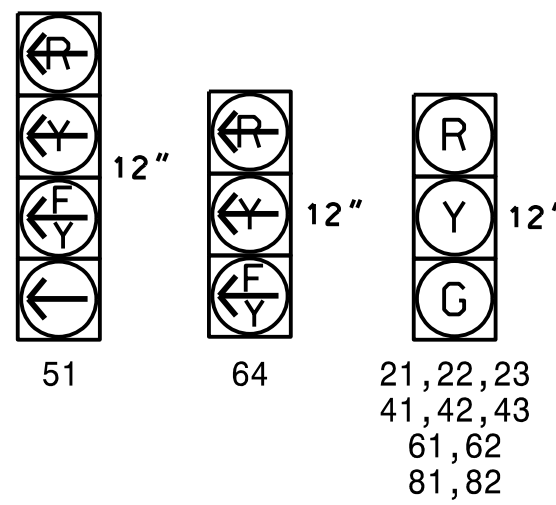


DEFAULT PHASING DIAGRAM

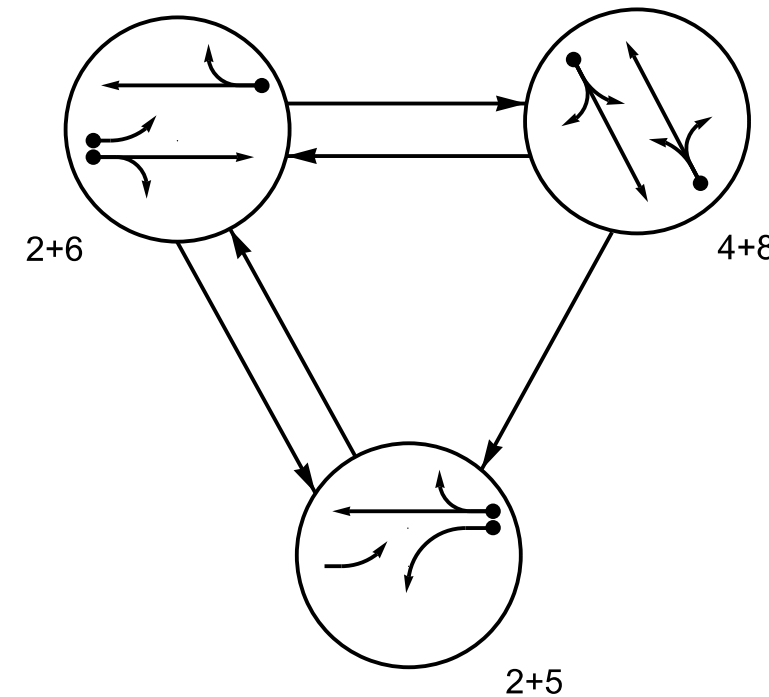


SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	F	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

SIGNAL FACE I.D.  
All Heads L.E.D.



ALTERNATE PHASING DIAGRAM



SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	R	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

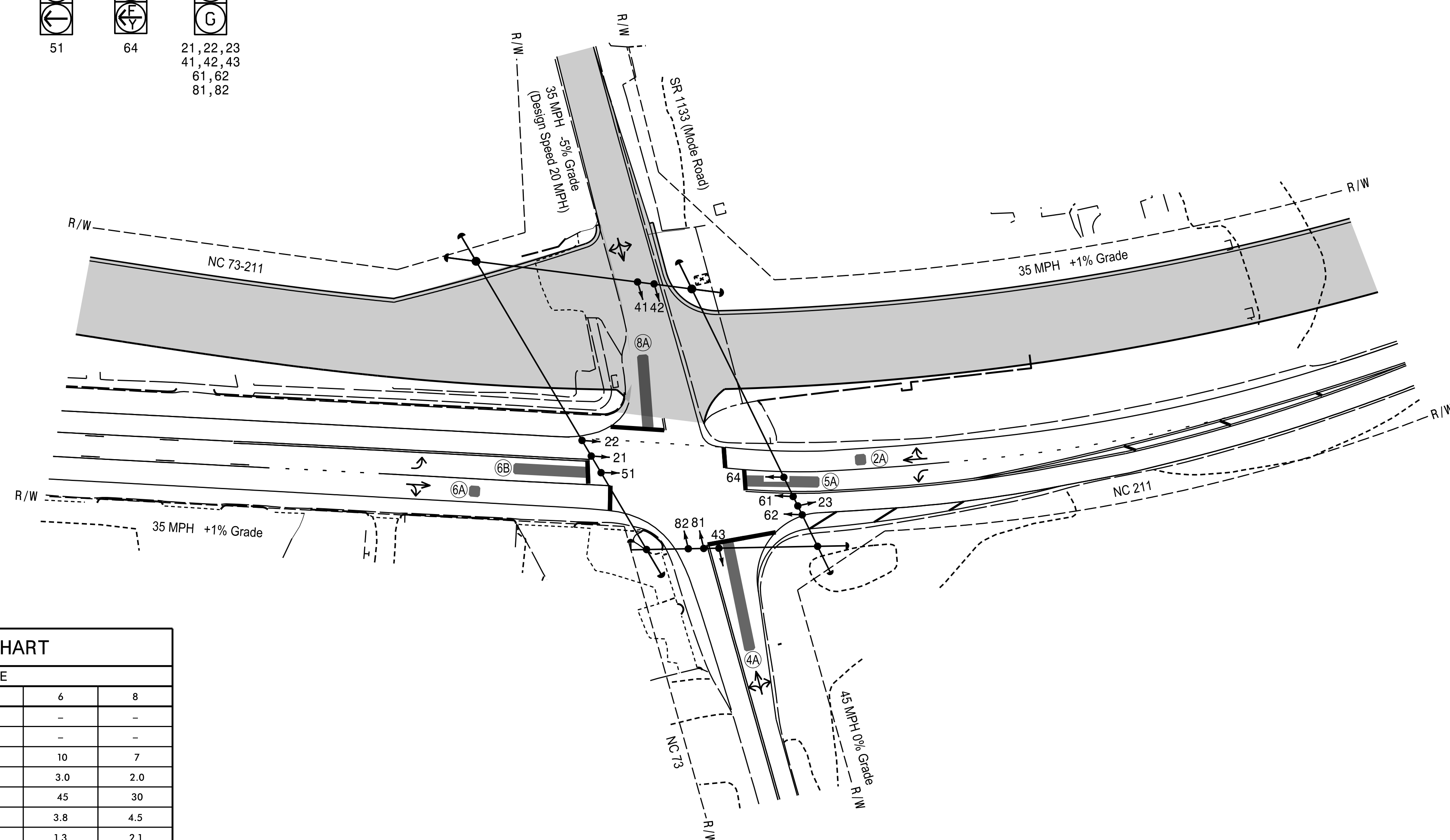
MAXTIME DETECTOR INSTALLATION CHART											
DETECTOR				PROGRAMMING							
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A*	6X6	70	*	*	2	-	-	X	-	X	-
4A*	6X60	0	*	*	4	10.0	-	X	-	X	-
5A*	6X40	0	*	*	5	15.0#	-	X	-	X	-
6A*	6X6	70	*	*	6	-	-	X	-	X	-
6B*	6X40	0	*	*	6	-	-	X	-	X	-
8A*	6X40	0	*	*	8	10.0	-	X	-	X	-

# Disable Delay During Alternate Phasing Operation.  
## Disable Phase Call for Loop During Alternate Phasing Operation.  
\* Video Detection Zone

3 Phase Fully Actuated (Isolated)

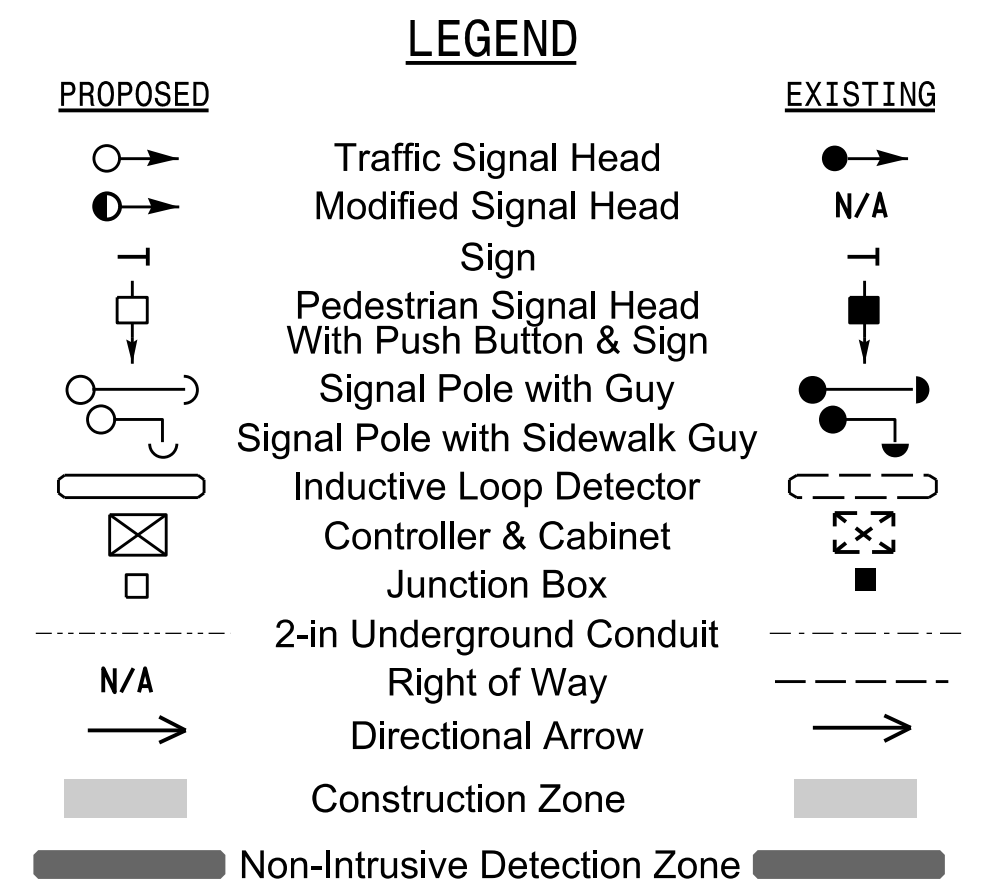
NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 5 may be lagged.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.



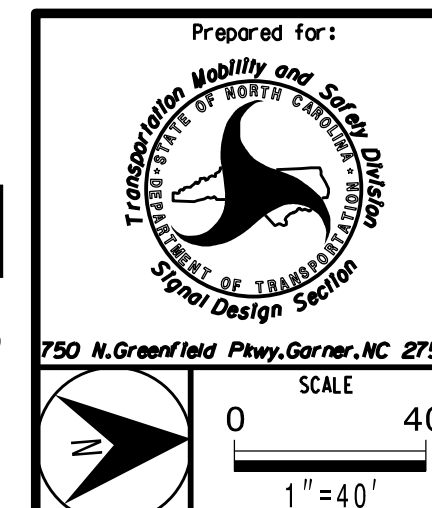
FEATURE	PHASE				
	2	4	5	6	8
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green *	10	7	7	10	7
Passage *	3.0	2.0	2.0	3.0	2.0
Max I *	45	30	20	45	30
Yellow Change	3.8	4.5	3.0	3.8	4.5
Red Clear	1.3	2.1	1.2	1.3	2.1
Added Initial *	-	-	-	-	-
Maximum Initial *	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-
Time To Reduce *	-	-	-	-	-
Minimum Gap	-	-	-	-	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	X
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	X	-	-	X

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade  
Temporary Design 2 (TMP Phase I)

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Prepared for: TRANSPORTATION MODILITY AND SCIENCE CENTER UNIVERSITY OF NORTH CAROLINA SIGNAL DESIGN SECTION		NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8 Moore County West End		PLAN DATE: June 2024 REVIEWED BY: R. Mullinax	
PREPARED BY: LD Stouchko		REVIEWED BY:	
REVISIONS			
	INIT.	DATE	

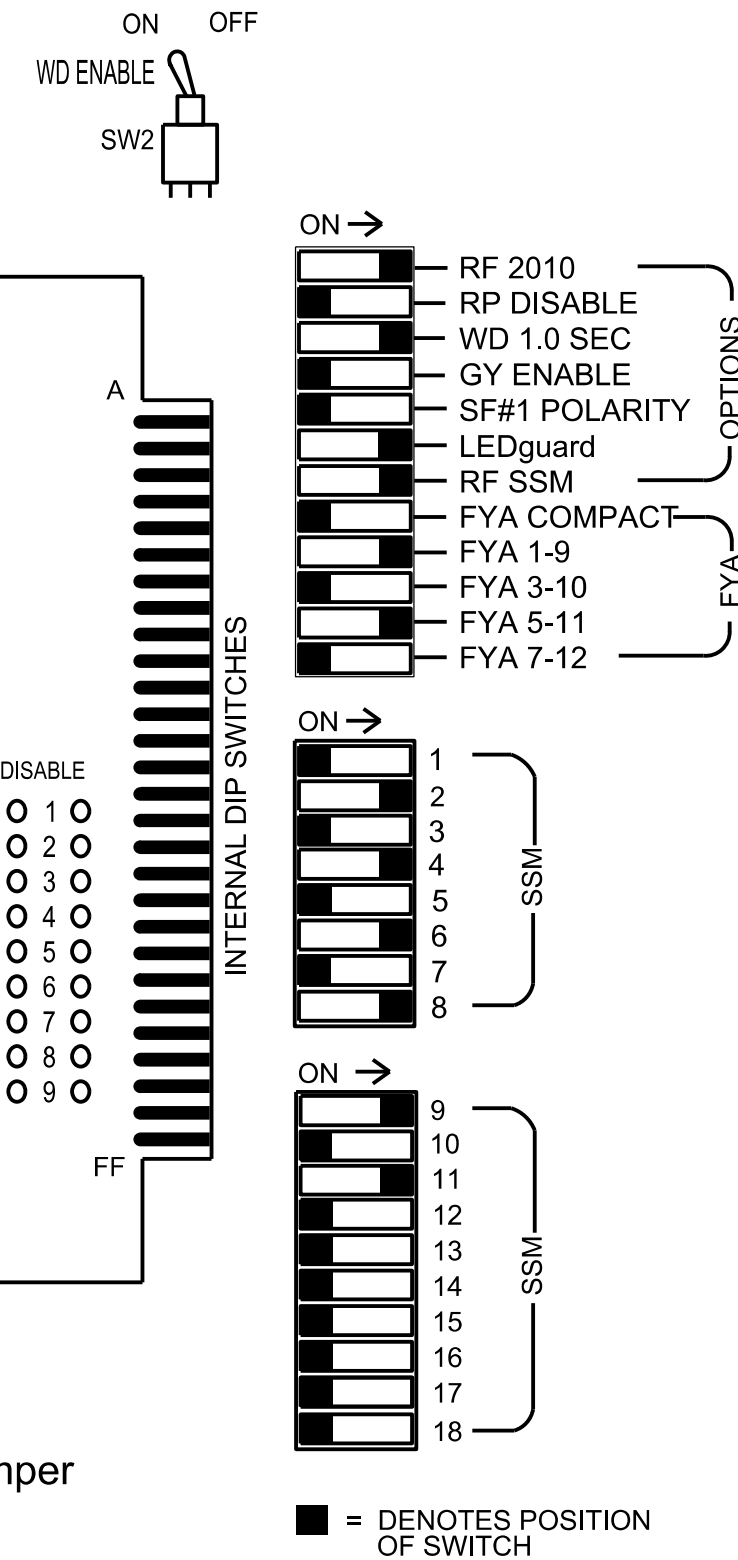
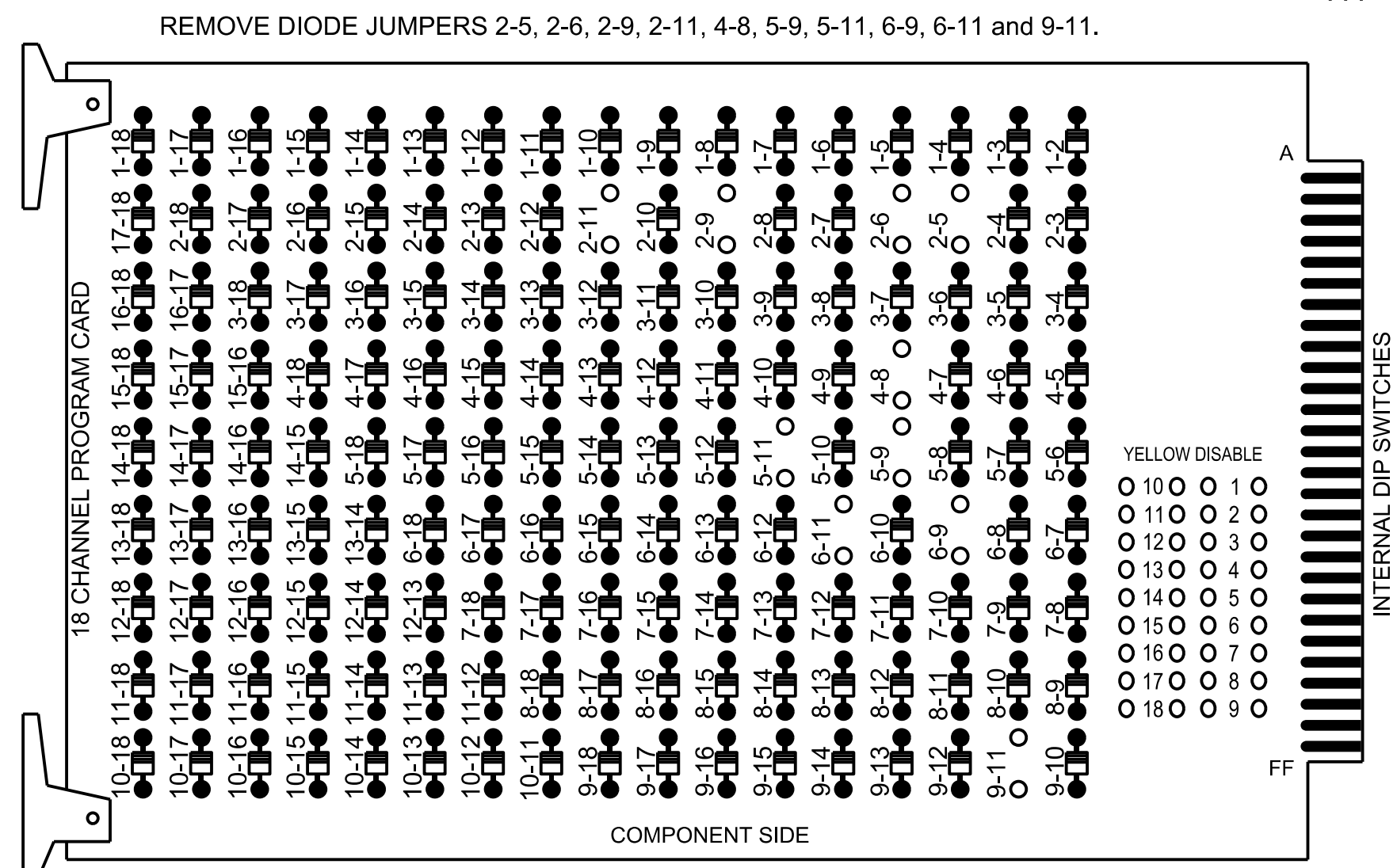
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
MOTT MACDONALD  
PROFESSIONAL ENGINEER  
STATE OF NORTH CAROLINA  
LICENSE NO. 034437  
LD STOUCHKO

SIGNATURE DATE  
SIG. INVENTORY NO. 08-009812

### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S2, S5, S7, S8, S11, AUX S1, AUX S4  
 Phases Used.....2, 4, 5, 6, 8  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....NOT USED

\*See overlap programming detail on sheet 2

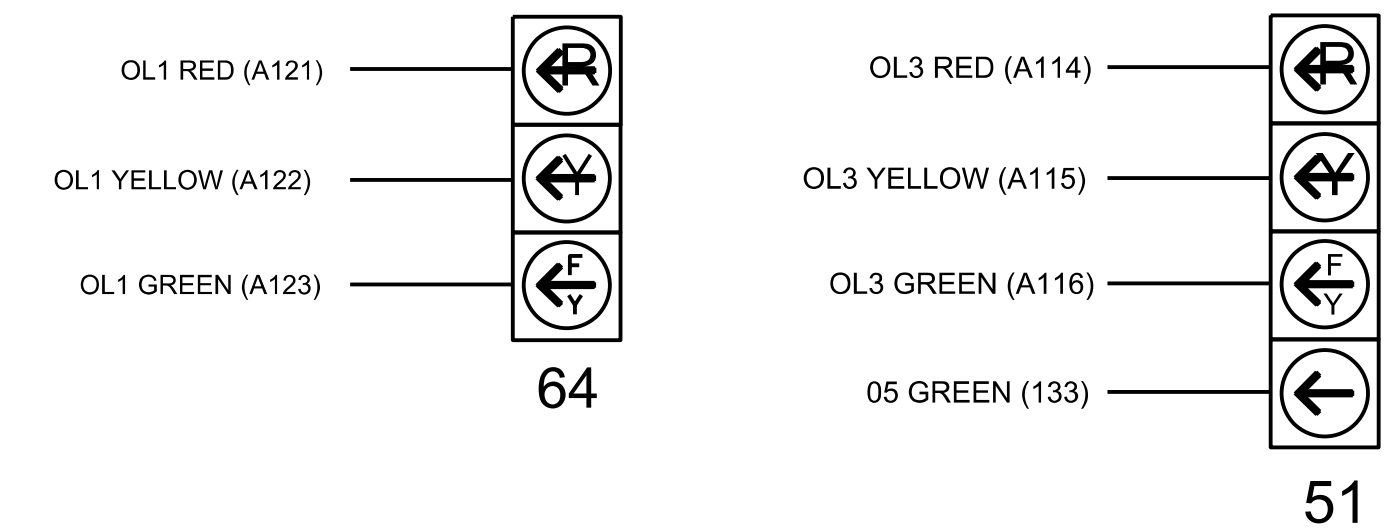
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	41,42 43	NU	51	61,62	NU	NU	81,82	NU	64	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121					A114
YELLOW ARROW													A122					A115
FLASHING YELLOW ARROW													A123					A116
GREEN ARROW								133										

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

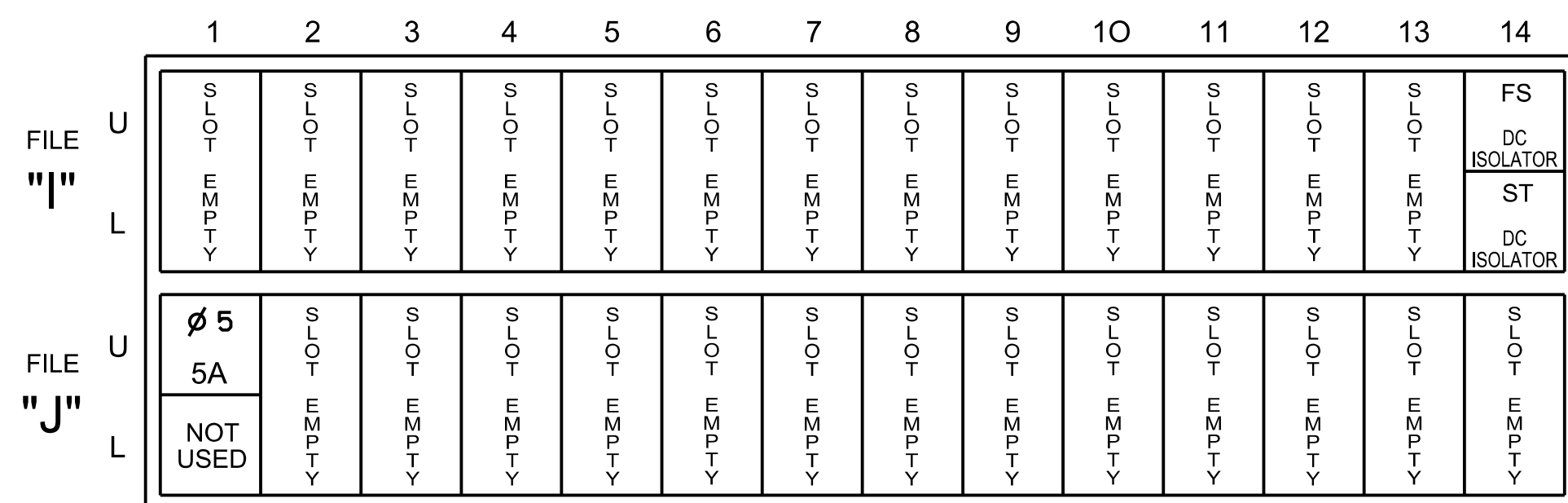
### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)



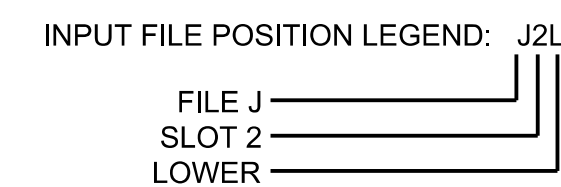
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-1,2	J1U	55	17	15 *	5	15.0		X		X	
				-	31 *	2			X		X	

\* For the detectors to work as shown on the signal design plan see the Vehicle Detector Setup Programming Detail for Alternate phasing on sheet 2 of 3



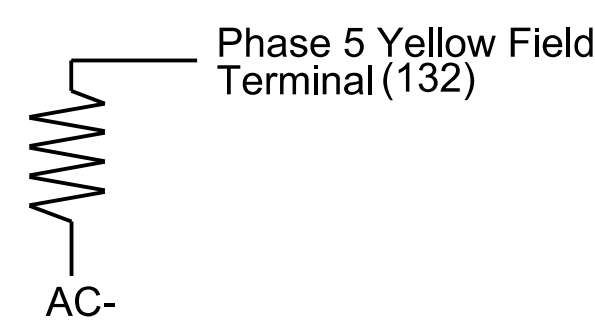
### SPECIAL DETECTOR NOTES

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3  
 Temporary Design 2 (TMP Phase I)

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Prepared for:

750 N. Greenfield Pkwy, Garner, NC 27529

NC 73-211/NC 211		at		NC 73/SR 1133 (Mode Rd)	
Division 8	Moore County	West End			
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax				
PREPARED BY: LD Stouchko	REVIEWED BY:				
REVISIONS	INIT.	DATE			

SEAL

DATE

SIG. INVENTORY NO. 08-009812

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	6	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
15	5	-
31	0	-

5A

### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	-	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

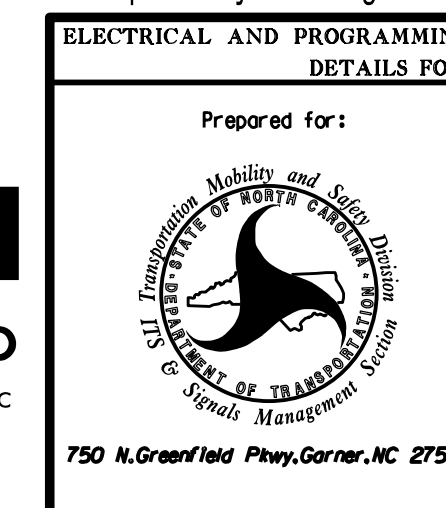
VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-009812  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

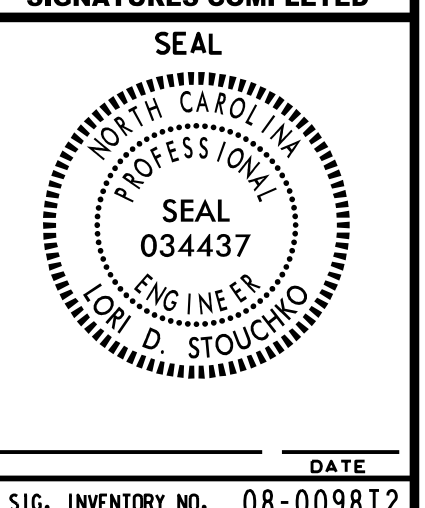
Electrical Detail - Sheet 2 of 3  
Temporary Design 2 (TMP Phase I)

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SIGNATURES COMPLETED

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Prepared for:		NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8 Moore County West End		REVIEWED BY: R. Mullinax	
PLAN DATE: June 2024	REVIEWED BY: LD Stouchko	REVIEWED BY:	
REVISIONS	INIT.	DATE	



SIG. INVENTORY NO. 08-009812

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		X	X	1
NOTICE PHASE 2 FLASH RED →	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
NOTICE PHASE 6 FLASH RED →	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

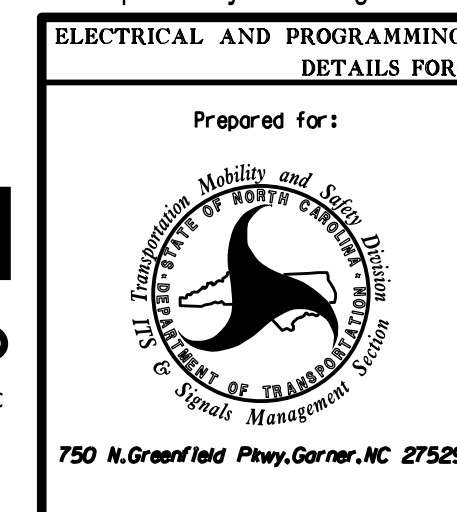
Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-0098T2  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

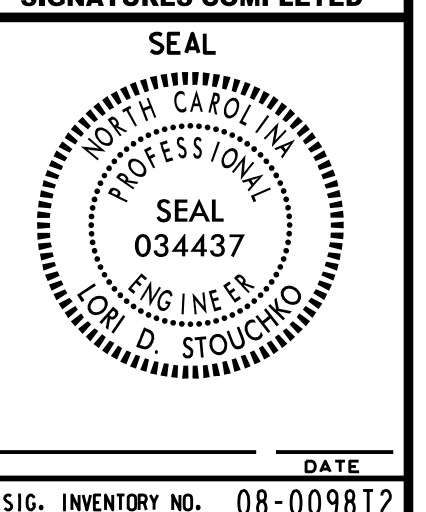
Electrical Detail - Sheet 3 of 3  
Temporary Design 2 (TMP Phase I)

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SIGNATURES COMPLETED

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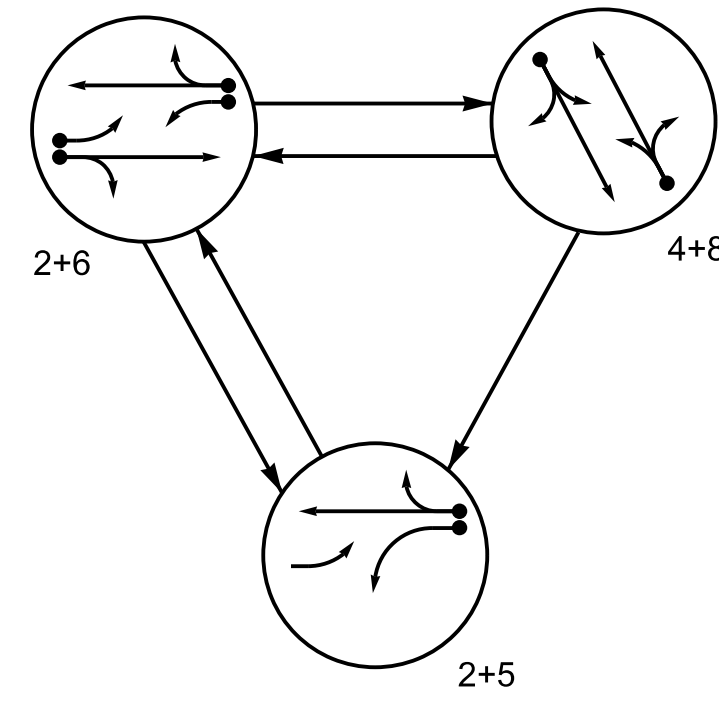
Prepared for:		NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8		Moore County	West End
PLAN DATE:	June 2024	REVIEWED BY:	R. Mullinax
PREPARED BY:	LD Stouchko	REVIEWED BY:	
REVISIONS	INIT.	DATE	



SIG. INVENTORY NO. 08-0098T2

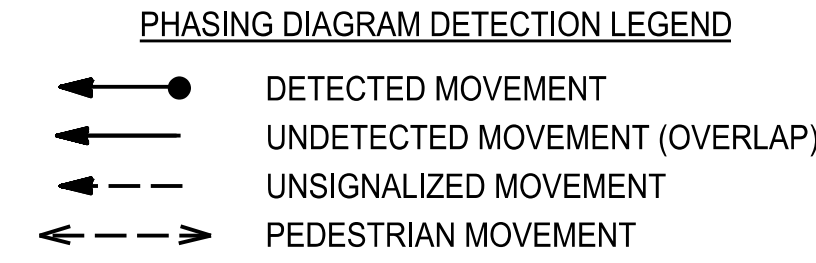
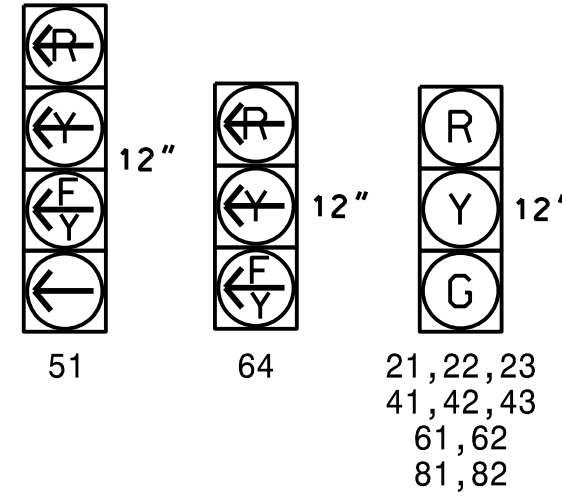


**DEFAULT PHASING DIAGRAM**

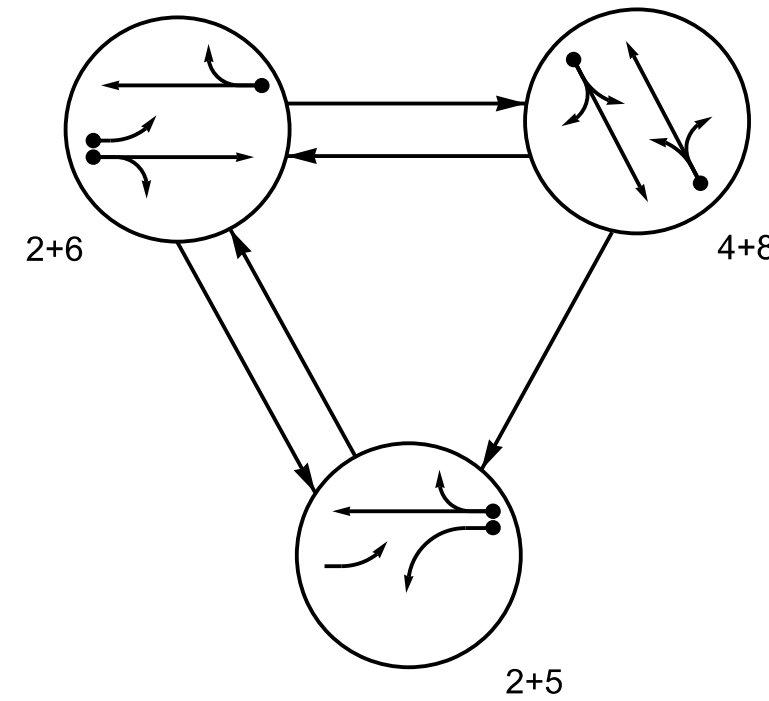


SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	F	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

**SIGNAL FACE I.D.**  
All Heads L.E.D.



**ALTERNATE PHASING DIAGRAM**



SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21,22,23	G	G	R	R
41,42,43	R	R	G	R
51	-	R	R	R
61,62	R	G	R	R
64	F	F	R	R
81,82	R	R	G	R

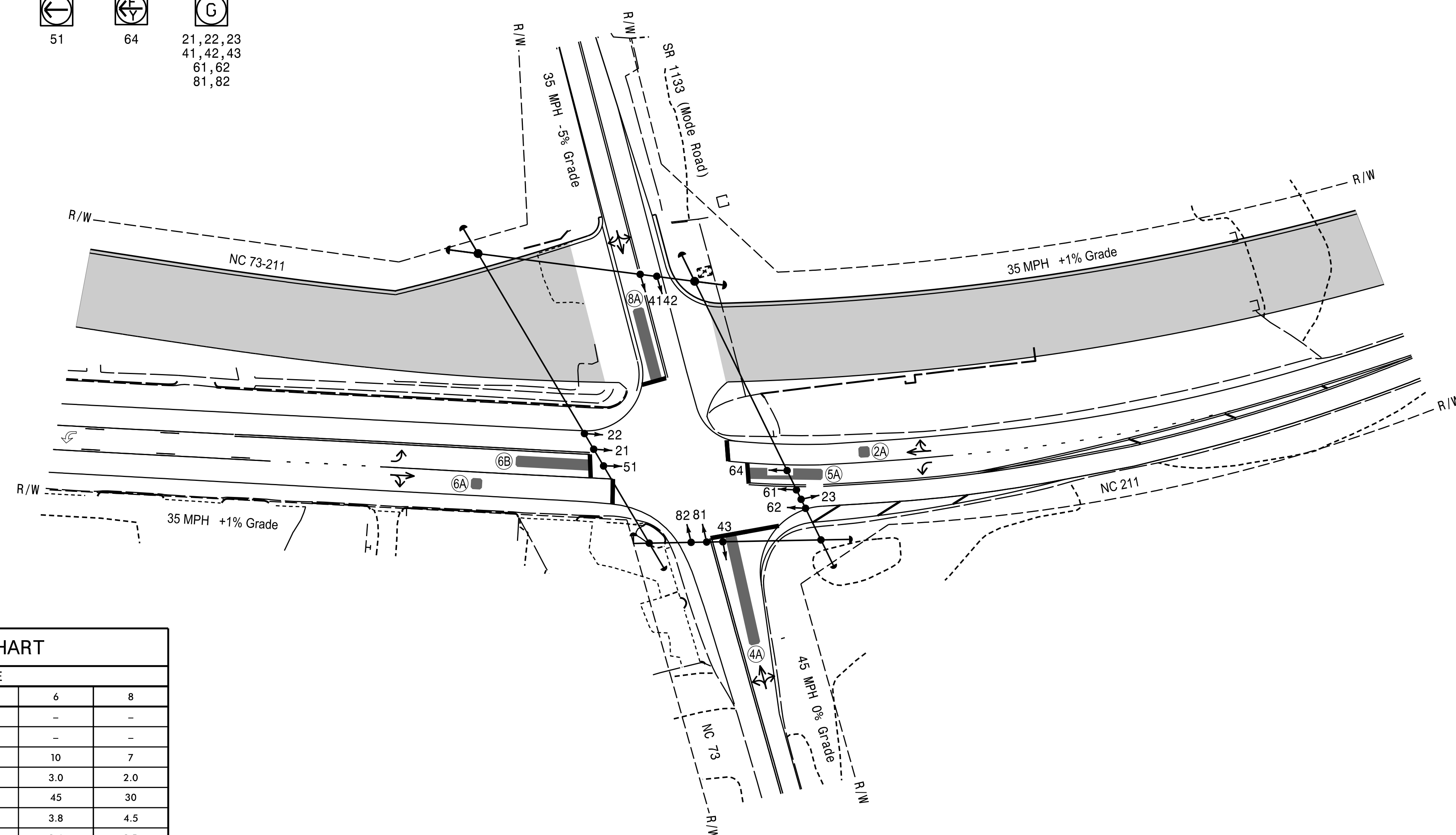
MAXTIME DETECTOR INSTALLATION CHART											
DETECTOR					PROGRAMMING						
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A *	6X6	70	*	*	2	-	-	X	-	X	-
4A *	6X60	0	*	*	4	10.0	-	X	-	X	-
5A *	6X40	0	*	*	6##	-	-	X	-	X	-
6A *	6X6	70	*	*	6	-	-	X	-	X	-
6B *	6X40	0	*	*	6	-	-	X	-	X	-
8A *	6X40	0	*	*	8	10.0	-	X	-	X	-

# Disable Delay During Alternate Phasing Operation.  
## Disable Phase Call for Loop During Alternate Phasing Operation.  
\* Video Detection Zone

3 Phase Fully Actuated (Isolated)

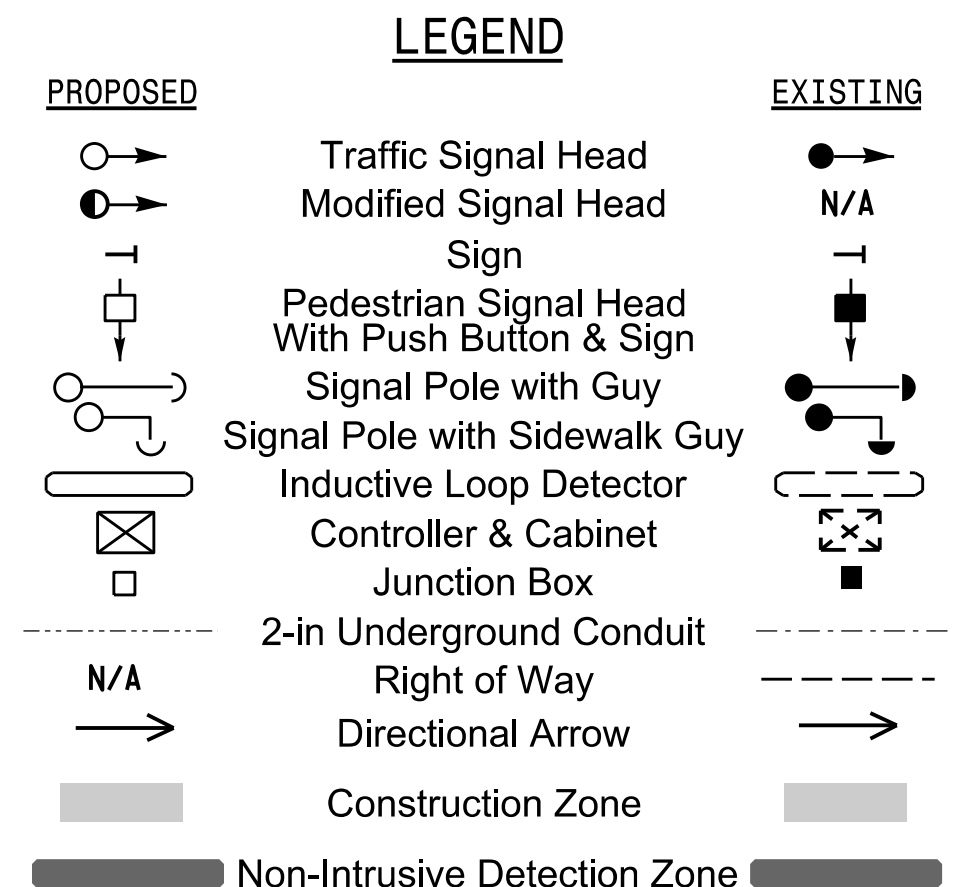
**NOTES**

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 5 may be lagged.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.



FEATURE	PHASE				
	2	4	5	6	8
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green *	10	7	7	10	7
Passage *	3.0	2.0	2.0	3.0	2.0
Max I *	45	30	20	45	30
Yellow Change	3.8	4.5	3.0	3.8	4.5
Red Clear	1.4	1.5	1.2	1.4	1.5
Added Initial *	-	-	-	-	-
Maximum Initial *	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-
Time To Reduce *	-	-	-	-	-
Minimum Gap	-	-	-	-	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	X
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	X	-	-	X

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade  
Temporary Design 3 (TMP Phase I)

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Prepared for:  
**TRANSPORTATION MODALITY AND SAFETY DIVISION**  
UNIVERSITY OF NORTH CAROLINA  
SCHOOL OF TRANSPORTATION  
Signal Design Section

750 N. Greenfield Pkwy, Garner, NC 27526  
SCALE: 0 40  
1" = 40'

NC 73-211/NC 211  
at  
NC 73/SR 1133 (Mode Rd)

Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS	INIT.	DATE

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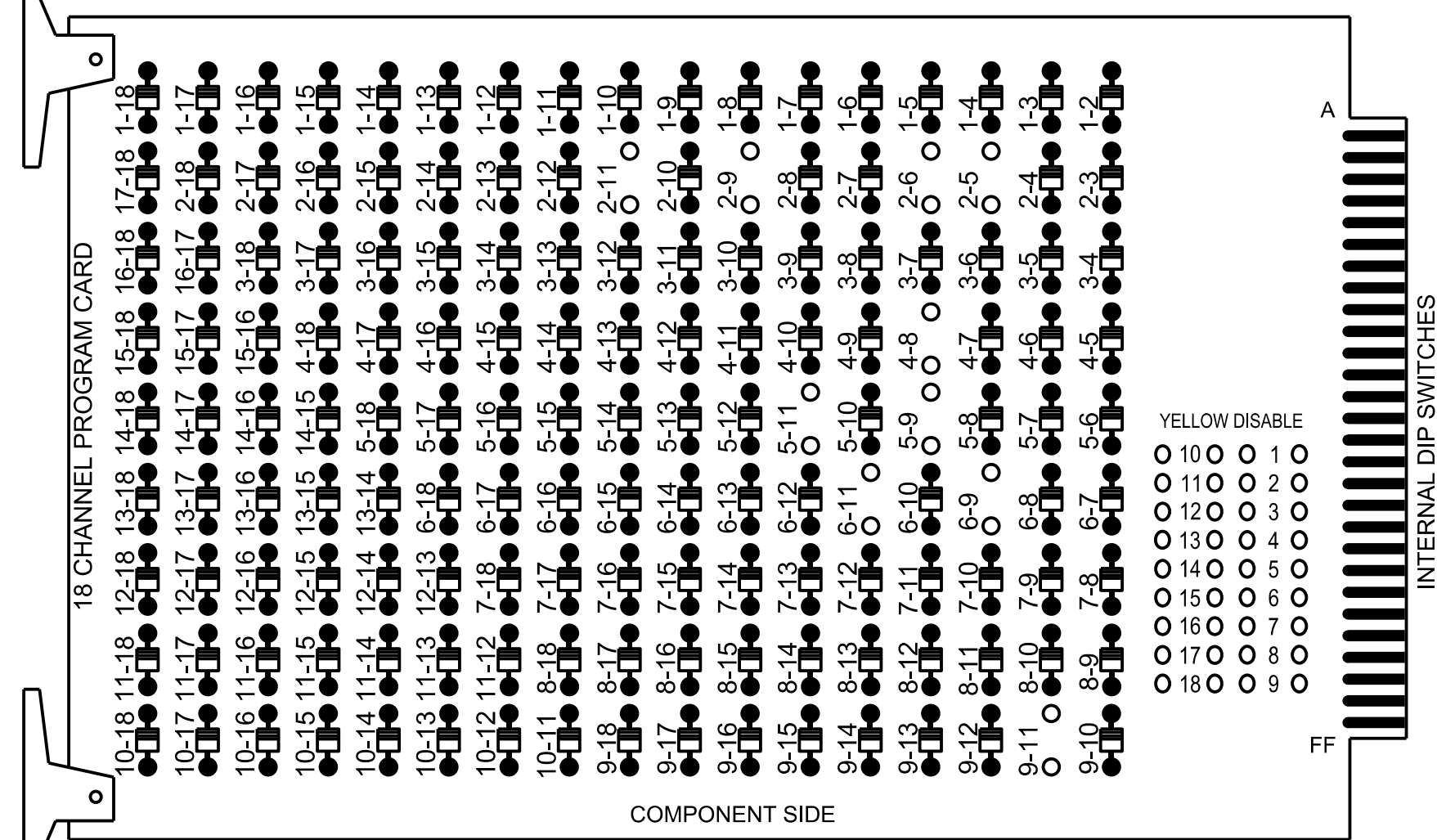
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MOTT MACDONALD  
PROFESSIONAL ENGINEER  
SEAL 034437  
LD STOUCHKO

SIGNATURE DATE  
SIG. INVENTORY NO. 08-009813

**18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

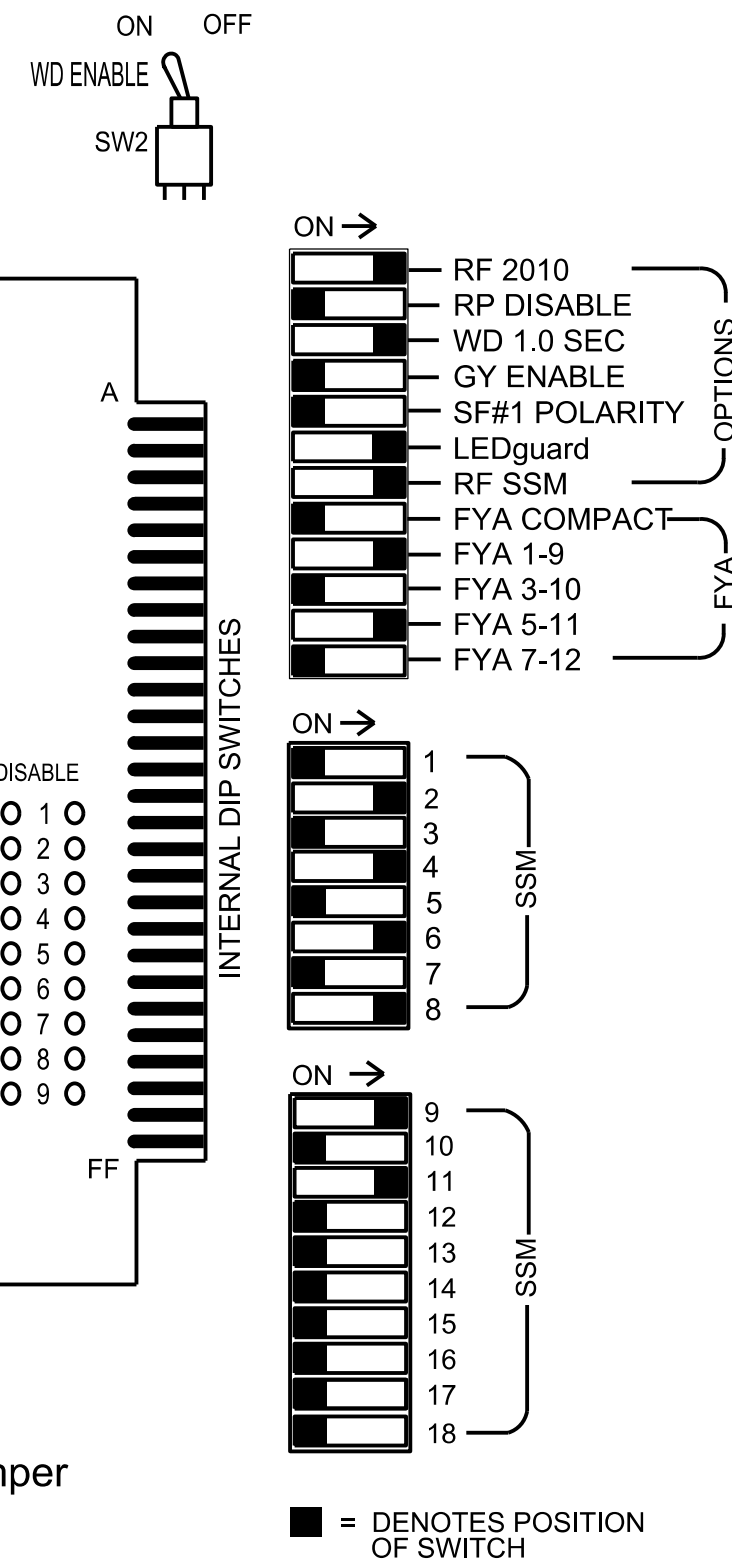
REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

**EQUIPMENT INFORMATION**

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S2, S5, S7, S8, S11, AUX S1, AUX S4  
 Phases Used.....2, 4, 5, 6, 8  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....NOT USED

\*See overlap programming detail on sheet 2

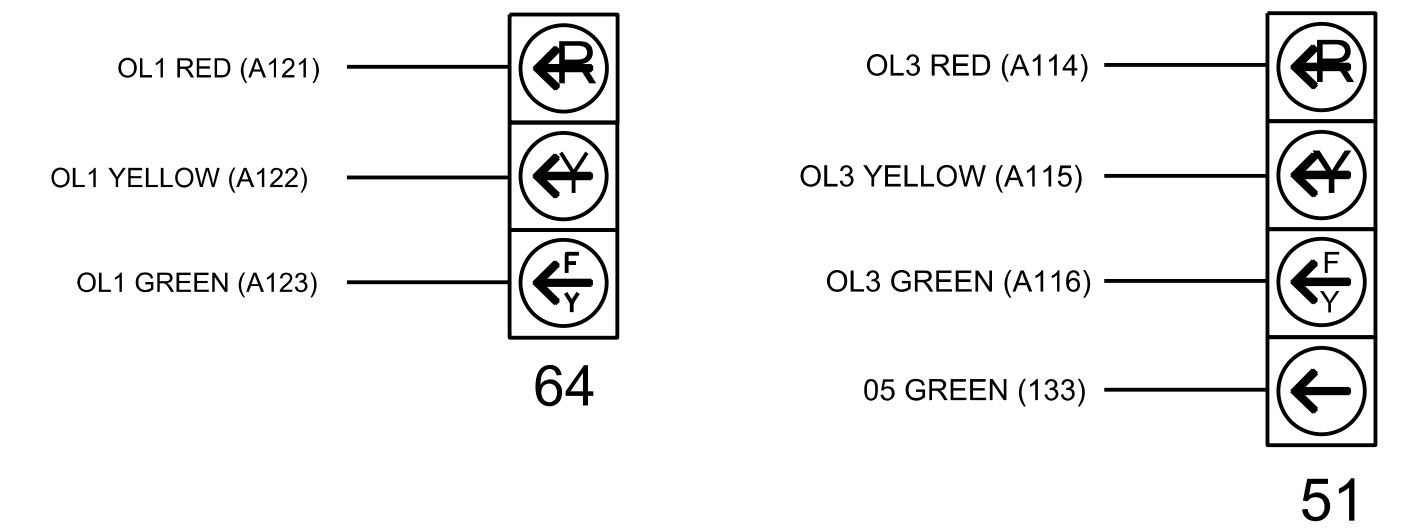
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	41,42 43	NU	51*	61,62	NU	NU	81,82	NU	64*	NU	NU	51*	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW								133										

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**INPUT FILE POSITION LAYOUT**

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	S	S	S	S	S	S	S	S	S	S	S	S	FS
L	I	I	I	I	I	I	I	I	I	I	I	I	I	DC ISOLATOR
U	5	S	S	S	S	S	S	S	S	S	S	S	S	S
L	5A	S	S	S	S	S	S	S	S	S	S	S	S	S
U	NOT USED	S	S	S	S	S	S	S	S	S	S	S	S	S
L	NOT USED	S	S	S	S	S	S	S	S	S	S	S	S	S

EX. : 1A, 2A, ETC. = LOOP NO.'S

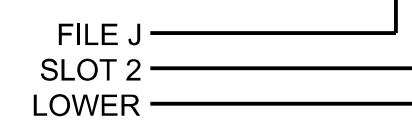
FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-12	J1U	55	17	15 *	5	15.0				X	
				-	31 *	2			X		X	

\* For the detectors to work as shown on the signal design plan see the Vehicle Detector Setup Programming Detail for Alternate phasing on sheet 2 of 3

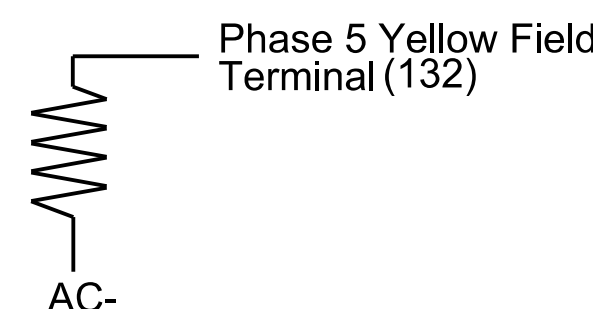
INPUT FILE POSITION LEGEND: J2L



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown)

ACCEPTABLE VALUES	Value (ohms)	Wattage
	1.5K - 1.9K	25W (min)
	2.0K - 3.0K	10W (min)



**SPECIAL DETECTOR NOTES**

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

Electrical Detail - Sheet 1 of 3  
 Temporary Design 3 (TMP Phase I)

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Prepared for:  
  
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NC 73-211/NC 211  
 at  
 NC 73/SR 1133 (Mode Rd)  
 Division 8 Moore County West End  
 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:  
 REVISIONS: INIT. DATE

SEAL  
  
 DATE  
 SIG. INVENTORY NO. 08-009813

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	6	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
15	5	-
31	0	-

5A

### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

\*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	-	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO <u>RUN DEFAULT PHASING</u>	1	1
ACTIVE PLAN REQUIRED TO <u>RUN ALTERNATE PHASING</u>	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0098T3  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

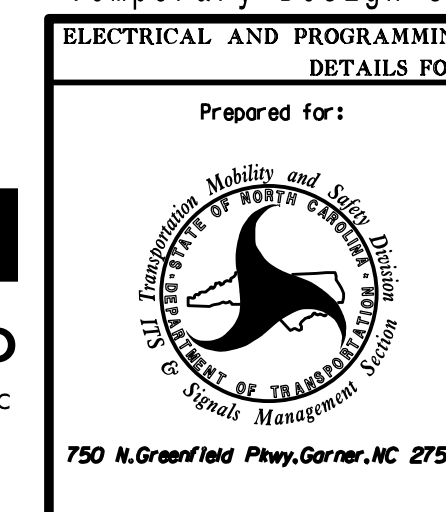
Electrical Detail - Sheet 2 of 3  
Temporary Design 3 (TMP Phase I)

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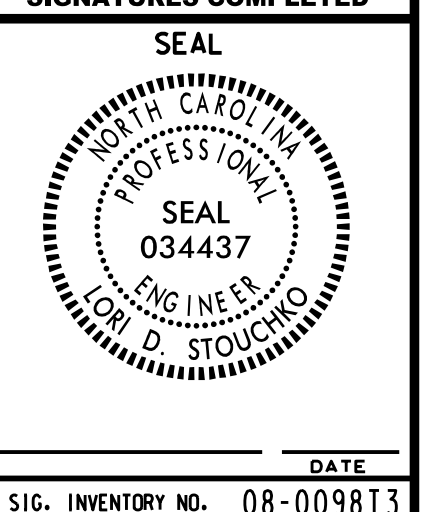
M M

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MACDONALD**

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Suite 200  
RALEIGH, NC 27606  
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NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8	Moore County
West End	
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE



SIG. INVENTORY NO. 08-0098T3

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		X	X	1
NOTICE PHASE 2 FLASH RED →	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
NOTICE PHASE 6 FLASH RED →	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.


Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-0098T3  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3  
Temporary Design 3 (TMP Phase I)

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ELECTRICAL AND PROGRAMMING  
DETAILS FOR:  
Prepared for:  
  
750 N. Greenfield Pkwy, Garner, NC 27529

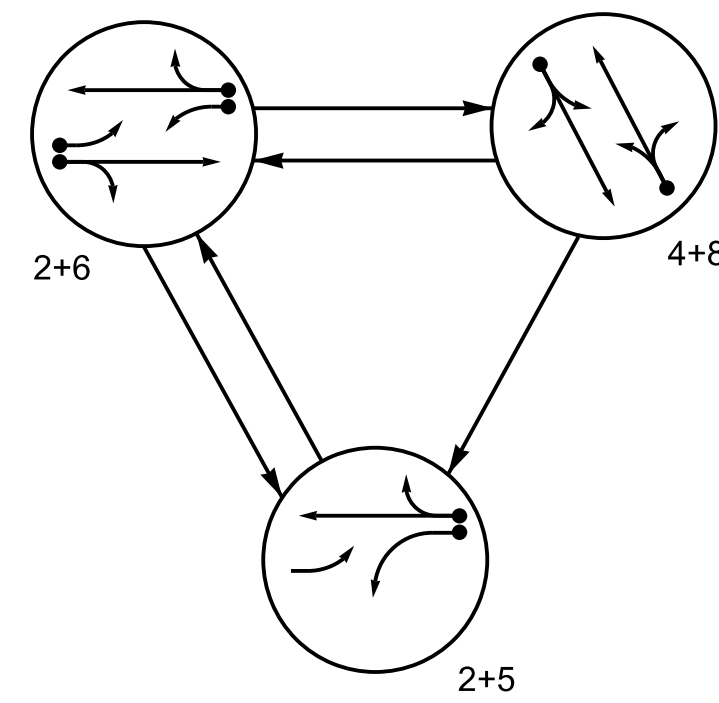
NC 73-211/NC 211  
at  
NC 73/SR 1133 (Mode Rd)  
Division 8 Moore County West End  
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
PREPARED BY: LD Stouchko REVIEWED BY:  
REVISIONS INIT. DATE  
DATE

SEAL  
NORTH CAROLINA  
PROFESSIONAL  
SEAL  
034437  
ENGINEER  
LD D. STOUCHKO

SIG. INVENTORY NO. 08-0098T3



DEFAULT PHASING DIAGRAM

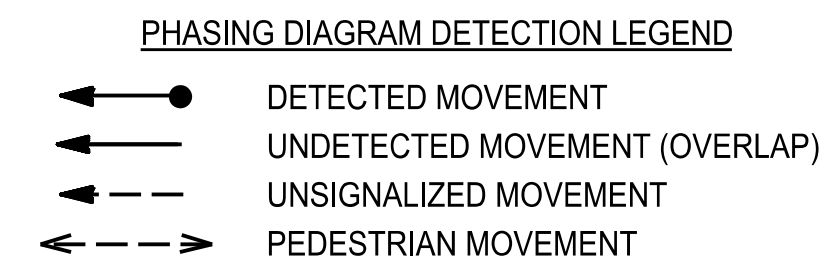
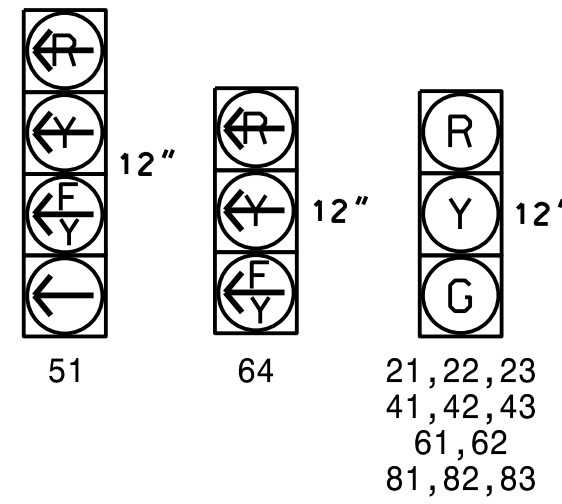


DEFAULT PHASING TABLE OF OPERATION

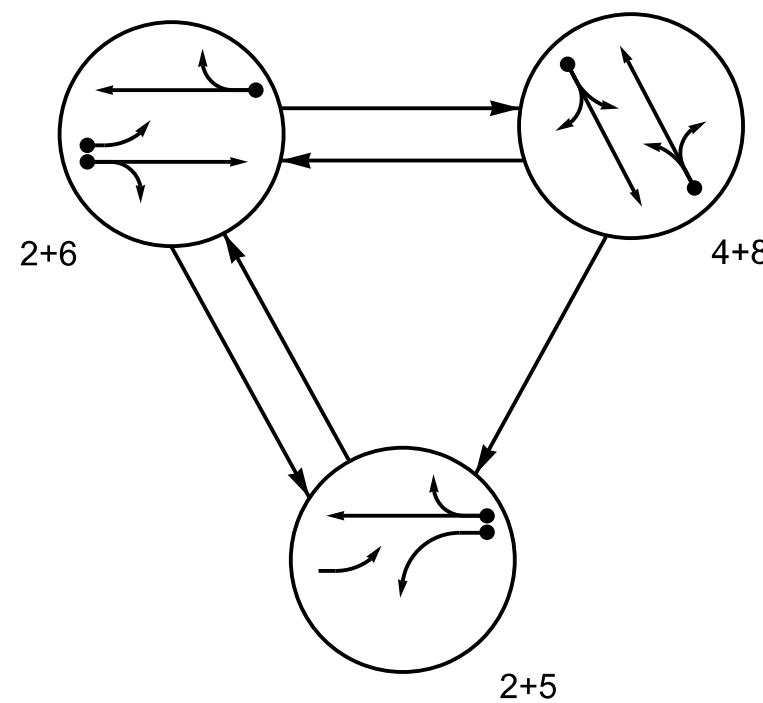
SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21, 22, 23	G	G	R	R
41, 42, 43	R	R	G	R
51	-	F	R	R
61, 62	R	G	R	R
64	F	F	R	R
81, 82, 83	R	R	G	R

SIGNAL FACE I.D.

All Heads L.E.D.



ALTERNATE PHASING DIAGRAM



ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21, 22, 23	G	G	R	R
41, 42, 43	R	R	G	R
51	-	R	R	R
61, 62	R	G	R	R
64	F	F	R	R
81, 82, 83	R	R	G	R

MAXTIME DETECTOR INSTALLATION CHART

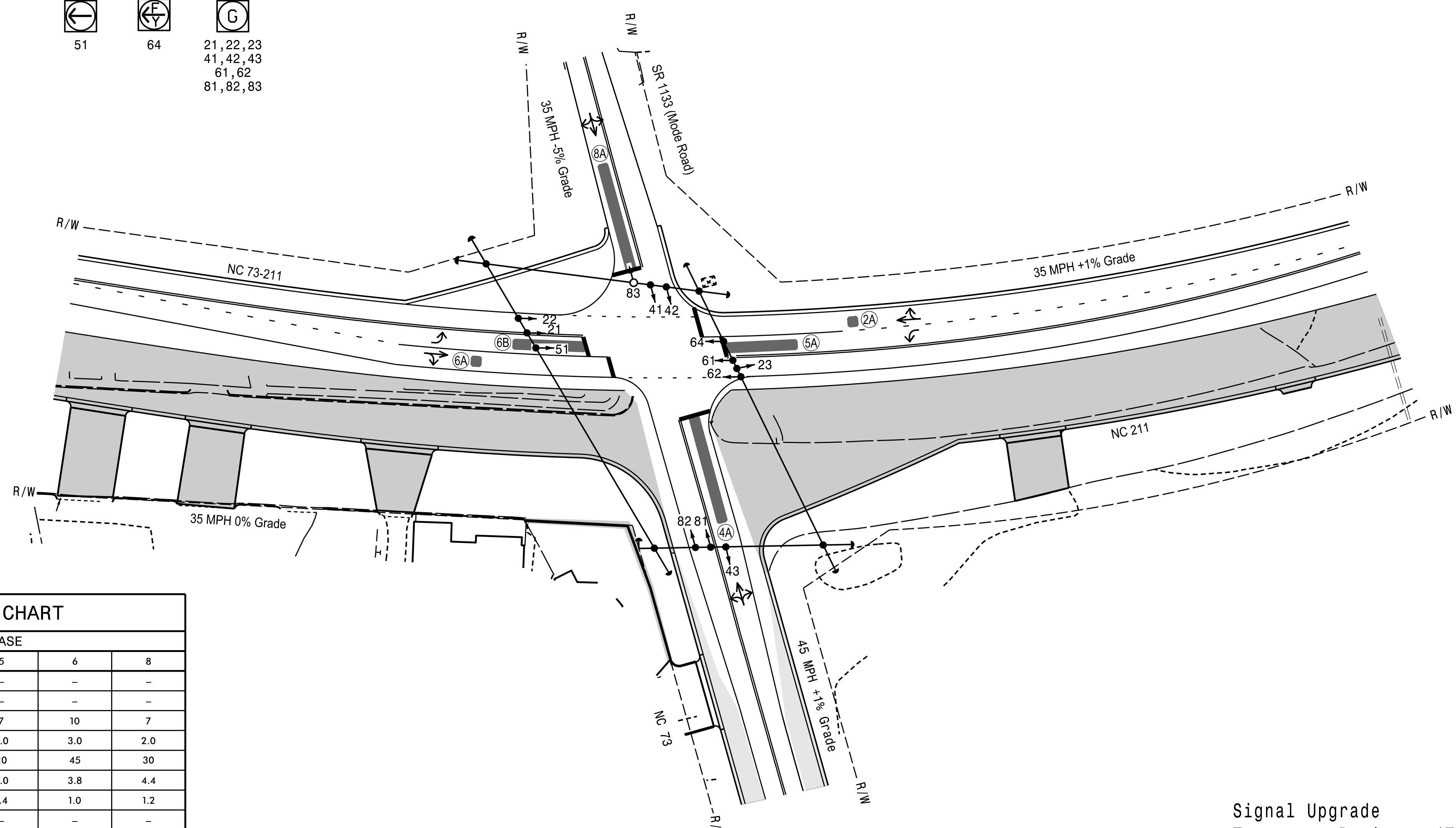
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
2A *	6X6	70	*	*	2	-	-	X	-	X	-	*
4A *	6X60	0	*	*	4	10.0	-	X	-	X	-	*
5A *	6X40	0	*	*	5	15.0#	-	X	-	X	-	*
					2##	-	-	X	-	X	-	*
6A *	6X6	70	*	*	6	-	-	X	-	X	-	*
6B *	6X40	0	*	*	6	-	-	X	-	X	-	*
8A *	6X60	0	*	*	8	10.0	-	X	-	X	-	*

# Disable Delay During Alternate Phasing Operation  
 ## Disable Phase Call for Loop During Alternate Phasing Operation  
 \* Video Detection Zone

3 Phase Fully Actuated (Isolated)

NOTES

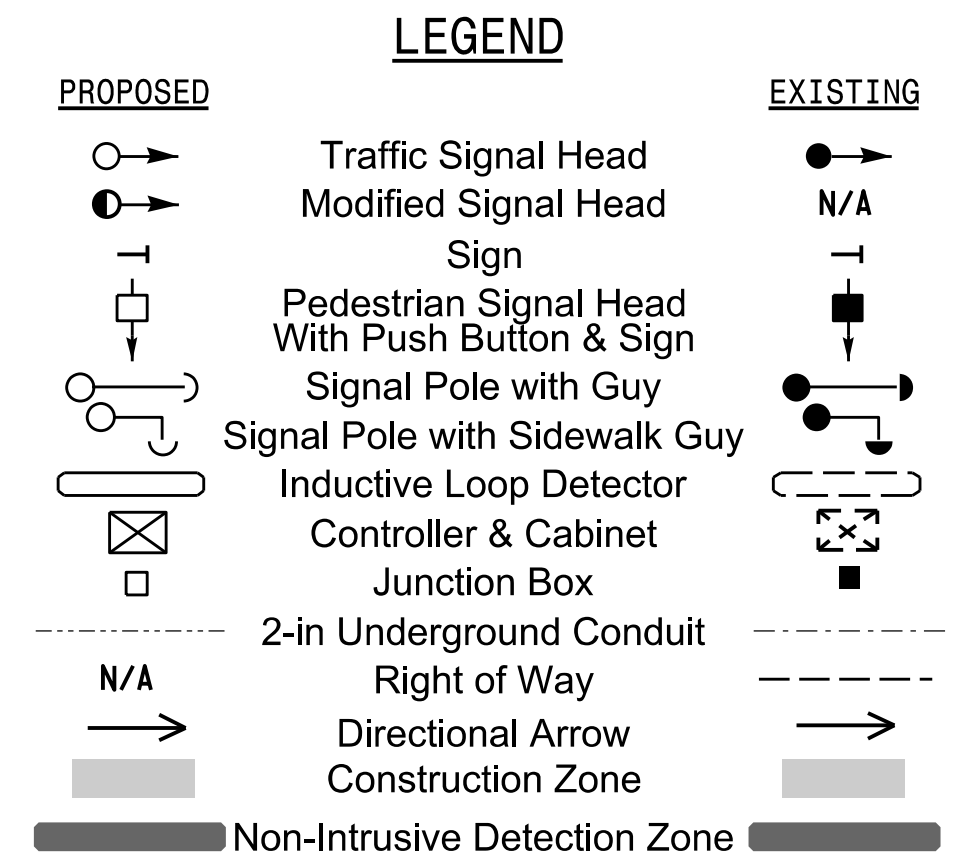
- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 5 may be lagged.
- Reposition existing signal heads numbered 21,22,23,51,61,62 and 64.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.



MAXTIME TIMING CHART

FEATURE	PHASE				
	2	4	5	6	8
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green *	10	7	7	10	7
Passage *	3.0	2.0	2.0	3.0	2.0
Max I *	45	30	20	45	30
Yellow Change	3.8	4.4	3.0	3.8	4.4
Red Clear	1.0	1.2	1.4	1.0	1.2
Added Initial *	-	-	-	-	-
Maximum Initial *	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-
Time To Reduce *	-	-	-	-	-
Minimum Gap	-	-	-	-	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	X
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	X	-	-	X

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade  
 Temporary Design 4 (TMP Phase II)

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Prepared for:  
  
 750 N. Greenfield Pkwy, Garner, NC 27526  
 SCALE: 1" = 40'

NC 73-211/NC 211  
 at  
 NC 73/SR 1133 (Mode Rd)  
 Division 8 Moore County West End  
 PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:  
 REVISIONS: \_\_\_\_\_ INIT. DATE

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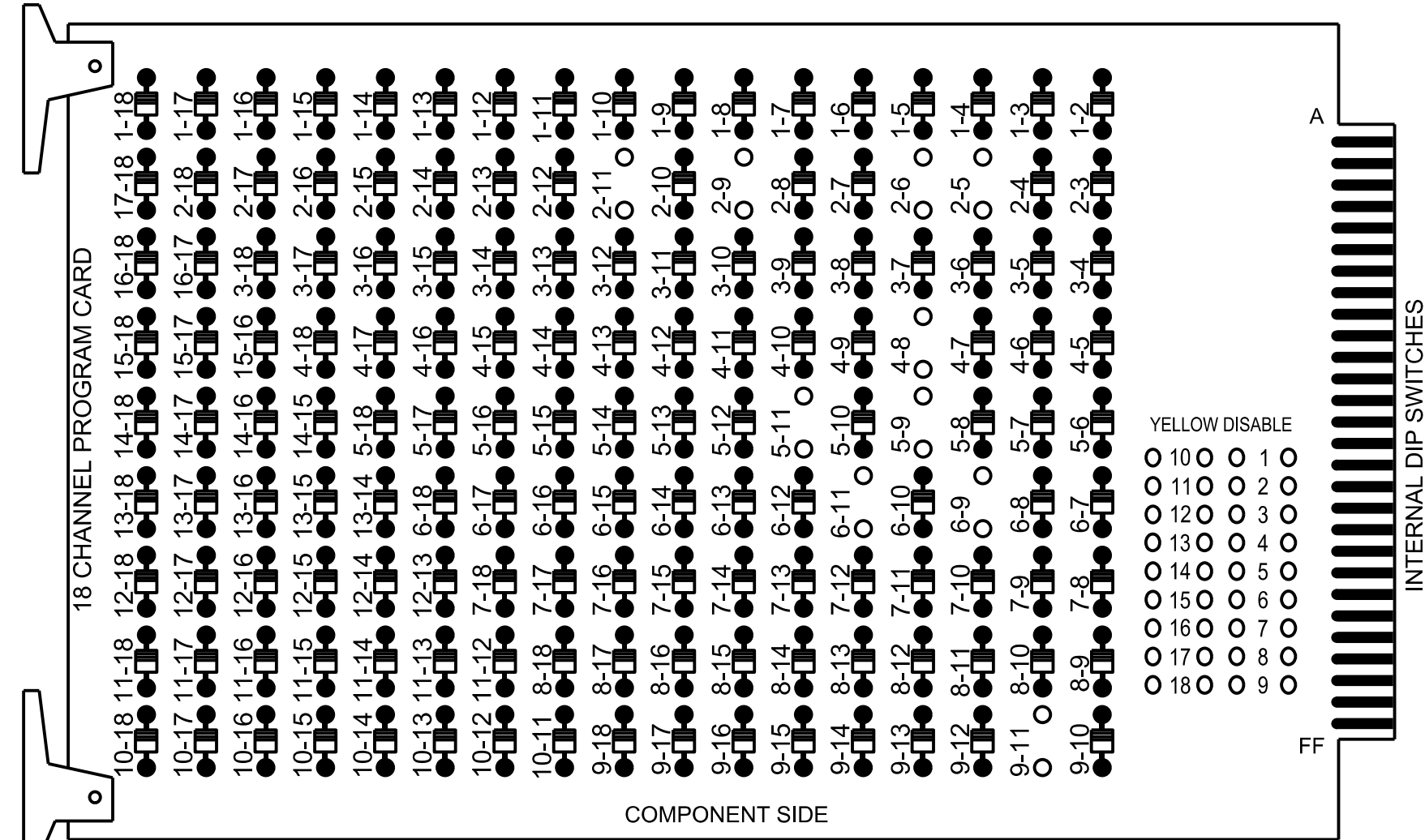
SEAL  
  
 SIGNATURE: \_\_\_\_\_ DATE: \_\_\_\_\_  
 SIG. INVENTORY NO. 08-009814

**18 CHANNEL CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

**EQUIPMENT INFORMATION**

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S2, S5, S7, S8, S11, AUX S1, AUX S4  
 Phases Used.....2, 4, 5, 6, 8  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....NOT USED

\*See overlap programming detail on sheet 2

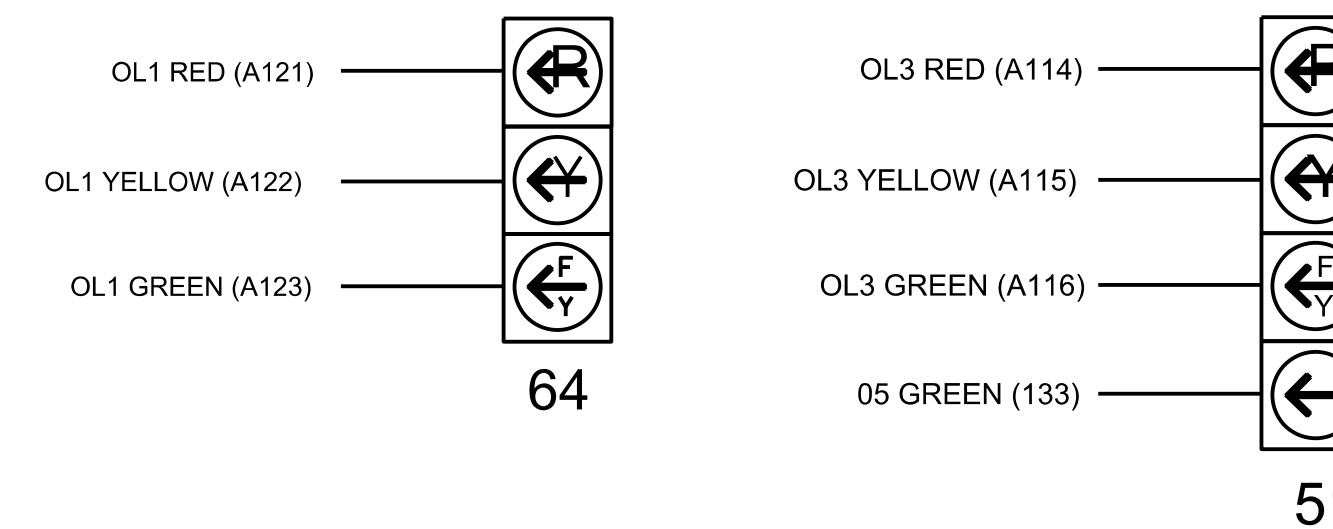
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	41,42 43	NU	51*	61,62	NU	NU	81,82 83	NU	64*	NU	NU	51*	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW								133										

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail this sheet.

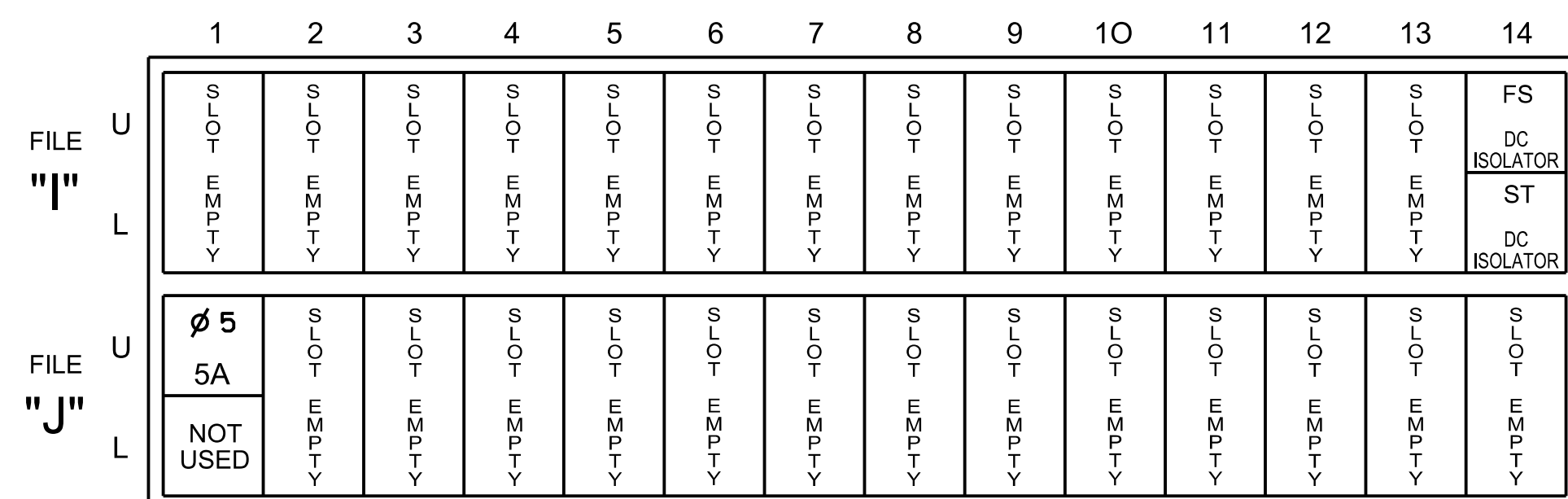
**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**INPUT FILE POSITION LAYOUT**

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

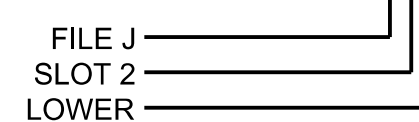
FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-12	J1U	55	17	15 *	5	15.0		X		X	
				-	31 *	2			X		X	

\* For the detectors to work as shown on the signal design plan see the Vehicle Detector Setup Programming Detail for Alternate phasing on sheet 2 of 3

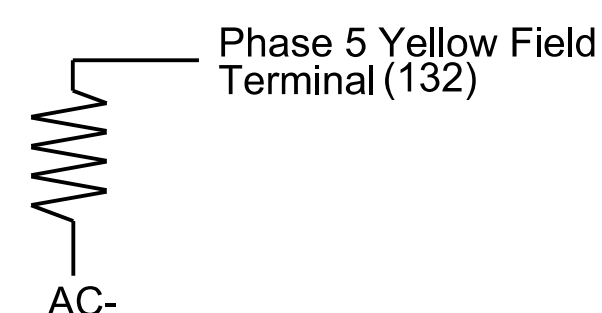
INPUT FILE POSITION LEGEND: J2L



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



**SPECIAL DETECTOR NOTES**

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

Electrical Detail - Sheet 1 of 3  
 Temporary Design 4 (TMP Phase II)

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SEAL  
  
 DATE  
 SIG. INVENTORY NO. 08-009814

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	6	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
15	5	-
31	0	-

5A

### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

\*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	-	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

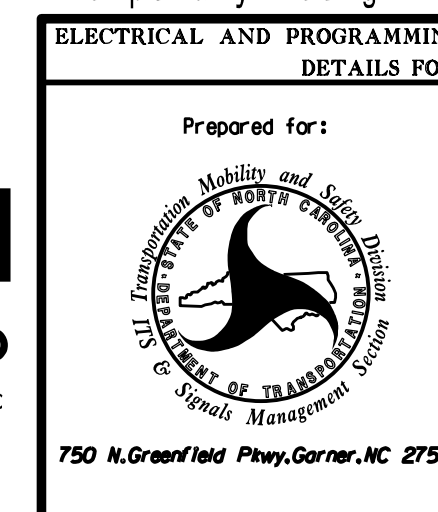
VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0098T4  
DESIGNED: June 2024  
SEALED: 7/11/2024  
REVISED:

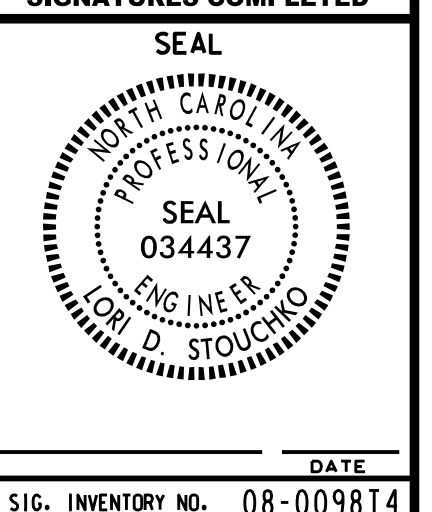
Electrical Detail - Sheet 2 of 3  
Temporary Design 4 (TMP Phase II)

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NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8	Moore County West End
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE



SIG. INVENTORY NO. 08-0098T4

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		X	X	1
NOTICE PHASE 2 FLASH RED →	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
NOTICE PHASE 6 FLASH RED →	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 08-0098T4  
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SEALED: 7/11/2024  
REVISED:

Electrical Detail - Sheet 3 of 3  
Temporary Design 4 (TMP Phase I)

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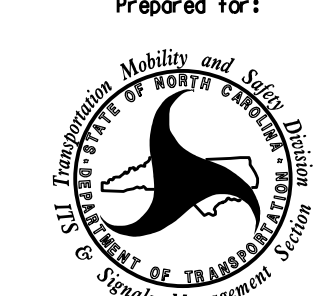
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930 Main Campus Drive  
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ELECTRICAL AND PROGRAMMING  
DETAILS FOR:

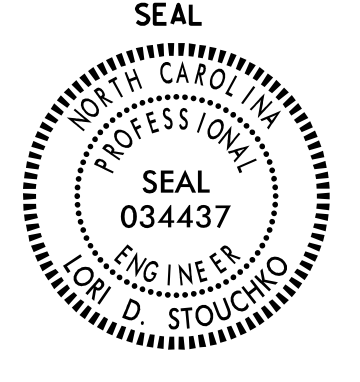
Prepared for:



750 N. Greenfield Pkwy, Garner, NC 27529

NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)	
Division 8	Moore County
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL

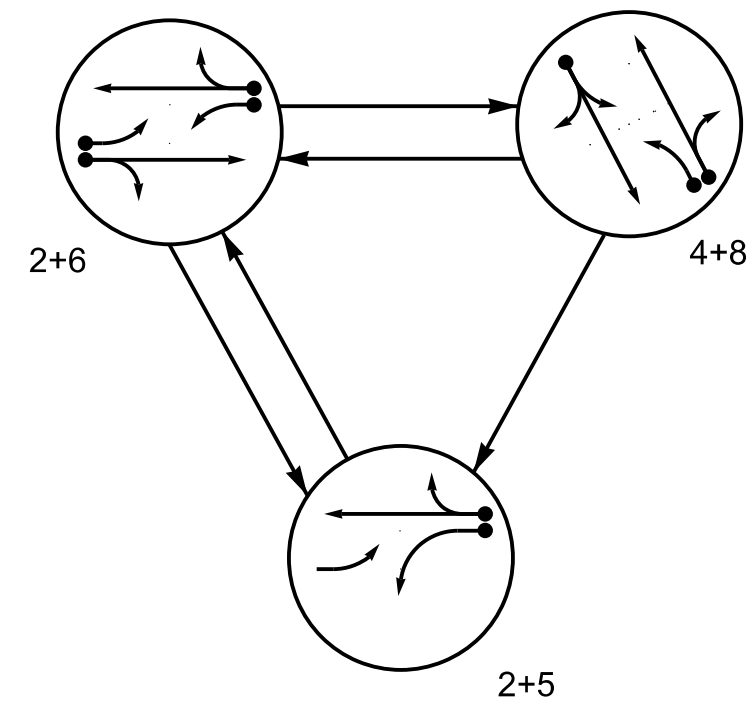


DATE

SIG. INVENTORY NO. 08-0098T4

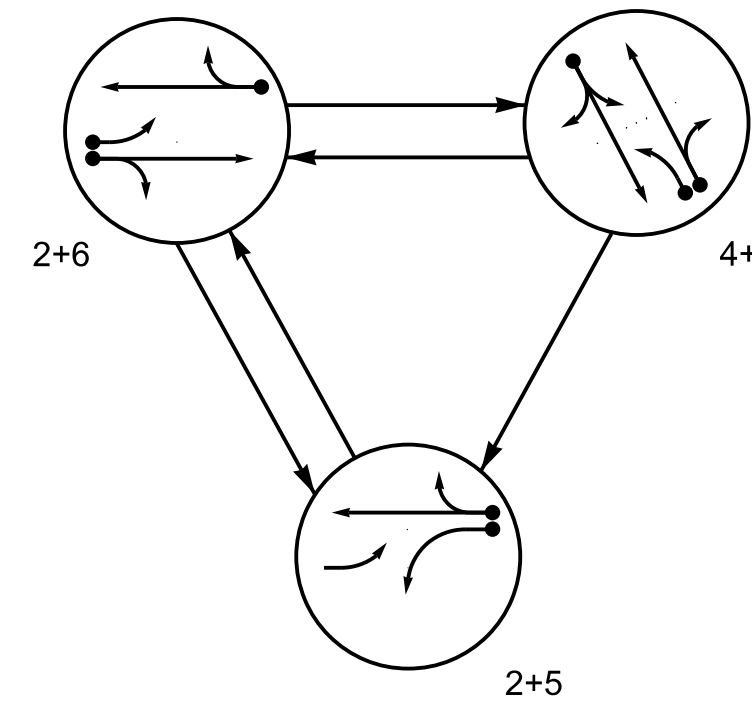


DEFAULT PHASING DIAGRAM



SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21, 22, 23	G	G	R	R
41, 42, 43	R	R	G	R
44	R	R	F	R
51	F	F	R	R
61, 62	R	G	R	R
64	F	F	R	R
81, 82, 83	R	R	G	R

ALTERNATE PHASING DIAGRAM



SIGNAL FACE	PHASE			
	2+5	2+6	4+8	FLASH
21, 22, 23	G	G	R	R
41, 42, 43	R	R	G	R
44	R	R	F	R
51	F	F	R	R
61, 62	R	G	R	R
64	F	F	R	R
81, 82, 83	R	R	G	R

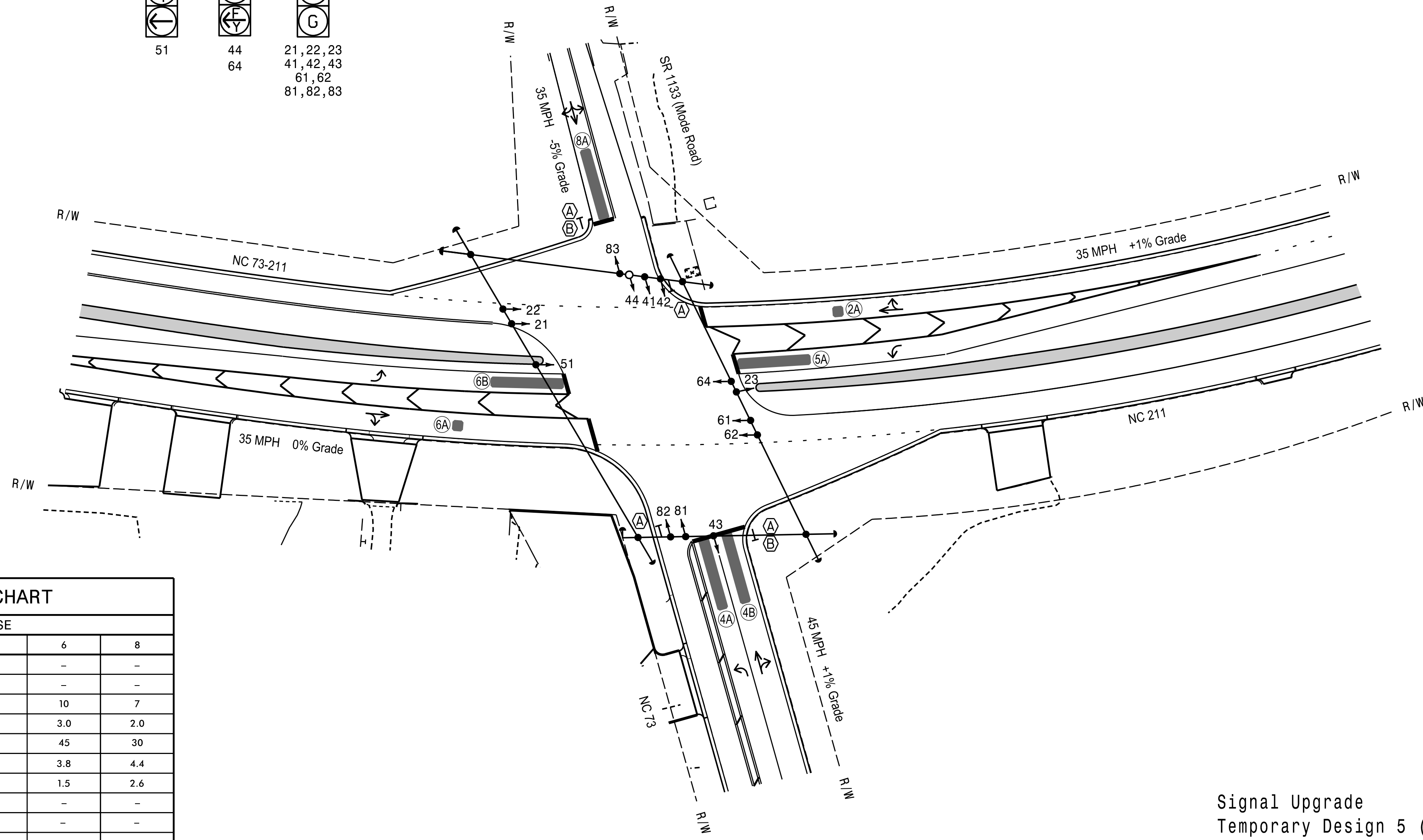
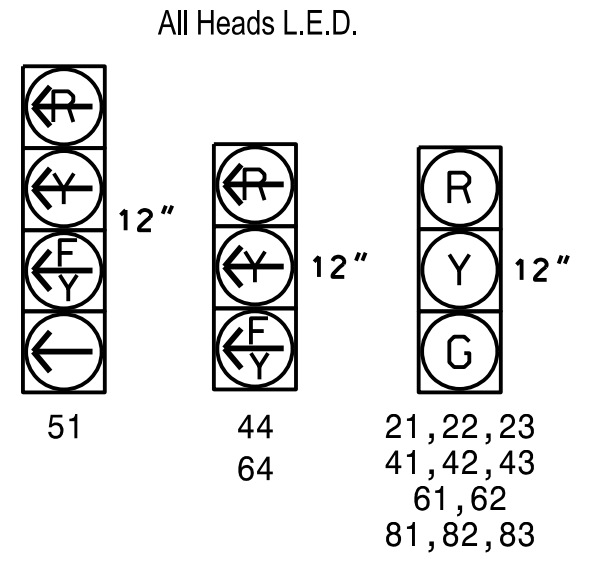
MAXTIME DETECTOR INSTALLATION CHART											
DETECTOR					PROGRAMMING						
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A *	6X6	70	*	*	2	-	-	X	-	X	-
4A *	6X40	0	*	*	4	3.0	-	X	-	X	-
4B *	6X40	0	*	*	4	-	-	X	-	X	-
5A *	6X40	0	*	*	5	15.0#	-	X	-	X	-
6A *	6X6	70	*	*	6	-	-	X	-	X	-
6B *	6X40	0	*	*	6	-	-	X	-	X	-
8A *	6X40	0	*	*	8	3.0	-	X	-	X	-

# Disable Delay During Alternate Phasing Operation  
 ## Disable Phase Call for Loop During Alternate Phasing Operation  
 \* Video Detection Zone

PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

SIGNAL FACE I.D.

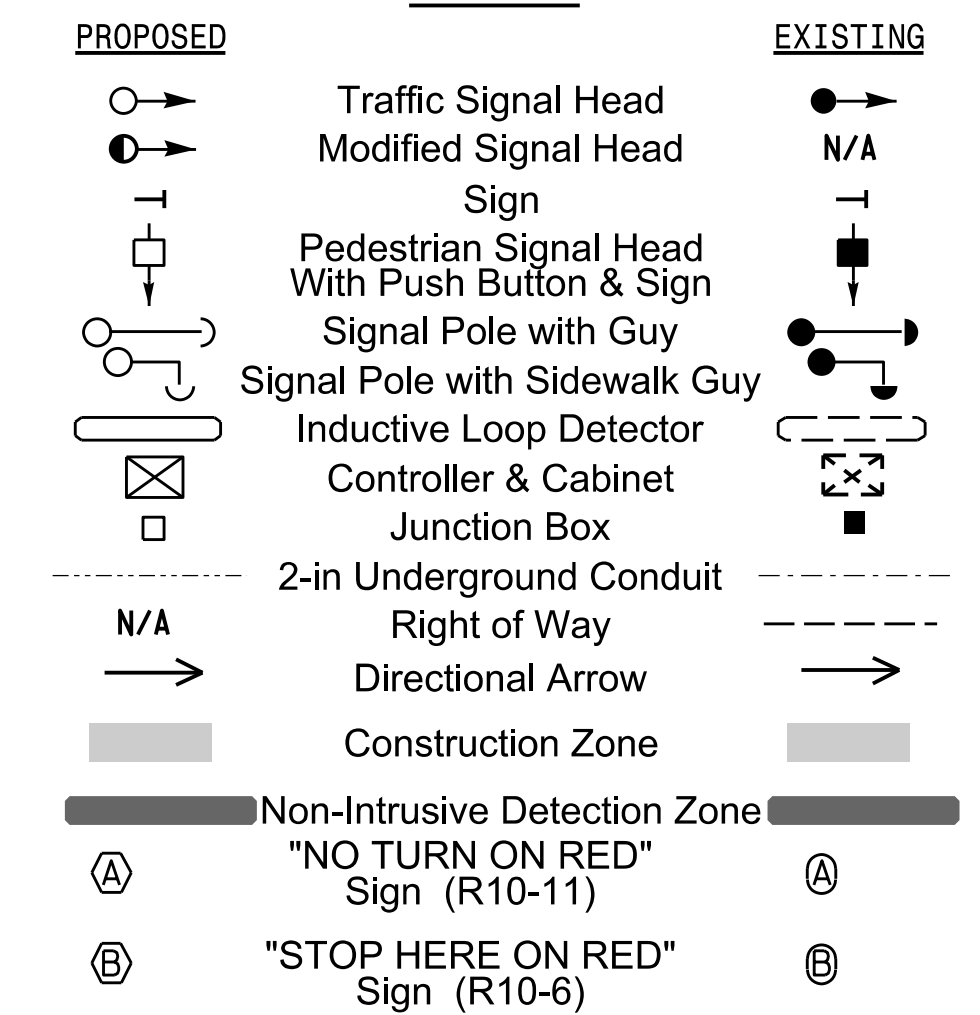


3 Phase Fully Actuated (Isolated)

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 5 may be lagged.
4. Reposition existing signal heads numbered 21, 22, 23, 51, 61, 62 and 64.
5. Set all detector units to presence mode.
6. The Division Traffic Engineer will determine the hours of use for each phasing plan.
7. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

LEGEND



FEATURE	PHASE				
	2	4	5	6	8
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green *	10	7	7	10	7
Passage *	3.0	2.0	2.0	3.0	2.0
Max 1 *	45	30	20	45	30
Yellow Change	3.8	4.4	3.0	3.8	4.4
Red Clear	1.5	2.6	2.3	1.5	2.6
Added Initial *	-	-	-	-	-
Maximum Initial *	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-
Time To Reduce *	-	-	-	-	-
Minimum Gap	-	-	-	-	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	X
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	X	-	-	X

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Signal Upgrade  
 Temporary Design 5 (TMP Phase III)

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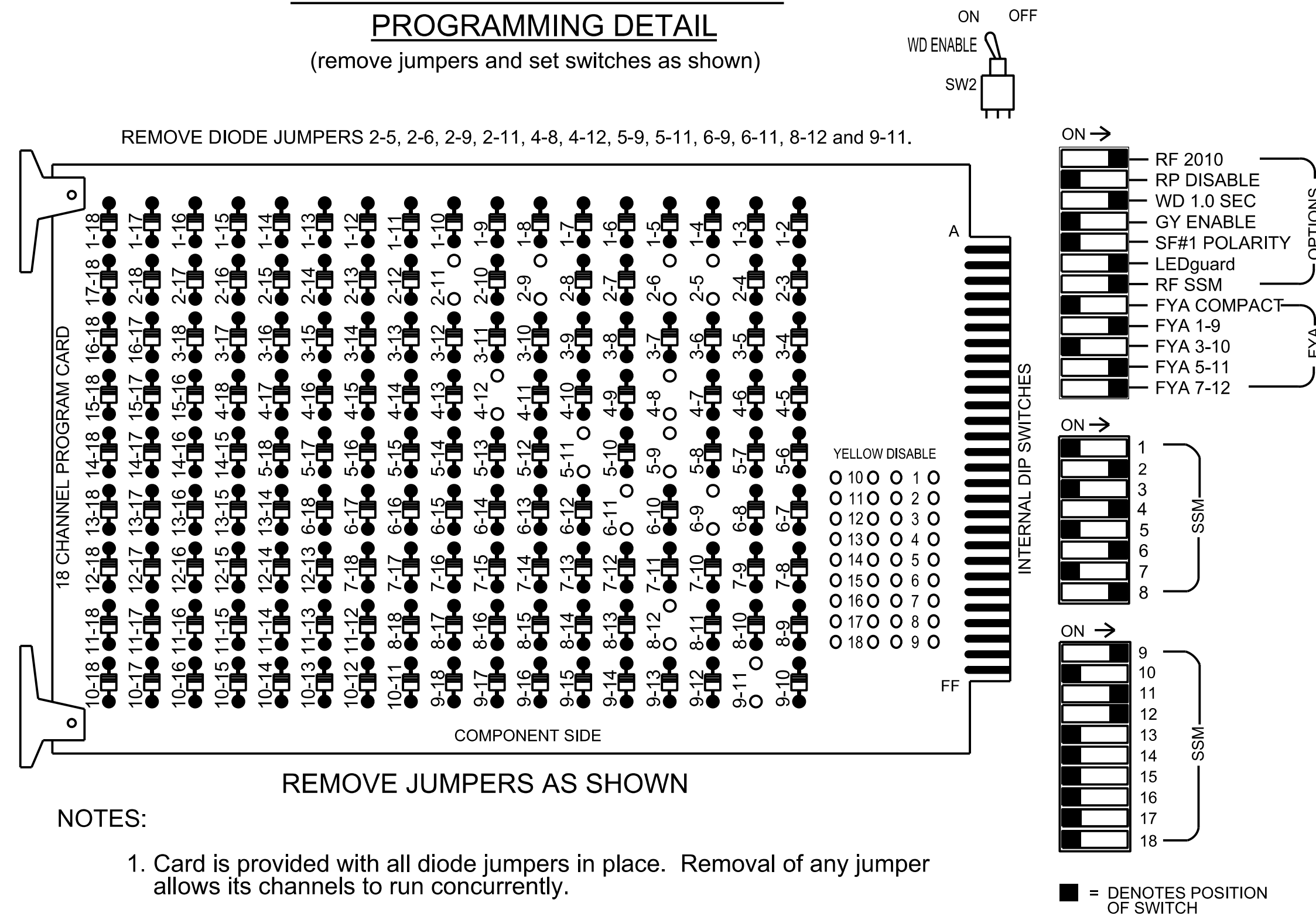
NC 73-211/NC 211 at NC 73/SR 1133 (Mode Rd)		
Division 8 Moore County West End		
PLAN DATE: June 2024	REVIEWED BY: R. Mullinax	
PREPARED BY: LD Stouchko	REVIEWED BY:	
REVISIONS	INIT.	DATE

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SIGNATURE DATE  
 SIG. INVENTORY NO. 08-009815

### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S2, S5, S7, S8, S11, AUX S1, AUX S4, AUX S5  
 Phases Used.....2, 4, 5, 6, 8  
 Overlap "1".....\*  
 Overlap "2".....NOT USED  
 Overlap "3".....\*  
 Overlap "4".....\*

\*See overlap programming detail on sheet 2

### SIGNAL HEAD HOOK-UP CHART

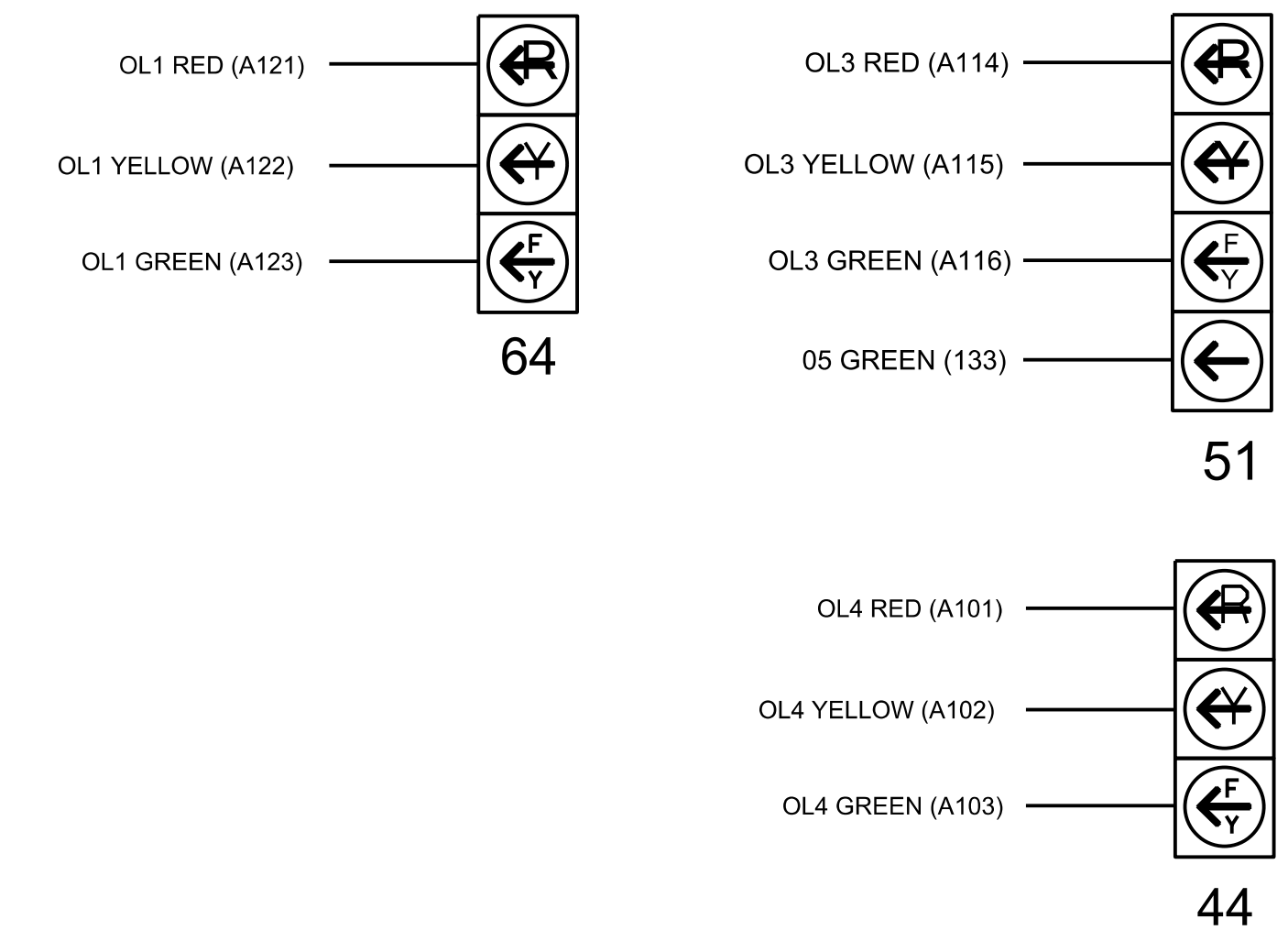
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22,23	NU	NU	41,42,43	NU	51*	61,62	NU	NU	81,82,83	NU	64*	NU	NU	51*	44*	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114	A101	
YELLOW ARROW													A122			A115	A102	
FLASHING YELLOW ARROW													A123			A116	A103	
GREEN ARROW								133										

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.  
 \*See pictorial of head wiring in detail this sheet.

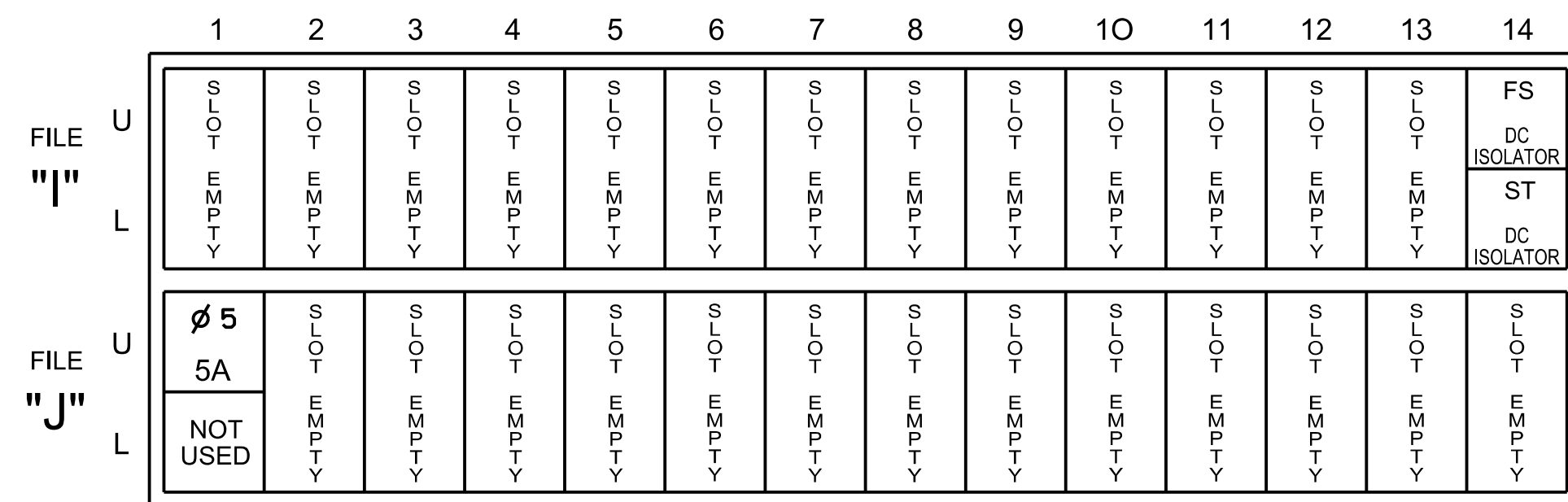
### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)



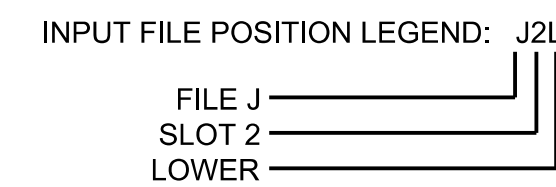
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-1,2	J1U	55	17	15 *	5	15.0		X		X	
				-	31 *	2			X		X	

\* For the detectors to work as shown on the signal design plan see the Vehicle Detector Setup Programming Detail for Alternate phasing on sheet 2 of 3



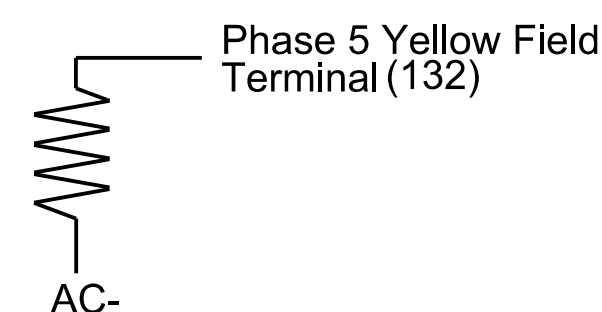
### SPECIAL DETECTOR NOTES

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-009815  
 DESIGNED: June 2024  
 SEALED: 7/11/2024  
 REVISED:

Electrical Detail - Sheet 1 of 3  
 Temporary Design 5 (TMP Phase III)

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Prepared for:  
  
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NC 73-211/NC 211  
 at  
 NC 73/SR 1133 (Mode Rd)

Division 8 Moore County West End

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax  
 PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS	INIT.	DATE

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 DATE  
 SIG. INVENTORY NO. 08-009815

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	6	8
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
15	5	-
31	0	-

5A

### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

\*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	-	-	8
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE  
← NOTICE MODIFIER PHASE

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO <u>RUN DEFAULT PHASING</u>	1	1
ACTIVE PLAN REQUIRED TO <u>RUN ALTERNATE PHASING</u>	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

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### FLASHER CIRCUIT MODIFICATION DETAIL

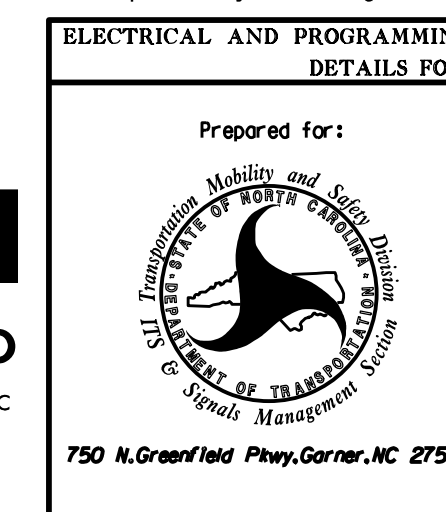
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

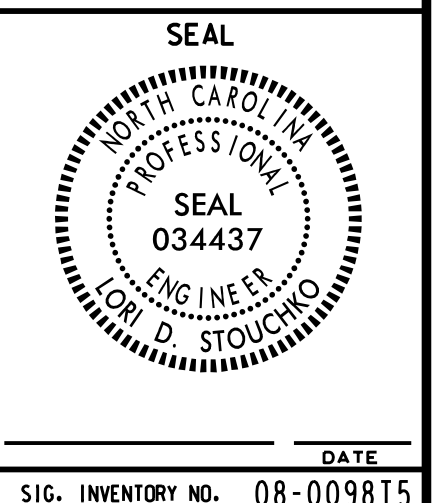
Electrical Detail - Sheet 2 of 3  
Temporary Design 5 (TMP Phase III)

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SIG. INVENTORY NO. 08-009815

**MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL**

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Startup Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

**OUTPUT CHANNEL CONFIGURATION**

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

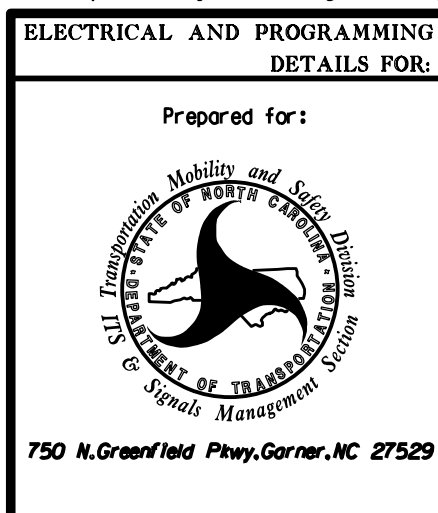
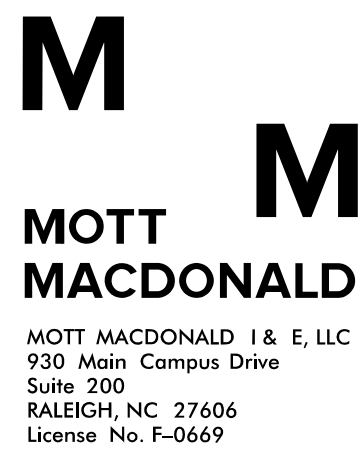
Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
NOTICE PHASE 2 FLASH RED →	1	Phase Vehicle	1		X	X	1
	2	Phase Vehicle	2		X		2
	3	Phase Vehicle	3		X	X	3
	4	Phase Vehicle	4		X		4
NOTICE PHASE 6 FLASH RED →	5	Phase Vehicle	5		X		5
	6	Phase Vehicle	6		X	X	6
	7	Phase Vehicle	7		X		7
	8	Phase Vehicle	8		X	X	8
NOTICE OVERLAP 1 FLASH RED →	9	Overlap	1		X	X	9
	10	Overlap	2		X	X	10
NOTICE OVERLAP 3 FLASH RED →	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	X	17
	18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR  
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Electrical Detail - Sheet 3 of 3  
Temporary Design 5 (TMP Phase I)

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