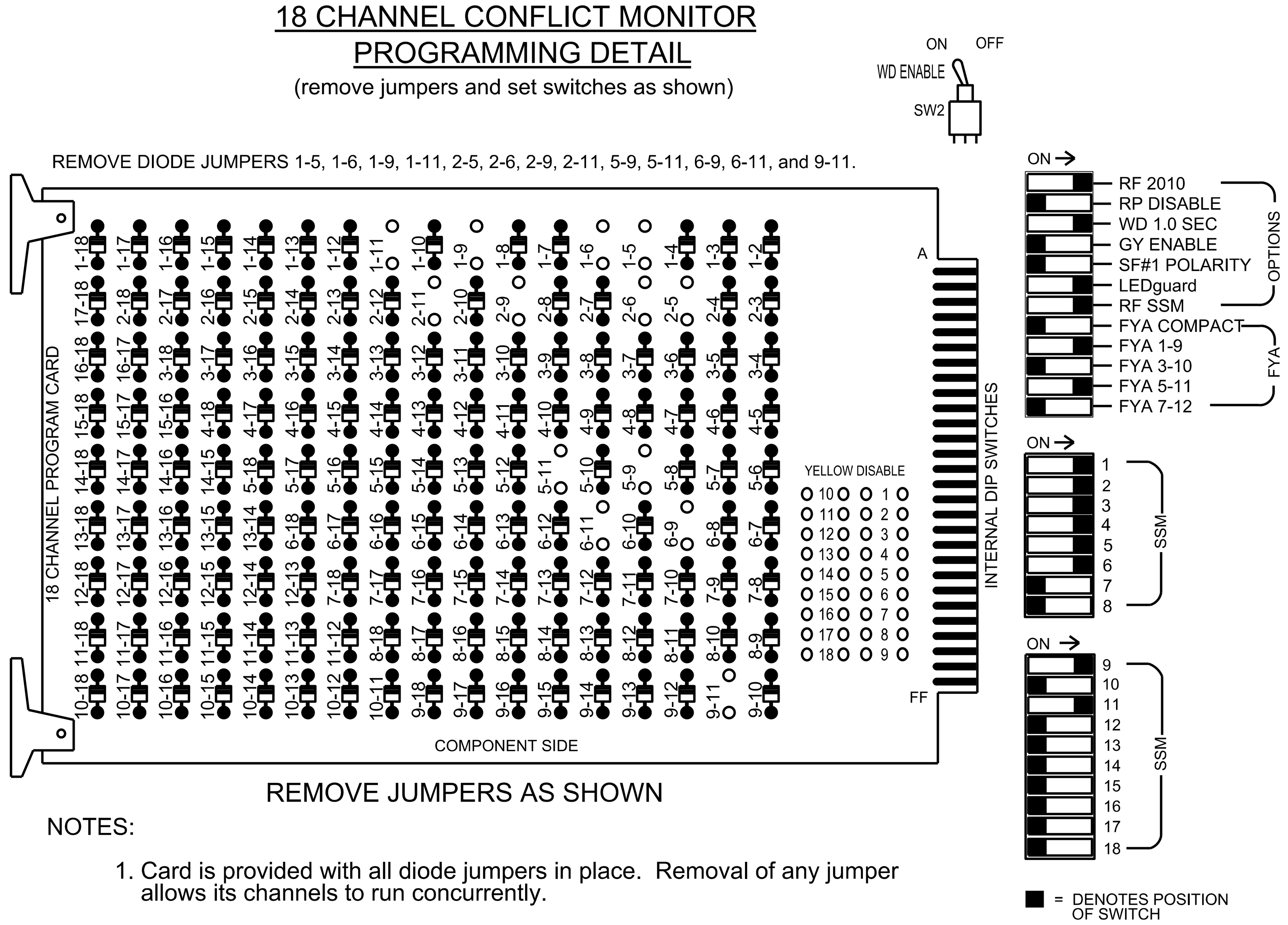
18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 2 Green No Walk, 6 Green No Walk, 39 Phase Not On, and 40 Green No Walk.
3. Program Phase 39 for No Startup Veh Call.
4. Program Phase 40 for Min Recall.
5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller..2070LX
Cabinet...332 w/ Aux
Software...Q-Free MAXTIME
Cabinet Mount....................................Base
Output File Positions...........................18 With Aux. Output File
Load Switches Used...........................S1, S2, S4, S5, S7, S8, AUX S1, AUX S4
Phases Used.......................................1, 2, 3, 4, 5, 6, 39**, 40**
Overlap "1"..*
Overlap "2"..NOT USED
Overlap "3"..*
Overlap "4"..NOT USED

*See overlap programming detail on sheet 2
**Phase used for preemption timing purposes only

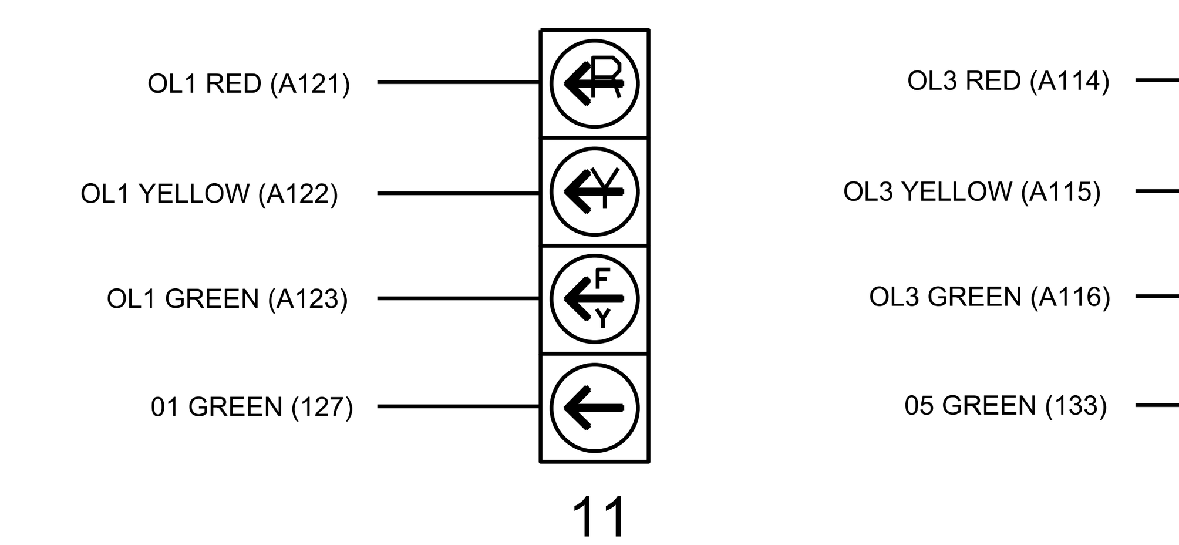
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4				S5		S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3				4		14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3				4		4 PED	5		6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11★	42	21.22	NU	31	32	33	62	41	42,43	NU	33	51★	61,62	NU	NU	NU	NU	11★	NU	NU	51★	NU	NU
RED		*	128			116	116		101	101		*		134										
YELLOW			129			117	117		102	102				135										
GREEN			130			118	118		103	103				136										
RED ARROW					116														A121			A114		
YELLOW ARROW		126			117			117				132							A122			A115		
FLASHING YELLOW ARROW																			A123			A116		
GREEN ARROW	127	127			118	118		118	103			133	133											
(hand)																								
(walk)																								

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE		1	2	3	4	5	6	7	8	9	1O	11	12	13	14
"I"	U	ø 1 1A	ø 2 2A	SLOT EMPTY	SLOT EMPTY	ø 3 3A	ø 4 4A	ø 1 1B	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
"I"	L	NOT USED	ø 2 2B			NOT USED	ø 4 4B	ø 1 1C							ST DC ISOLATOR
"J"	U	ø 5 5A	ø 6 6A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	ø 3 3B	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	PRE1 AC ISOLATOR
"J"	L	NOT USED	ø 6 6B				ø 5 5B								NOT USED

EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME
PRE = PREEMPT

INPUT FILE CONNECTION & PROGRAMMING CHART

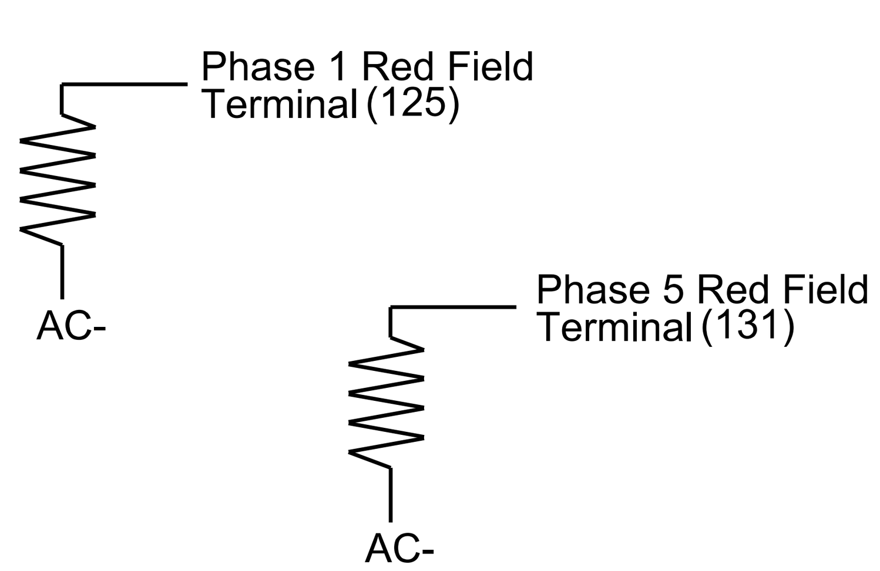
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	15.0		X		X	
				-	29	6	3.0		X		X	X
1B	TB6-1,2	I7U	65	31	10	1	15.0	2.0	X		X	
1C	TB6-3,4	I7L	78	44	11	1	15.0		X		X	
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
3A	TB4-5,6	I5U	58	20	7	3	3.0		X		X	
3B	TB5-9,10	J6U	42	4	22	3			X		X	
4A	TB4-9,10	I6U	41	3	8	4		2.0	X		X	
4B	TB4-11,12	I6L	45	7	9	4	3.0		X		X	
5A	TB3-1,2	J1U	55	17	15	5	15.0		X		X	
				-	31	2	3.0		X		X	X
5B	TB5-11,12	J6L	46	8	23	5	15.0		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



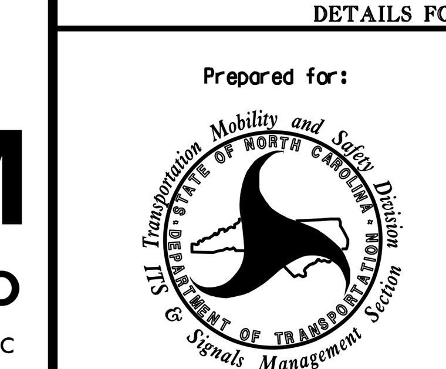
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 08-0410
DESIGNED: June 2024
SEALED: 7/11/2024
REVISED:

MOTT MACDONALD
MOTT MACDONALD I & E, LLC
930 Main Campus Drive
Suite 200
RALEIGH, NC 27606
License No. F-0669

Electrical Detail - Sheet 1 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for:



750 N.Greenfield Pkwy,Garner,NC 27529

NC 211
at
SR 1239 (Seven Lakes Drive)
and SR 1190 (Lakeway Drive)

Division 8 Moore County Seven Lakes

PLAN DATE: June 2024 REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 034437 ENGINEER LORI D. STOUCHKO
DocuSigned by: Lori D. Stouchko
FF586C7596C645A...
11-Jul-2024
DATE

SIG. INVENTORY NO. 08-0410