OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Tin

Web Interface

Home >Controller >Overlap Configuration >Overlaps Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section	OFF	FYA 4 - Section	OFF
Included Phases	2	-	6	-
Modifier Phases	1	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATIO

Front Panel

Main Menu >Controller >More>Channels>Channels Co

Web Interface Home >Controller >Advanced IO>Channels>Channels

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash
	1	Phase Vehicle	1		Х
NOTICE PHASE 2	2	Phase Vehicle	2		Х
FLASH RED	3	Phase Vehicle	3		Х
	4	Phase Vehicle	4		Х
NOTICE	5	Phase Vehicle	5		Х
PHASE 6	6	Phase Vehicle	6		Х
FLASH RED	7	Phase Vehicle	7		Х
NOTICE OVERLAP 1 FLASH RED	8	Phase Vehicle	8		Х
	9	Overlap	1		Х
	10	Overlap	2		Х
NOTICE OVERLAP 3	11	Overlap	3		Х
FLASH RED	12	Overlap	4		Х
	13	Phase Ped	2		
	14	Phase Ped	4		
	15	Phase Ped	6		
	16	Phase Ped	8		
	17	Overlap	5		Х
	18	Overlap	6		Х

mings	MAXTIME STARTUP A PROGRAM Front Panel Main Menu >Controller >Unit Web Interface Home >Controller >Unit Modify parameters as shown Startup Parameters Startup Clearance Hold 6
DN onfig Configuration	
h Red Flash Alt MMU Channel X X 1 X 2 X X 3 X X 3 X X 3 X X 5 X X 6 X X 6 X X 8 X X 9 X X 10 X X 10 X 11 12 13 14 15 16 X X 17 X 18	THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0410TI DESIGNED: June 2024 SEALED: 7/11/2024 REVISED:

	PROJECT REFERENCE NO.	SHEET NO.
	R - 5726	Sig.16.2
AND SOFTWARE FLASH		
MMING DETAIL		
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wn below and save changes.		
Unit Flash Parameters		
All Red Flash Exit Time		
6		
trical Detail - Sheet 2 of 3	DOCUMENT NOT	CONSIDERED
trical Detail - Sheet 2 of 3 orary Design 1 (TMP Phase I)	FINAL UNLE SIGNATURES C	ESS ALL OMPLETED
RICAL AND PROGRAMMING DETAILS FOR: At	SEAL	TITA
Prepared for: SP 1230 (Soven Lakes		N

