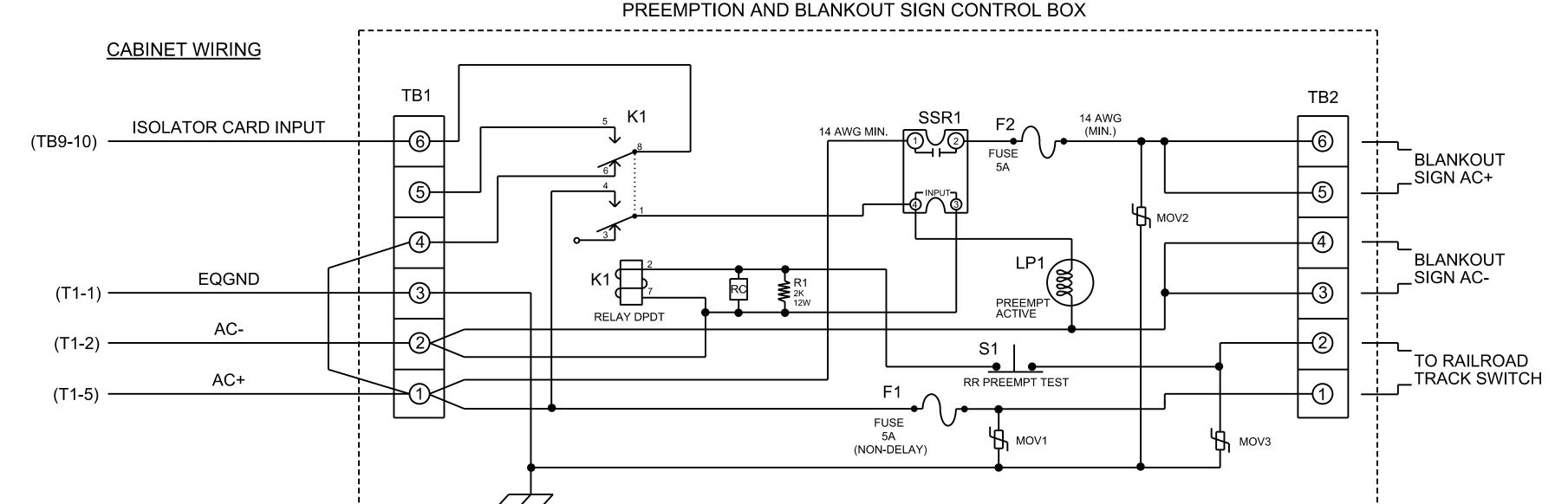
RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

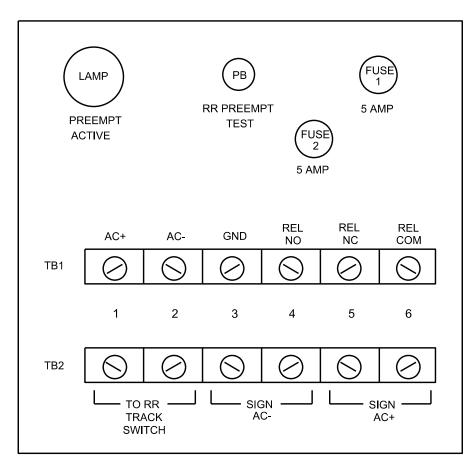


NOTES

- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to

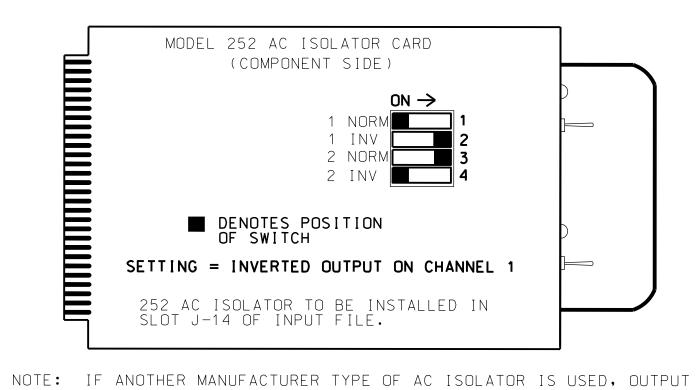
- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC neutral (jumper may have to be added).

FRONT VIEW



PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

SEALED: 7/11/2024 REVISED: Electrical Detail - Sheet 3 of 3

DESIGNED: June 2024

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 08-1103

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for:

MOTT

MACDONALD

MOTT MACDONALD I & E, LLC 930 Main Campus Drive Suite 200 RALEIGH, NC 27606

NC 211/NC 73-211 NC 73 (South Intersection) Division 8 Moore County

June 2024

PREPARED BY: LD Stouchko

REVISIONS

PLAN DATE:

West End REVIEWED BY: R. Mullinax REVIEWED BY: INIT. DATE SIG. INVENTORY NO. 08-1103

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 034437

SEQUENCE DETAIL

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

Home >Controller >Sequence

Sequence 1

| Ring | Sequence Data | |
|------|---------------|--|
| 1 | 1,2,a,3,4,b | |
| 2 | 5,6,a,7,8,b | |
| 3 | 39.c.40.d | |

PREEMPTION PROGRAMMING

Front Panel

Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

Web Interface

Home >Controller >Preempt Configuration >Preempts

Preempt Configuration

| Preempt | 1 |
|-------------------------|-------------|
| Enabled | Enabled |
| Туре | Rail Road |
| Track Phases | 4,39 |
| Track Overlaps | - |
| Dwell Phases | 2,6 |
| Dwell Peds | ± |
| Dwell Overlaps | 3 |
| Cycling Phases | - |
| Cycling Peds | - |
| Cycling Overlaps | - |
| Exit Phases | 4 |
| Exit Overlaps | 4 |
| Delay | 0 |
| Call Ext Time | 1.0 |
| Max Presence | 0 |
| Max Pres Act | Terminate |
| Enter Min Green | 1 |
| Enter Walk | 0 |
| Enter Ped Clear | 0 |
| Enter Yellow Change | 4.5 |
| Enter Red Clear | 3.1 |
| Track Green | 27 |
| Track Yellow Clr | 3.8 |
| Track Red Clear | 2.0 |
| Dwell Green | 0 |
| Exit Min Green | 255 |
| Exit Yellow Change | 25.5 |
| Exit Red Clear | 25.5 |
| Exit Type | Exit Phases |
| Non Locking Memory | 1 |
| Not Ovrd Flash | X |
| Not Ovrd Nxt Pre | - |
| Require All Red Entry | - |
| Track Clear Ovrd | Х |
| Ped Clear During Yellow | - |
| Entry Omit OLTG | Х |
| Track Reserve | X |
| | |