R-5726 Sig.5.

OVERLAP PROGRAMMING

Front Panel

Main Menu > Controller > Overlap > Overlap Parameters / Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	FYA 4 - Section
Included Phases	2	1	6	2,4
Modifier Phases	1	-	5	-
Modifier Overlaps	=	=	•	•
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

	Startup Parameters
	Startup Clearance Hold
Γ	6

Unit Flash Parameters
All Red Flash Exit Time

OUTPUT CHANNEL CONFIGURATION

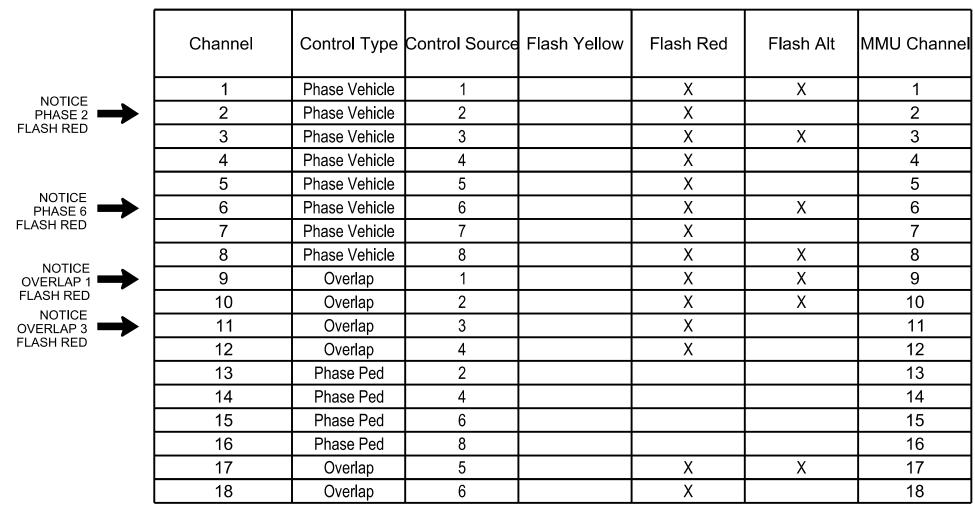
Front Pane

Main Menu >Controller >More>Channels>Channels Config

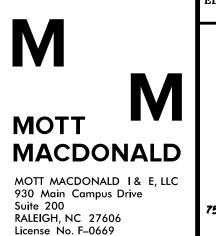
Web Interface

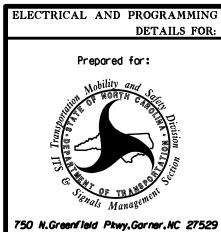
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1103
DESIGNED: June 2024
SEALED: 7/11/2024
REVISED:





Electrical Detail - Sheet 2 of 3

NC 211/NC 73-211

at

NC 73 (South Intersection)

Division 8 Moore County West

Division 8 Moore County West End
PLAN DATE: June 2024 REVIEWED BY: R. Mullinax
PREPARED BY: LD Stouchko REVIEWED BY:
REVISIONS INIT. DATE

SEAL
034437

Op. NG INE E.

Docusigned D. STOUCH

Lori D. Stouchko

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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED