Docusign Envelope ID: E19AB3D8-A0DD-425E-9486-D6179FF196BF

JECT REFERENCE NO. SHEET NO. Sig.2.2

MAXTIME OVERLAP PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2	3	4	7
Туре	FYA 4 - Section	FYA 4 - Section	NORMAL	NORMAL
Included Phases	6	6	4, 5	4
Modifier Phases	÷	5	-	<u>-</u>
Modifier Overlaps	7	÷	<u>-</u>	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

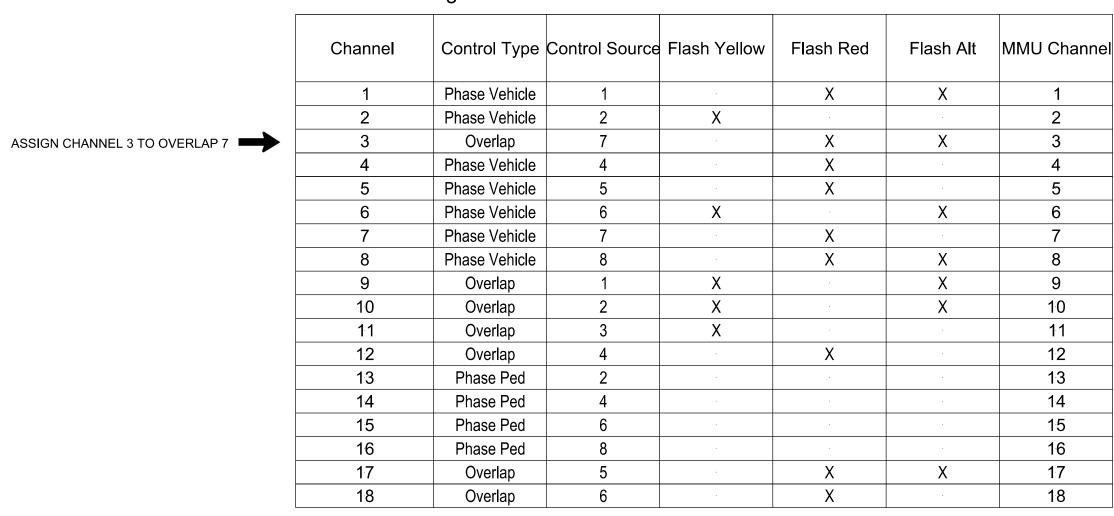
Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-0422T
DESIGNED: April 2024
SEALED: 4/18/2024



Electrical Detail-Sheet 2 of 2-Temporary Design (TCP Phase I)

ELECTRICAL AND PROGRAMMING DETAILS FOR:

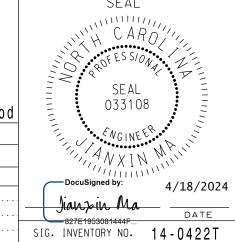
REVISED: N/A



US 19 (Soco Road) /
US 19-276 (Dellwood Road)
at

US 276 (Johnathan Creek Road)
Division 14 Haywood County Dellwood
PLAN DATE: April 2024 REVIEWED BY: M.L. Stygles

PLAN DATE: April 2024 REVIEWED BY: M.L. Stygles
PREPARED BY: J. Ma REVIEWED BY: J. L. Lewis
REVISIONS INIT. DATE



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