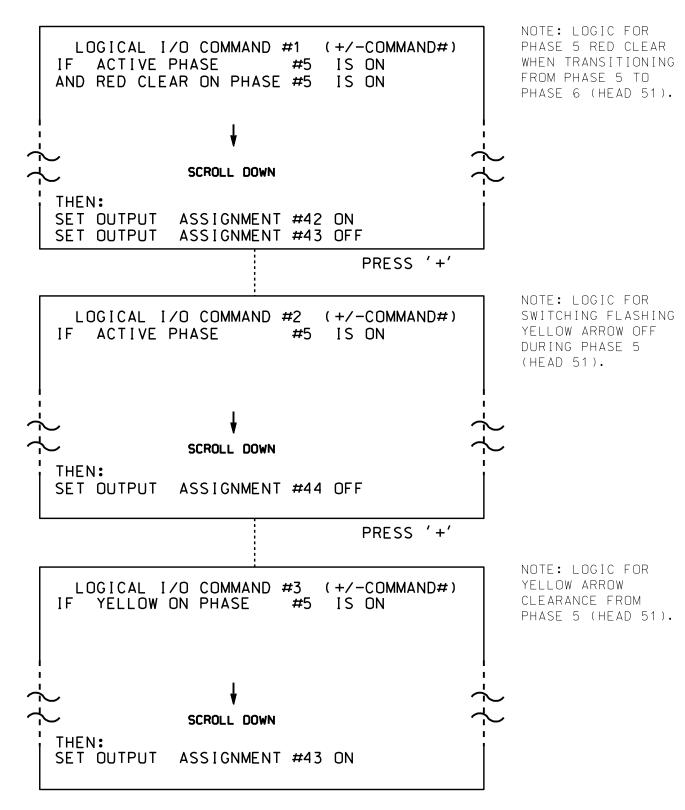
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)O YELLOW CLEAR (0=PARENT.3-25.5 SEC)O OUTPUT AS PHASE # (0=NONE. 1-16)O	■ NOTICE GREEN FLASH
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OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1095T DESIGNED: February 2024 SEALED: 03/14/2024 REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 56 SR 1215 (W. Lyon Station Road)

Granville County Division 5 PLAN DATE: March 2024 REVIEWED BY: PREPARED BY: S. Kirkpatrick REVIEWED BY: REVISIONS INIT. DATE

Ryan W. Hough 03/15/2024 SIG. INVENTORY NO. 05-1095T

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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