

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1 ★	1	15.0		X		X	
2A	TB2-5,6	I2U	39	1	29 ★	6	3.0		X		X	
3A	TB4-5,6	I5U	58	20	7 ★	3	15.0		X		X	
4A	TB4-9,10	I6U	41	3	30 ★	8	3.0		X		X	
4B	TB4-11,12	I6L	45	7	9	4	5.0	2.0	X		X	X
5A	TB3-1,2	J1U	55	17	15 ★	5	15.0		X		X	
6A	TB3-5,6	J2U	40	2	31 ★	2	3.0		X		X	
7A	TB5-5,6	J5U	57	19	21 ★	7	15.0		X		X	
8A	TB5-9,10	J6U	42	4	32 ★	4	3.0		X		X	
8B	TB5-11,12	J6L	46	8	22	8	5.0	2.0	X		X	X
PED PUSH BUTTONS												
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P41,P42	TB8-5,6	I12L	69	35	4	PED 4						
P61,P62	TB8-7,9	I13U	68	34	6	PED 6						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

★ For the detectors to work as shown on the signal design plan, see the Vehicle Detector Setup Programming Detail for Alternate Phasing on sheet 2.

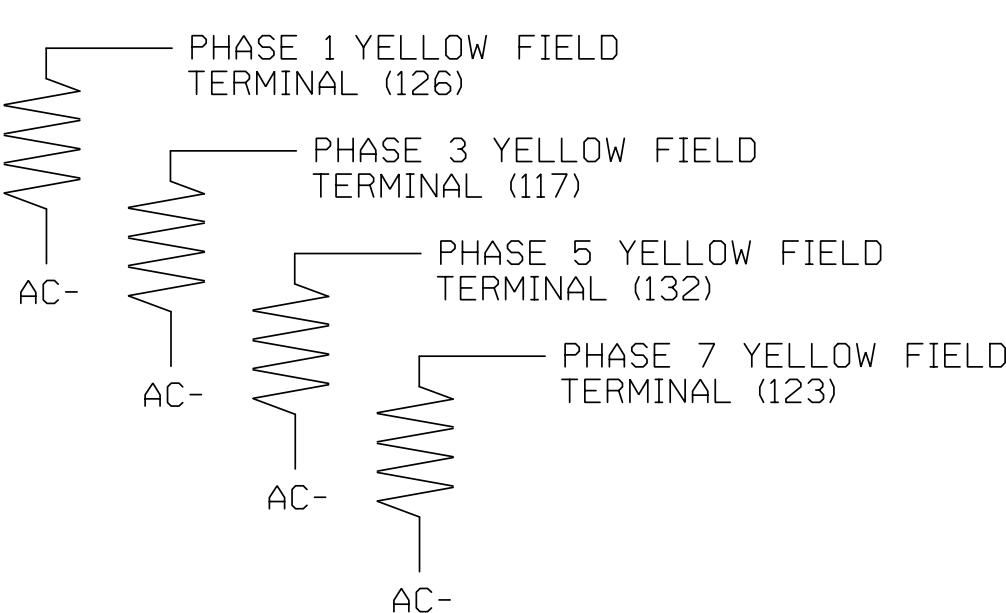
INPUT FILE POSITION LEGEND: J2L  
FILE J SLOT 2 LOWER

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.

2. Return controller to Factory Defaults before programming per this electrical detail.

3. Program phases 4 and 8 for Dual Entry.

4. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.

5. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

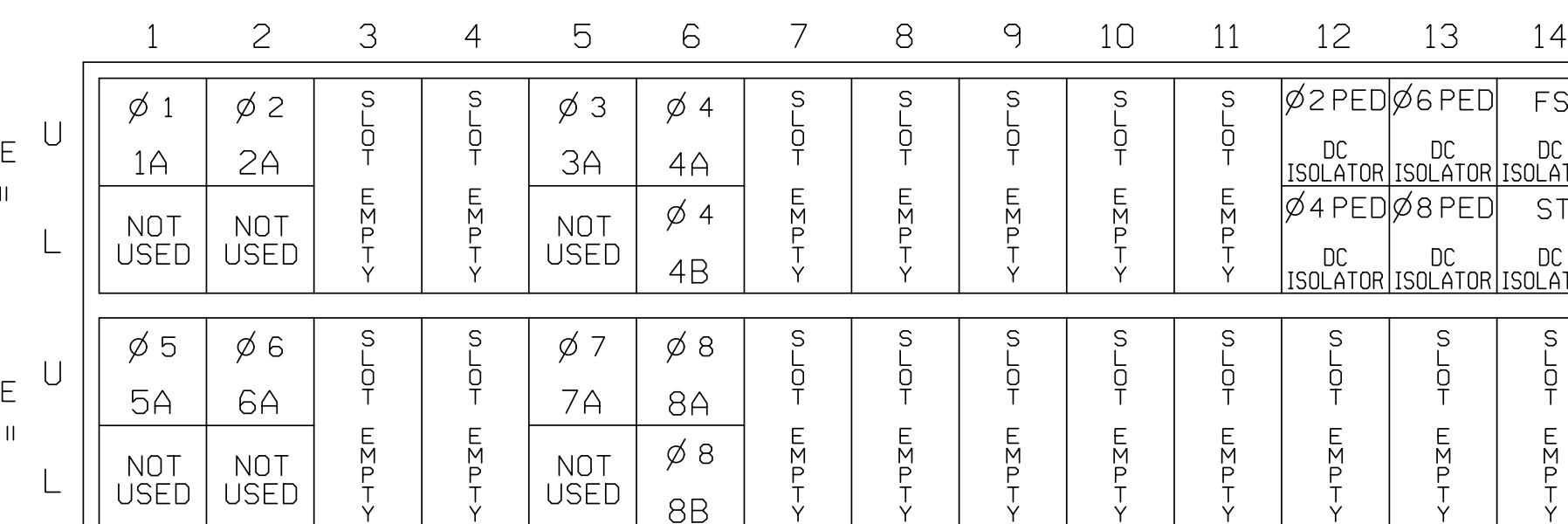
### EQUIPMENT INFORMATION

Controller.....2070LX  
Cabinet.....332 w/ Aux  
Software.....Q-Free MAXTIME  
Cabinet Mount.....Base  
Output File Positions.....18 With Aux. Output File  
Load Switches Used.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,S11,S12, AUX S1, AUX S2,AUX S4, AUX 5  
Phases Used.....1,2,2PED,3,4,4PED,5,6,6PED,8,8PED  
Overlap "1".....\*  
Overlap "2".....\*  
Overlap "3".....\*  
Overlap "4".....\*

\*See overlap programming detail on sheet 2

### INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

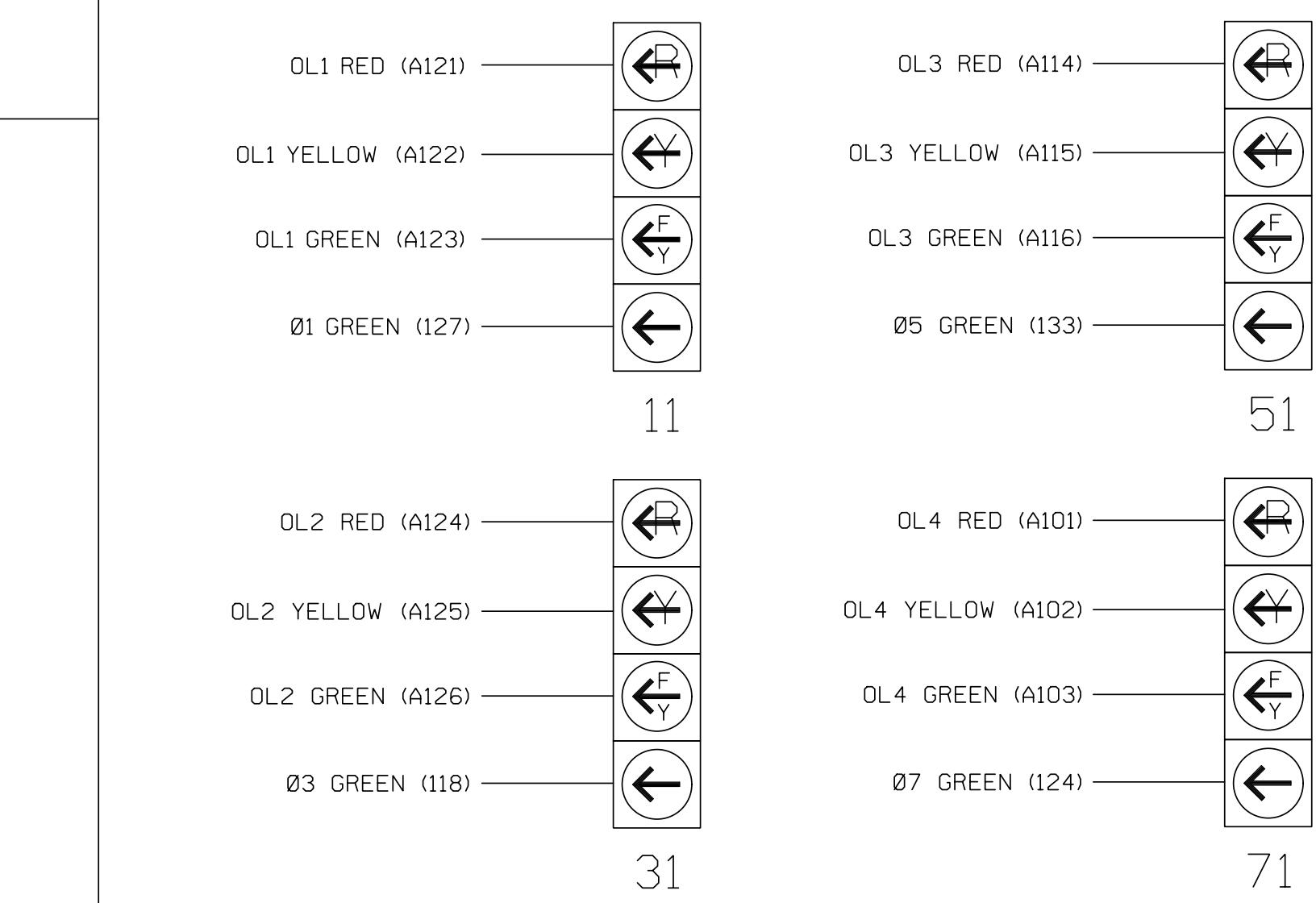
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	PED	3	4	PED	5	6	PED	7	8	PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11 ★ 21,22,23 P21, P22			31 ★ 41,42 P41, P42		51 ★ 61,62,63 P61, P62		71 ★ 81,82 P81, P82		11 ★ 31 ★ NU			51 ★ 71 ★ NU					
RED	128				101					134			107					
YELLOW	*	129		*	102		*	135		*	108							
GREEN	130				103					136			109					
RED ARROW													A121	A124	A114	A101		
YELLOW ARROW													A122	A125	A115	A102		
FLASHING YELLOW ARROW													A123	A126	A116	A103		
GREEN ARROW	127				118			133			124							
HAND		113				104			119			110						
PEDESTRIAN		115				106			121			112						

NU = Not Used  
\* Denotes install load resistor. See load resistor installation detail this sheet.  
★ See pictorial of head wiring in detail this sheet.

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 10-1325  
DESIGNED: AUGUST 2023  
SEALED: 04/29/2024  
REVISED: N/A

ELECTRICAL AND PROGRAMMING DETAILS FOR:		SR 3448 (Pleasant Plains Road) at SR 3440 (McKee Road)	
Prepared for: North Carolina Department of Transportation, Division of Transportation Management		Division 10 Mecklenburg County Matthews	
PLAN DATE: August 2023		REVIEWED BY: SL Phillips	
PREPARED BY: SP Pennington		REVIEWED BY:	
REVISIONS		INIT.	DATE
4/29/2024			
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED			
SEAL		CAROLINA PROFESSIONAL ENGINEER STACE L. PHILLIPS 032607 4/29/2024 SPL	
DocuSigned by: SPL 4/29/2024 DATE			
SIG. INVENTORY NO. 10-1325			