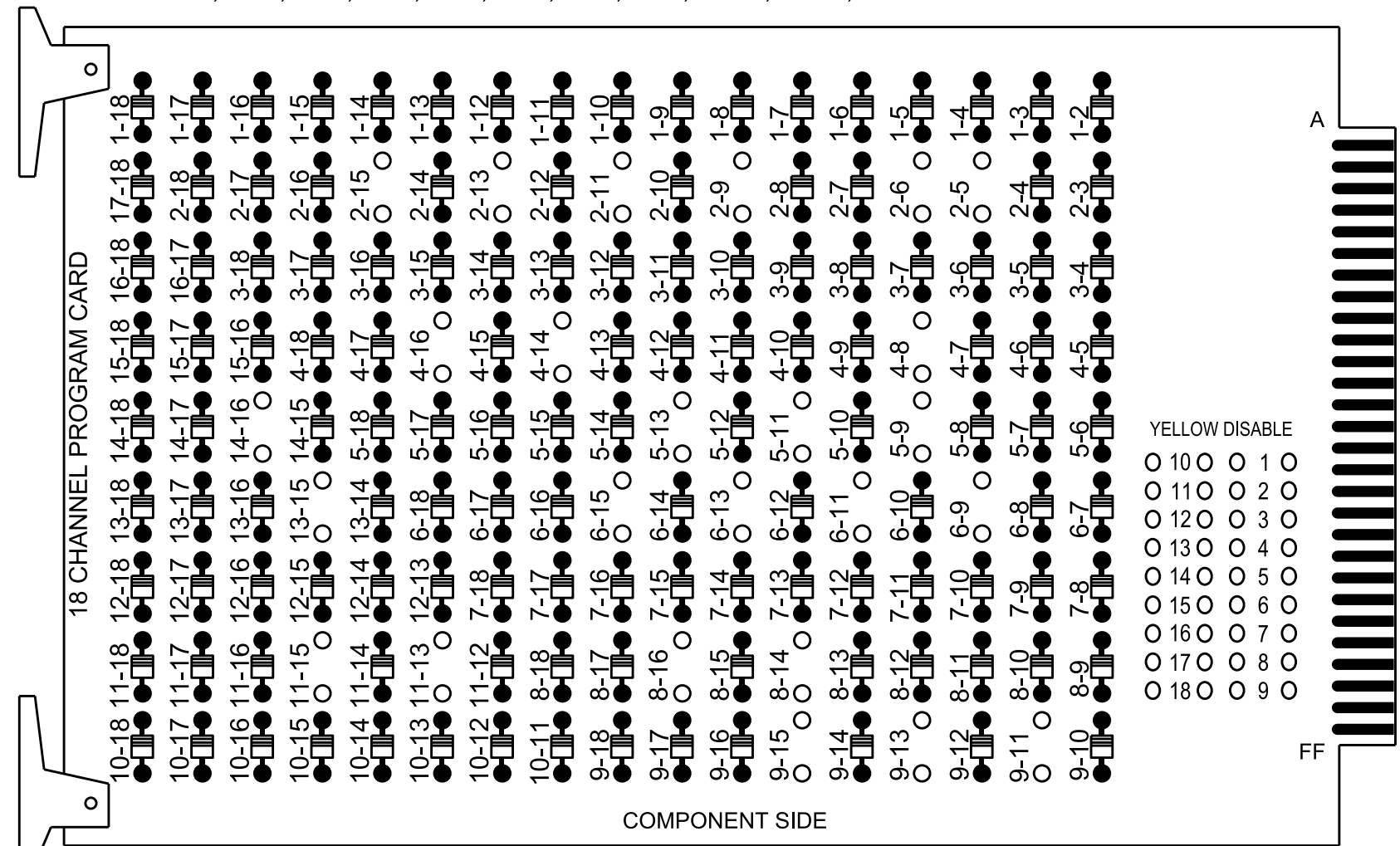


18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program phases 4 and 8 for Simultaneous Start.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the Lexington NC 8 CLS. Signal System #: D09-19_Lexington

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S2, S3, S5, S6, S7, S8, S9, S11, S12, AUX S1, AUX S4
 Phases Used.....2, 2PED, 4, 4PED, 5, 6, 6PED, 8, 8PED

Overlap "1".....*
 Overlap "2".....Not Used
 Overlap "3".....*
 Overlap "4".....Not Used

*See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

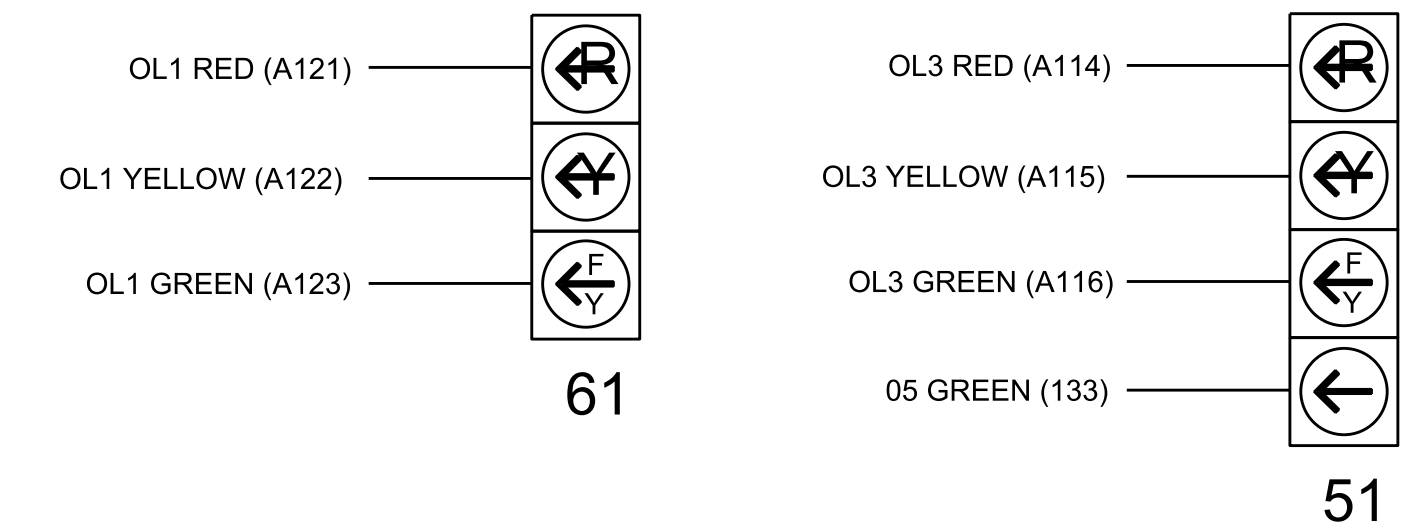
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6		
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18		
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE		
SIGNAL HEAD NO.	NU	21,22, 23	P21, P22	NU	41,42	P41, P42	51*	42	62,63	P61, P62	NU	81,82	P81, P82	61*	NU	NU	51*	NU		
RED		128			101		*		134			107								
YELLOW		129			102				135			108								
GREEN		130			103				136			109								
RED ARROW																		A121	A114	
YELLOW ARROW									132										A122	A115
FLASHING YELLOW ARROW																			A123	A116
GREEN ARROW								133	133											
Hand																				
Foot																				

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

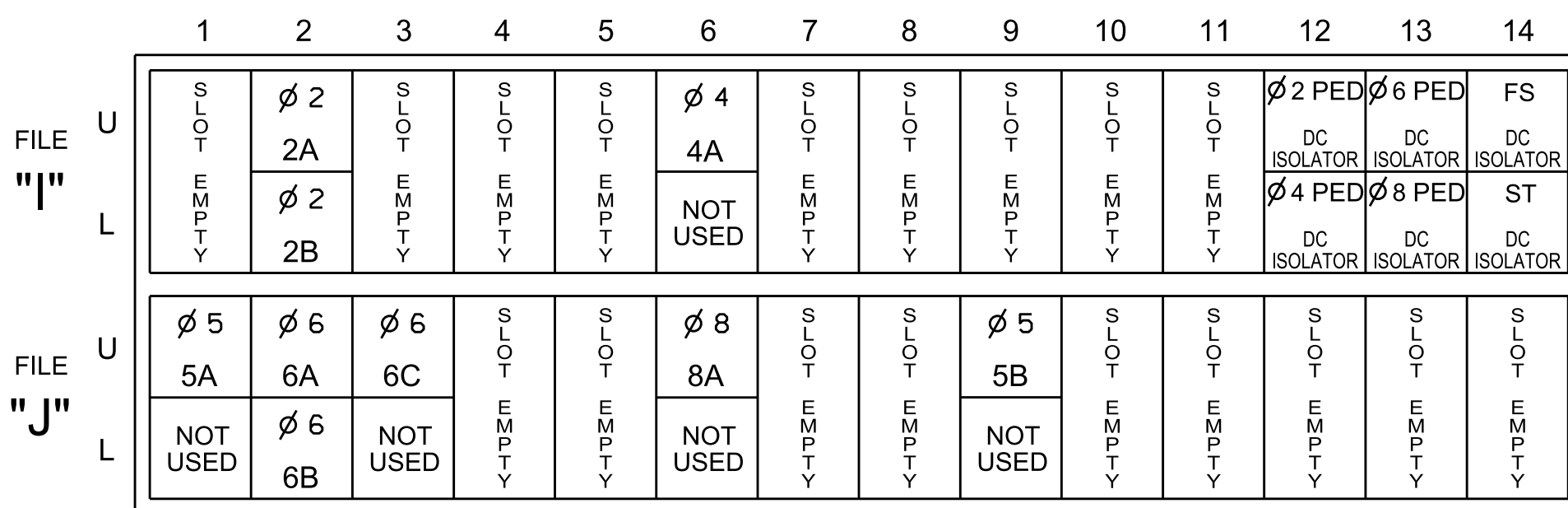
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

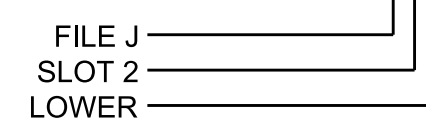
! If present, remove jumper from J1W to I4-W on rear of input file.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2					X	
2B	TB2-7,8	I2L	43	5	3	2					X	
4A	TB4-9,10	I6U	41	3	8	4					X	
5A	TB3-1,2	J1U	55	17	15	5	15.0				X	
6A	TB3-5,6	J2U	40	2	16	6					X	
6B	TB3-7,8	J2L	44	6	17	6					X	
6C	TB3-9,10	J3U	64	30	18	6					X	
8A	TB5-9,10	J6U	42	4	22	8	5.0				X	
5B	TB7-9,10	J9U	59	21	27	5	15.0				X	
PED PUSH BUTTONS												
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P41,P42	TB8-5,6	I12L	69	35	4	PED 4						
P61,P62	TB8-7,9	I13U	68	34	6	PED 6						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

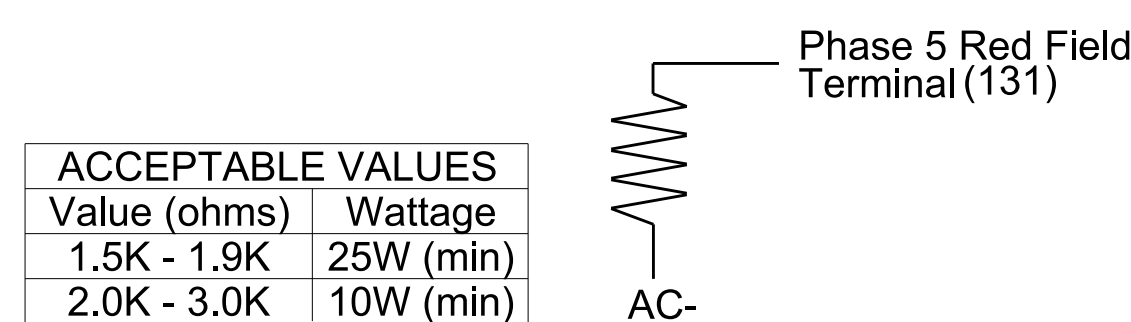
NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0734
 DESIGNED: January 2024
 SEALED: 02/29/2024
 REVISED: N/A

Electrical Detail

Electrical and Programming Details For:

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

NC 8 (Winston Rd./MLK Blvd.)
 at
 US 29 NB/US 64-70 EB Ramp

Division 9	Davidson County	Lexington
PLAN DATE: February 2024	REVIEWED BY:	
PREPARED BY: Sarah Kirkpatrick	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

DocuSigned by:
 Ryan W. Hough
 03/01/2024

SIG. INVENTORY NO. 09-0734