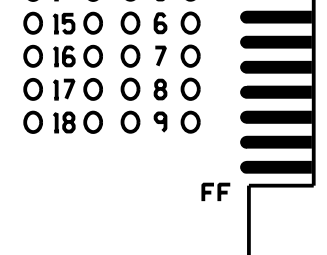
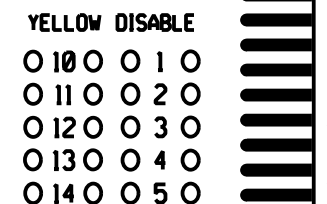
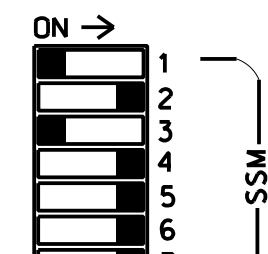
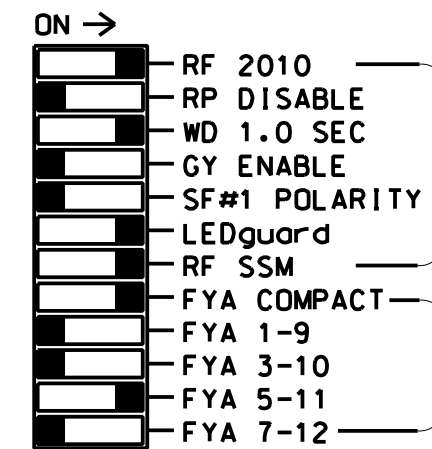
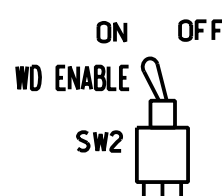
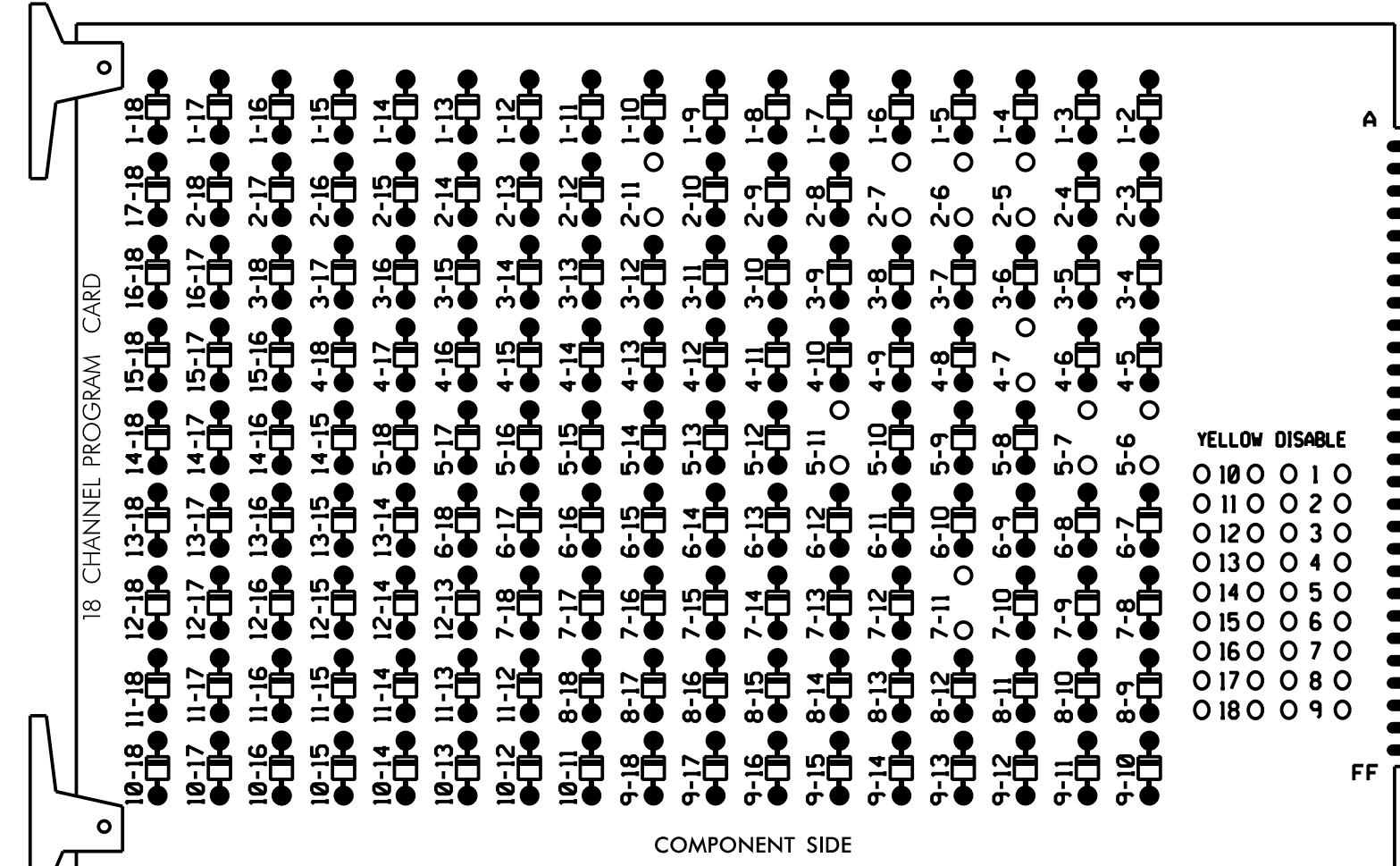


16 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-7, 2-11, 4-7, 5-6, 5-7, 5-11, and 7-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- 2. Make sure jumpers SEL2-SEL5 are present on the monitor board.
- 3. Special cabinet wiring is required to utilize FYA COMPACT mode. See Ped Yellow Conflict Monitor Wiring Detail on sheet 2.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up in Green.
- Program phases 2 and 6 for Yellow Flash.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE MOUNTED
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S2,S5,S7,S8,S9,S10
 PHASES USED.....2,4,5,6
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....4+5

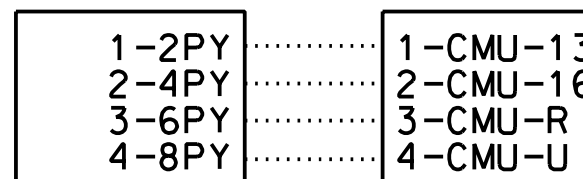
PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode on the 2010ECL-NC Monitor, the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to Channel 9 Green (monitor pin 13), from 4 PY (field term. 105) to Channel 9 Yellow (monitor pin 16), from 6 PY (field term. 120) to Channel 10 Green (monitor pin R), and from 8 PY (field term. 111) to Channel 10 Yellow (monitor pin U).

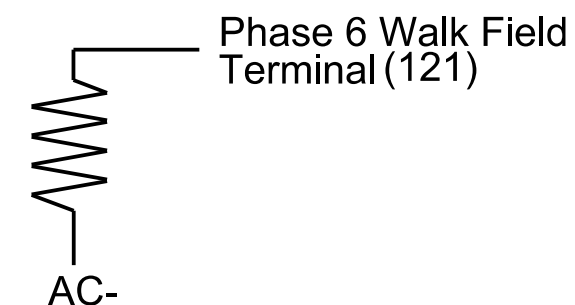
- Follow the instructions below to make the appropriate connections:
 STEP 1: Fold down rear panel of output file.
 STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).
 STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:
- CMU-R _____ 6PY (term. 120)

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)



ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	11	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	OLC	6	5 GRN	6 PED	OLD	8	8 PED
SIGNAL HEAD NO.	NU	21	NU	NU	41	NU	51	61	62	51	NU	42,43	NU
RED		128						134	134			122	
YELLOW		129						135	135				
GREEN									136				
RED ARROW						101			131				
YELLOW ARROW						102			132			123	
FLASHING YELLOW ARROW									133				
PED YELLOW													
GREEN ARROW		130				103		136	120		124		
PED GREEN											*		

NU = Not Used

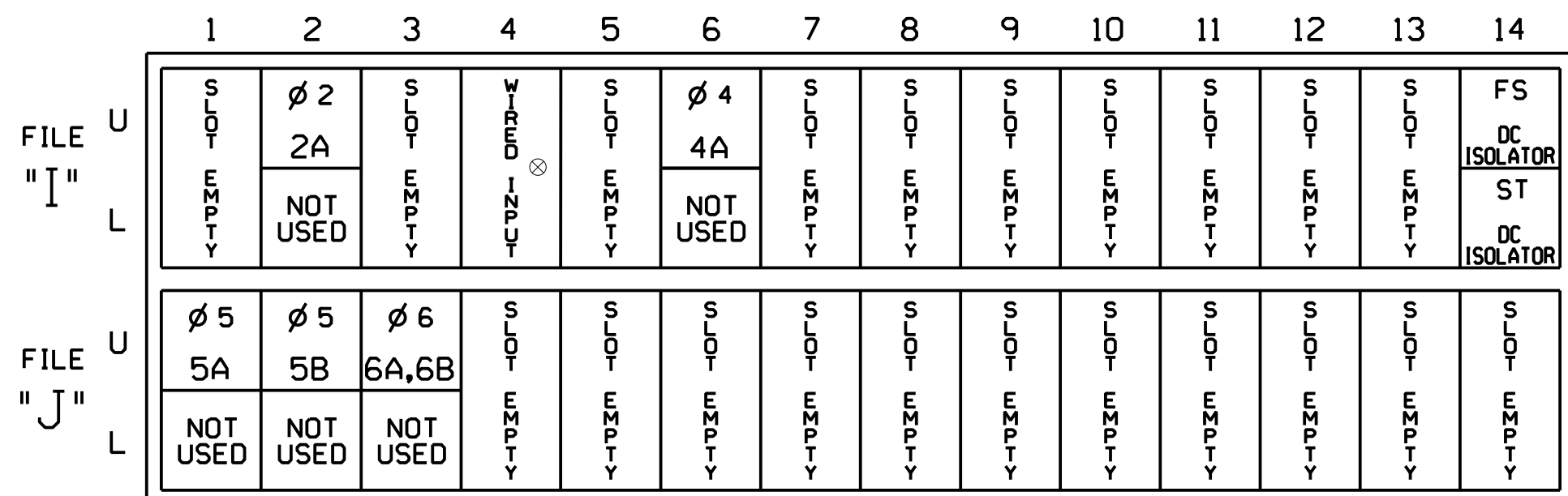
* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

Note: Load switches S7, S9, and S10 require output remapping. See sheets 2 and 3 for details.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

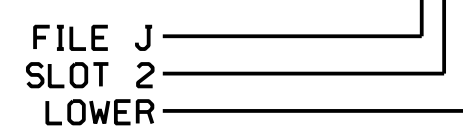
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A ¹	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9 ★	22	2	Y	Y	Y		
5B	TB3-5,6	J2U	40	2	5	5	Y	Y			10
6A,6B	TB3-9,10	J3U	64	26	36	6	Y	Y			

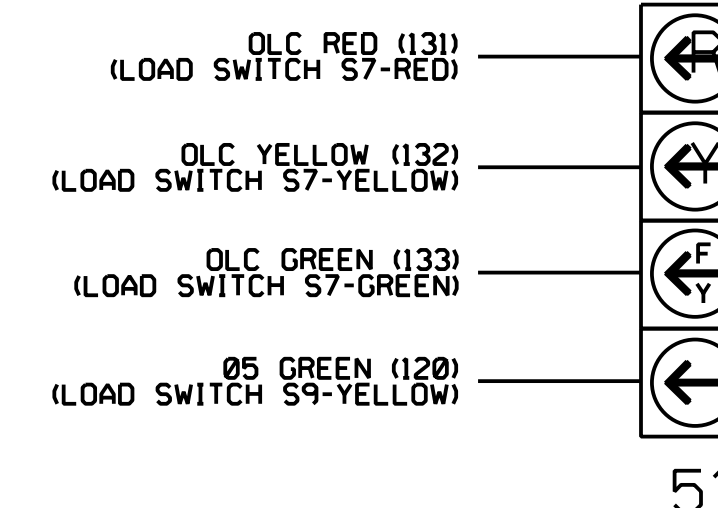
¹Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0245T
 DESIGNED: AUGUST 2021
 SEALED: 05/21/2024
 REVISED:

Signal Upgrade
 Temporary Design
 Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: 	SR 1713 (Albemarle Rd)	SEAL
	at SR 1446 (Lewallen Rd)	
Division 8 PLAN DATE: August 2021 PREPARED BY: N.K. Vianich	Randolph County ASHEBORO REVIEWED BY: A.D. Klinskiesk REVIEWED BY: N.R. Simmons	Division 8 ASHEBORO REVIEWED BY: A.D. Klinskiesk REVIEWED BY: N.R. Simmons
750 N. Greenfield Pkwy, Garner, NC 27529		HNTB NORTH CAROLINA, P.C. 343 E. Six Forks Road, Suite 200 Raleigh, North Carolina 27609 NC License No: C-1554 (919) 546-8997