

— SF#1 POLARITY 📮 LEDguard FYA COMPACT— — FYA 3-10 ── FYA 5-11 FYA 7-12 ----COMPONENT SIDE REMOVE JUMPERS AS SHOWN NOTES:

ON OFF

= DENOTES POSITION OF SWITCH

WD ENABLE \(\)

SW2

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|----------------------------------|
| Cabinet | 332 w/ Aux |
| Software | Q-Free MAXTIME |
| Cabinet Mount | Base |
| Output File Positions | 18 With Aux. Output File |
| Load Switches Used | S1, S2, S5, S7, S8, S11, AUX S1, |
| | AUX S2, AUX S4, AUX S5 |
| Phases Used | 1, 2, 4, 5, 6, 8 |
| Overlap "1" | * |
| Overlap "2" | |
| Overlap "3" | |
| Overlap "4" | |
| • | |

*See overlap programming detail on sheet 2

| Controller | 2070LX |
|-----------------------|--|
| Cabinet | 332 w/ Aux |
| Software | Q-Free MAXTIME |
| Cabinet Mount | Base |
| Output File Positions | 18 With Aux. Output File |
| Load Switches Used | S1, S2, S5, S7, S8, S11, AUX S1, AUX S2, AUX S4, AUX S5 |
| Phases Used | 1, 2, 4, 5, 6, 8 |
| Overlap "1" | * |
| Overlap "2" | * |
| Overlan "3" | * |

INPUT FILE POSITION LAYOUT

(front view)

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

| | _ | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 |
|------|---|------|------|-------------|-------------|-------------|-----|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-----------------|
| | | Ø 1 | ø 2 | S L | S L | S L | ø 4 | S L | FS |
| FILE | U | 1A | 2A | O T | O T | O T | 4A | O T | O T | O T | O T | O T | O T | P | DC ISOLATOR |
| " " | | NOT | NOT | E M P | E M P | E M P | Ø 4 | E M P | ST |
| | | USED | USED | T Y | T Y | T Y | 4B | T Y | T Y | T Y | T Y | T Y | Y | Y | DC ISOLATOR |
| | | Ø 5 | ø6 | S L | S L | S L | ø 8 | S L | S |
| FILE | U | 5A | 6A | O T | O T | O T | 8A | O T | 우 |
| "J" | | NOT | NOT | E M P | E M P | E M P | Ø 8 | E M P | E M P |
| | | USED | USED | T Y | T Y | T Y | 8B | T Y |

EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT POINT | DETECTOR NO. | CALL PHASE | DELAY TIME | EXTEND TIME | EXTEND | ADDED INITIAL | CALL | DELAY DURING GREEN | | | | | | | | | | | |
|----------|------------------|--------------------|------------|----------------|-----------------|---------------|---------------|----------------|--------|------------------|------|--------------------------|-----|----|----|------|---|----|--|---|--|---|--|
| 1A | TB2-1,2 | I1U | 56 | 18 | 1 ★ | 1 | 15 | | Х | | Χ | | | | | | | | | | | | |
| IA | 102-1,2 | 110 | 36 | - | 29 ★ | 6 | | | Х | | Χ | | | | | | | | | | | | |
| 2A | TB2-5,6 | 1 2U | 39 | 1 | 2 | 2 | | | Х | | Χ | | | | | | | | | | | | |
| 4A | TB4-9,10 | 1 6U | 41 | 3 | 8 | 4 | 3 | | Х | | Χ | | | | | | | | | | | | |
| 4B | TB4-11,12 | I6L | 45 | 7 | 9 | 4 | 10 | | Х | | Χ | | | | | | | | | | | | |
| 5A | TB3-1,2 | 1411 | 1411 | 1411 | 1411 | 1411 | 1411 | 1111 | 1411 | 1411 | 1411 | 1111 | J1U | 55 | 17 | 15 ★ | 5 | 15 | | Х | | Χ | |
| JA | 100-1,2 | 310 | 33 | - | 31 ★ | 2 | | | Х | | Χ | | | | | | | | | | | | |
| 6A | TB3-5,6 | J2U | 40 | 2 | 16 | 6 | | | Х | | Χ | | | | | | | | | | | | |
| 8A | TB5-9,10 | J6U | 42 | 4 | 22 | 8 | 3 | | Х | | Χ | | | | | | | | | | | | |
| 8B | TB5-11,12 | J6L | 46 | 8 | 23 | 8 | 10 | | Х | | Х | | | | | | | | | | | | |

★ For the detectors to work as shown on the signal design plan. see the Vehicle Detector Setup Programming Detail for Alternate Phasing on sheet 2.

> INPUT FILE POSITION LEGEND: J2L SLOT 2-LOWER -

> > This Plan Supersedes Electrical Detail Sealed on 10/21/2021

ROJECT REFERENCE NO. Sig. 2.1

| | | | | SI | GNA | \L H | IEA | DΗ | 00 | K-U | IP C | HA | RT | | | | | |
|-----------------------------|----------------|-------|----------|----|-------|----------|----------------|-------|----------|-----|-------|----------|-----------|----------------|-----------|-------------|-------------|-----------|
| LOAD SWITCH NO. | S1 | S2 | S3 | S4 | S5 | S6 | S7 | S8 | S9 | S10 | S11 | S12 | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 |
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 4 | 14 | 5 | 6 | 15 | 7 | 8 | 16 | 9 | 10 | 17 | 11 | 12 | 18 |
| PHASE | 1 | 2 | 2 PED | 3 | 4 | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED | OL1 | | SPARE | OL3 | | SPARE |
| SIGNAL HEAD NO. | ★ 11 | 21.22 | NU | NU | 42,43 | NU | ★ 51 | 61,62 | NU | NU | 82,83 | NU | ★ | ★ 81 | NU | 51 ★ | ★ 41 | NU |
| RED | | 128 | | | 101 | | | 134 | | | 107 | | | | | | | |
| YELLOW | * | 129 | | | 102 | | * | 135 | | | 108 | | | | | | | |
| GREEN | | 130 | | | 103 | | | 136 | | | 109 | | | | | | | |
| RED ARROW | | | | | | | | | | | | | A121 | A124 | | A114 | A101 | |
| YELLOW ARROW | | | | | | | | | | | | | A122 | A125 | | A115 | A102 | |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | A123 | A126 | | A116 | A103 | |
| GREEN ARROW | 127 | | | | | | 133 | | | | | | | | | | | |

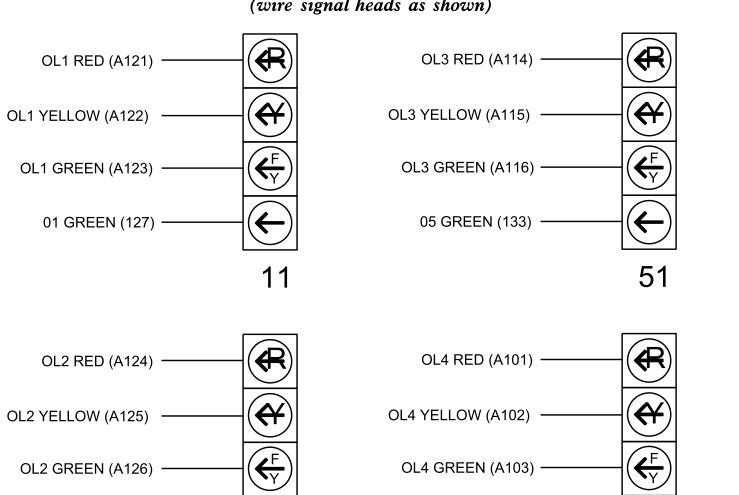
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0089 DESIGNED: August 2023 SEALED: 11/14/2023 REVISED: N/A

Electrical Detail - Sheet 1 of 2

Prepared in the Offices of:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED US 601 (S. State Street) SR 1742 (Sharon Drive)/

Shopping Center Driveway PLAN DATE: November 2023 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

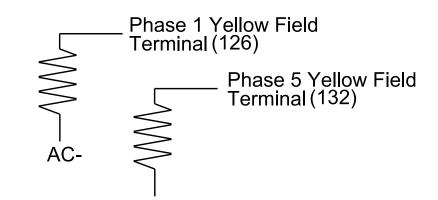
031001

D. Told Joya 11/16/2023 SIG. INVENTORY NO. 11-0089

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)



Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| Overlap | 1 | 2 | 3 | 4 |
|-------------------|-----------------|-----------------|-----------------|-----------------|
| Туре | FYA 4 - Section |
| Included Phases | 2 | 4 | 6 | 8 |
| Modifier Phases | 1 | - | 5 | - |
| Modifier Overlaps | | | ı | - |
| Trail Green | 0 | 0 | 0 | 0 |
| Trail Yellow | 0.0 | 0.0 | 0.0 | 0.0 |
| Trail Red | 0.0 | 0.0 | 0.0 | 0.0 |

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

| • | | | | | |
|-------------------|-----------------|-----------------|-----------------|-----------------|-----------------------|
| Overlap | 1 | 2 | 3 | 4 | |
| Туре | FYA 4 - Section | |
| Included Phases | - | 4 | - | 8 | NOTICE INCLUDED PHASE |
| Modifier Phases | 1 | - | 5 | 1 | |
| Modifier Overlaps | ı | | ı | ı | |
| Trail Green | 0 | 0 | 0 | 0 | |
| Trail Yellow | 0.0 | 0.0 | 0.0 | 0.0 | |
| Trail Red | 0.0 | 0.0 | 0.0 | 0.0 | |

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

| Detector | Call Phase | Delay |
|----------|------------|-------|
| 1 | 1 | 3 |
| 29 | 0 | • |

| Detector | Call Phase | Delay |
|----------|------------|-------|
| 15 | 5 | 3 |
| 31 | 0 | - |

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

| PHASING | OVERLAP PLAN | VEH DET PLAN |
|---|--------------|--------------|
| ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING | 1 | 1 |
| ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING | 2 | 2 |

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

| <u>Pattern Para</u> | meters | |
|---------------------|--------------|--------------|
| Pattern | Veh Det Plan | Overlap Plan |
| * | 2 | 2 |

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0089 DESIGNED: August 2023 SEALED: 11/14/2023 REVISED: N/A

| Electrical Detail - Sheet 2 of 2

Prepared in the Offices of:

US 601 (S. State Street) SR 1742 (Sharon Drive)/ Shopping Center Driveway

PLAN DATE: November 2023 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

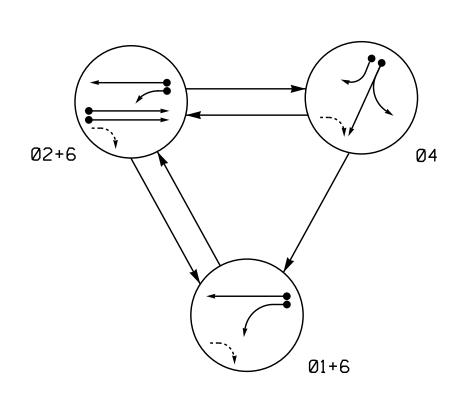
031001

D. told Joya 11/16/2023

SIG. INVENTORY NO. 11-0089

This Plan Supersedes Electrical Detail Sealed on 10/21/2021

PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

| ← | DETECTED MOVEMENT |
|----------|-------------------------------|
| ← | UNDETECTED MOVEMENT (OVERLAP) |
| - | UNSIGNALIZED MOVEMENT |
| <> | PEDESTRIAN MOVEMENT |

| TABLE OF | TABLE OF OPERATION | | | | | | |
|----------------|--------------------|----------------|-------------|----------------|--|--|--|
| | | PHA | SE | | | | |
| SIGNAL FACE | 0 1 + 6 | Ø2+6 | Ø 4 | LUGOI | | | |
| 1 1 | - | - F | | - Y | | | |
| 2 1 | R | 1 | R | Υ | | | |
| 2 2 | R | G | R | Υ | | | |
| 41,42 | R | R | G | R | | | |
| 4 3 | R | R | F | R | | | |
| 6 1 | G | G | R | Υ | | | |
| 6 2 | <u></u> | <u> </u> | R | Υ | | | |

| | All Head | ds L.E.D. | |
|----|---------------------------|------------------------------------|----------|
| 11 | P Y 12" 21 62 | R Y 12" 22 41,42 61 | 12 43 |

SIGNAL FACE I.D.

| MAXTIME DETECTOR INSTALLATION CHART | | | | | | | | | | | | |
|-------------------------------------|--------------|-------------------------------------|-------|----------|---------------|---------------|----------------|--------|---------------|------|--------------------|----------|
| | DETI | ECTOR | | | | PRO | GRAMM | IN | G | | | |
| L00P | SIZE (FT) | DISTANCE FROM STOPBAR (FT) | TURNS | NEW LOOP | CALL PHASE | DELAY TIME | EXTEND TIME | EXTEND | ADDED INITIAL | CALL | DELAY DURING GREEN | NEW CARD |
| 1A * | 6X40 | +5 | * | * | 1 | 15.0 | - | Χ | - | Χ | - | Χ |
| IA | 0.40 | 13 | | | 6 | - | - | Χ | - | Х | - | Х |
| 2A * | 6X6 | 70 | * | * | 2 | - | - | Χ | - | Χ | - | Χ |
| 2B * | 6X6 | 70 | * | * | 2 | - | - | Χ | - | Χ | - | Χ |
| 4A * | 6X40 | 0 | * | * | 4 | - | - | Χ | - | Χ | - | Χ |
| 4B * | 6X40 | 0 | * | * | 4 | 15.0 | - | Χ | - | Χ | - | Χ |
| 4C * | 6X15 | 0 | * | * | 4 | 15.0 | - | Χ | - | Χ | - | Χ |
| 6A * | 6X6 | 70 | * | * | 6 | - | - | Χ | - | Χ | - | Χ |

^{*} Video Detection Zone

New Signal Pole -L- 15+96, 71' LT 35 MPH 0% Grade **← (6A)** 35 MPH -1% Grade US 601 (S. State Street) Pole Mounted Cabinet New Signal Pole -L- 15+92, 65' RT

This plan supersedes the plan signed and sealed on 11/14/23.

3 Phase Fully Actuated US 601 (Yadkinville) System 1 (TBC)

PROJECT REFERENCE NO.

U-5809

Sig.3.0

NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 may be lagged.
- 4. Set all detector units to presence mode.
- 5. Program controller to operate using FYA compact mode.
- 6. Refer to Pavement Marking Plan for stop line locations.
- 7. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 8. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
- 9. Time Based system data: Controller Asset #: 1208.

LEGEND EXISTING <u>PROPOSED</u> Traffic Signal Head Modified Signal Head Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector Controller & Cabinet Junction Box ----- 2-in Underground Conduit Right of Way \longrightarrow Directional Arrow Video Detection Zone Construction Zone N/A "YIELD" Sign (R1-2) No Left Turn Sign (R3-2) No Right Turn Sign (R3-1) Guardrail

| MAX | TIME T | IMING | CHART | |
|-------------------------|--------|------------|-------|------------|
| FEATURE | | PH | ASE | |
| FEATURE | 1 | 2 | 4 | 6 |
| Walk * | _ | - | _ | - |
| Ped Clear | _ | - | _ | _ |
| Min Green * | 7 | 10 | 7 | 10 |
| Passage * | 2.0 | 3.0 | 2.0 | 3.0 |
| Max 1 * | 15 | 60 | 30 | 60 |
| Yellow Change | 3.0 | 3.9 | 4.1 | 3.9 |
| Red Clear | 2.8 | 2.8 | 1.1 | 2.8 |
| Added Initial * | _ | _ | _ | _ |
| Maximum Initial * | _ | _ | _ | _ |
| Time Before Reduction * | _ | _ | _ | _ |
| Time To Reduce * | _ | - | - | _ |
| Minimum Gap | _ | _ | _ | _ |
| Advance Walk | _ | _ | _ | _ |
| Non Lock Detector | Х | _ | Х | _ |
| Vehicle Recall | _ | MIN RECALL | _ | MIN RECALL |
| Dual Entry | _ | _ | _ | _ |

* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Signal Upgrade - Temporary Design 1 - Phase I

US 601 (S. State Street) US 421 Southbound Ramp

Division 11 Yadkin County Yadkinville PLAN DATE: February 2024 REVIEWED BY: R.N. Zinser 750 N.Greenfield Pkwy, Garner, NC 27529 PREPARED BY: T.A. Kenion REVIEWED BY: INIT. DATE

043914 SIG. INVENTORY NO. | |- |208T|

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

CARO

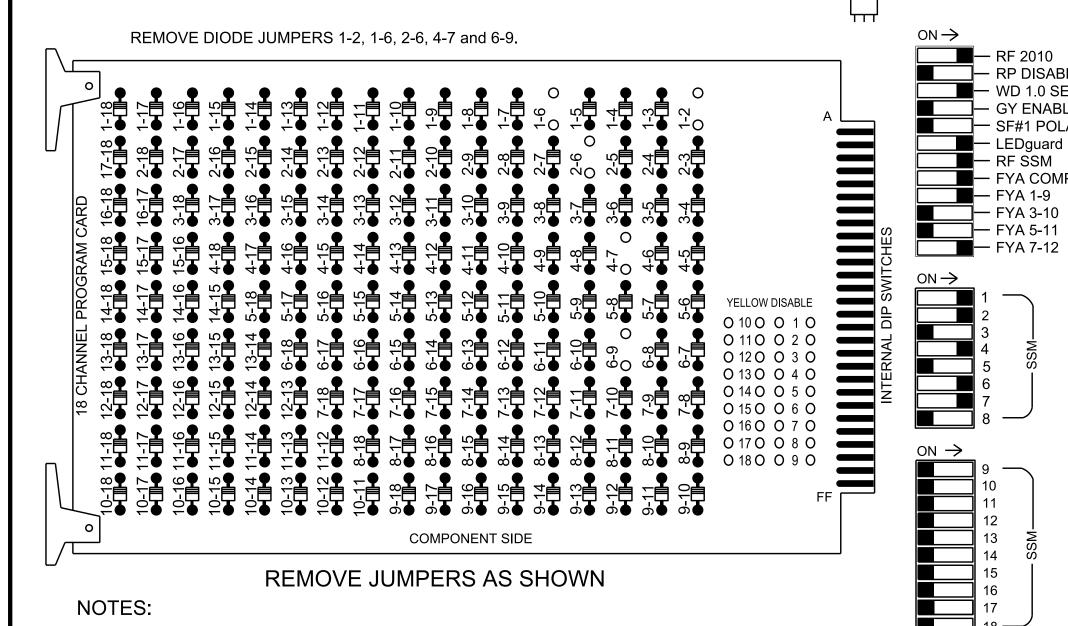
ON

─ SF#1 POLARITY

LEDguard

WD ENABLE 🕥

(remove jumpers and set switches as shown)



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 4. The cabinet and controller are part of the US 601 (Yadkinville) System 1 (TBC).

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|-------------------------|
| Cabinet | 336 |
| Software | .Q-Free MAXTIME |
| Cabinet Mount | Pole |
| Output File Positions | .12 |
| Load Switches Used | S1, S2, S3, S5, S8, S10 |
| Phases Used | 1, 2, 4, 6 |
| Overlap "1" | * |
| Overlap "2" | Not Used |
| Overlap "3" | Not Used |
| Overlap "4" | Not Used |
| Overlap "7" | * |
| | |

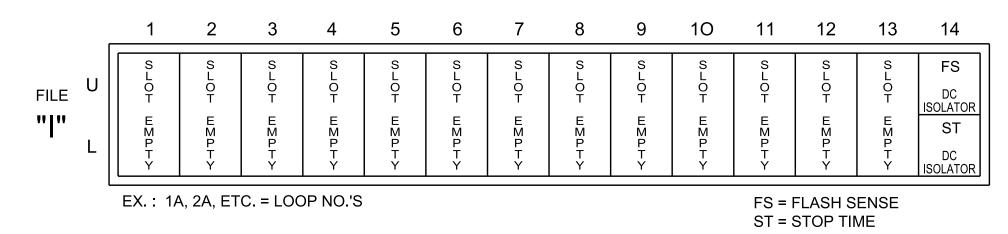
*See overlap programming detail on sheet 2

| | | S | IGN | IAL | ΗE | 4D | HO | OK- | UP | СН | 4R1 | - | | | |
|-----------------------------|----------------|-----|-----|-------|----------|----|-------|----------|----|-----|-----|----|-----|-----|----------|
| LOAD SWITCH NO. | S1 | S | 2 | S | 3 | S4 | S5 | S6 | S7 | S | 8 | S9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 2 | 9 | 13 | 3 | 4 | 14 | 5 | (| 6 | 15 | 7 | 8 | 16 |
| PHASE | OL1 | 2 | 2 | 1 GRN | 2 PED | 3 | 4 | 4 PED | 5 | (| 6 | | OL7 | 8 | 8 PED |
| SIGNAL HEAD NO. | ★ 11 | 21 | 22 | 11 | NU | NU | 41,42 | NU | NU | 61 | 62 | NU | 43 | NU | NU |
| RED | | 128 | 128 | | | | 101 | | | 134 | 134 | | 122 | | |
| YELLOW | | 129 | 129 | | | | 102 | | | 135 | 135 | | | | |
| GREEN | | | 130 | | | | 103 | | | 136 | | | | | |
| RED ARROW | 125 | | | | | | | | | | | | | | |
| YELLOW ARROW | 126 | | | | | | | | | | | | 123 | | |
| FLASHING YELLOW ARROW | 127 | | | | | | | | | | | | 124 | | |
| ₩ | | | | | | | | | | | | | | | |
| GREEN ARROW | | 130 | | 114 | | | | | | | 136 | | | | |
| × | | | | | * | | | | | | | | | | |
| NU = Not l | lsed | | | | | | | | | | | | | | |

★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

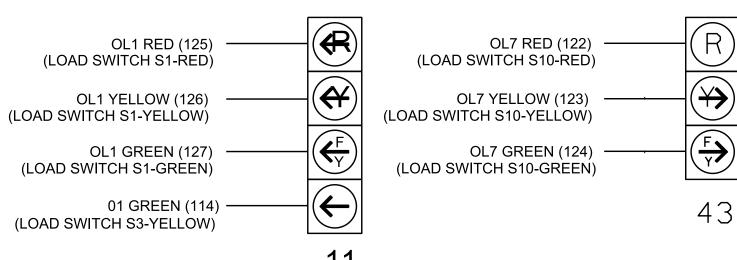


SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K 10W (min)

Phase 2 Walk Field Terminal (115)

This Plan Supersedes Electrical Detail Sealed on 11/16/2023

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1208T1 DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A

Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: Prepared in the Offices of:

US 601 (S. State Street) US 421 Southbound Ramp

Yadkin County Yadkinville PLAN DATE: November 2023 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY:

REVISIONS

D. Told Joyce 03/06/2024 SIG. INVENTORY NO. 11-1208T1

031001

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

^{*}Denotes install load resistor. See load resistor installation detail this sheet.

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

| | Channel | Control Type | Control Source | Flash Yellow | Flash Red | Flash Alt | MMU Channel |
|-----------------------|---------|---------------|----------------|--------------|-----------|-----------|-------------|
| NOTE: Phase Vehicle 1 | 1 | Overlap | 1 | Х | | Х | 1 |
| Changed to Overlap 1 | 2 | Phase Vehicle | 2 | Х | | | 2 |
| | 3 | Phase Vehicle | 3 | | Х | Х | 3 |
| | 4 | Phase Vehicle | 4 | | Х | | 4 |
| | 5 | Phase Vehicle | 5 | | Х | | 5 |
| | 6 | Phase Vehicle | 6 | Х | | Х | 6 |
| NOTE: Phase Vehicle 7 | 7 | Overlap | 7 | | Х | | 7 |
| Changed to Overlap 7 | 8 | Phase Vehicle | 8 | | Х | Х | 8 |
| | 9 | Overlap | 1 | Х | | Х | 9 |
| | 10 | Overlap | 2 | | Х | Х | 10 |
| | 11 | Overlap | 3 | Х | | | 11 |
| | 12 | Overlap | 4 | | Х | | 12 |
| | 13 | Phase Ped | 2 | | | | 13 |
| | 14 | Phase Ped | 4 | | | | 14 |
| | 15 | Phase Ped | 6 | | | | 15 |
| | 16 | Phase Ped | 8 | | | | 16 |
| | 17 | Overlap | 5 | | Х | Х | 17 |
| | 18 | Overlap | 6 | | Χ | | 18 |

FYA SIGNAL OUTPUT REMAPPING ASSIGNMENT PROGRAMMING DETAIL FOR SIGNAL HEAD 11

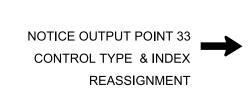
Front Panel

Main Menu >Controller >More >Advanced IO >Output Points

Web Interface

Home >Controller >Advanced IO >Cabinet Configuration >Output Points

IO Module 1



| Output Point | Descripton | Output Control Type | Index |
|--------------|------------|---------------------|-------|
| 33 | C1-35 | Phase Green | 1 |
| 34 | C1-36 | Not Active | 15 |
| 35 | C1-37 | Not Active | 14 |
| 36 | C1-38 | Not Active | 16 |

PROJECT REFERENCE NO.

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to Channel 9 Green (monitor pin 13).

Follow the instructions below to make the appropriate connections:

STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

| 1-2PY 2-4PY 3-6PY |] | 1-CMU-13 2-CMU-16 3-CMU-R 4-CMU-U |
|-------------------------|---|--|
|-------------------------|---|--|

OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| <u>'</u> | | |
|-------------------|-----------------|-----------------|
| Overlap | 1 | 7 |
| Туре | FYA 4 - Section | FYA 4 - Section |
| Included Phases | 2 | 4 |
| Modifier Phases | 1 | - |
| Modifier Overlaps | | - |
| Trail Green | 0 | 0 |
| Trail Yellow | 0.0 | 0.0 |
| Trail Red | 0.0 | 0.0 |

This Plan Supersedes Electrical Detail Sealed on 11/16/2023

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1208T1 DESIGNED: February 2024

REVISED: N/A

SEALED: 3/4/2024

Electrical Detail - Sheet 2 of 2 Prepared in the Offices of:

US 601 (S. State Street)

US 421 Southbound Ramp

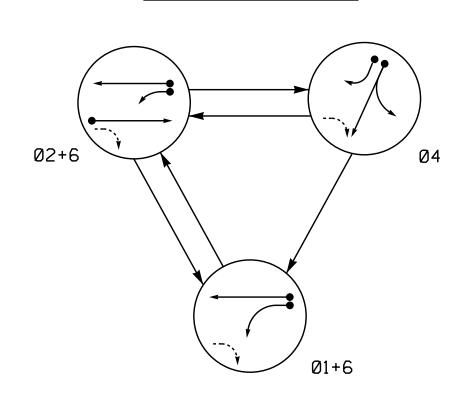
PLAN DATE: November 2023 REVIEWED BY: PREPARED BY: Zarrar Zafar

D. Told Joya 03/06/2024 SIG. INVENTORY NO. 11-1208T1

031001

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PHASING DIAGRAM



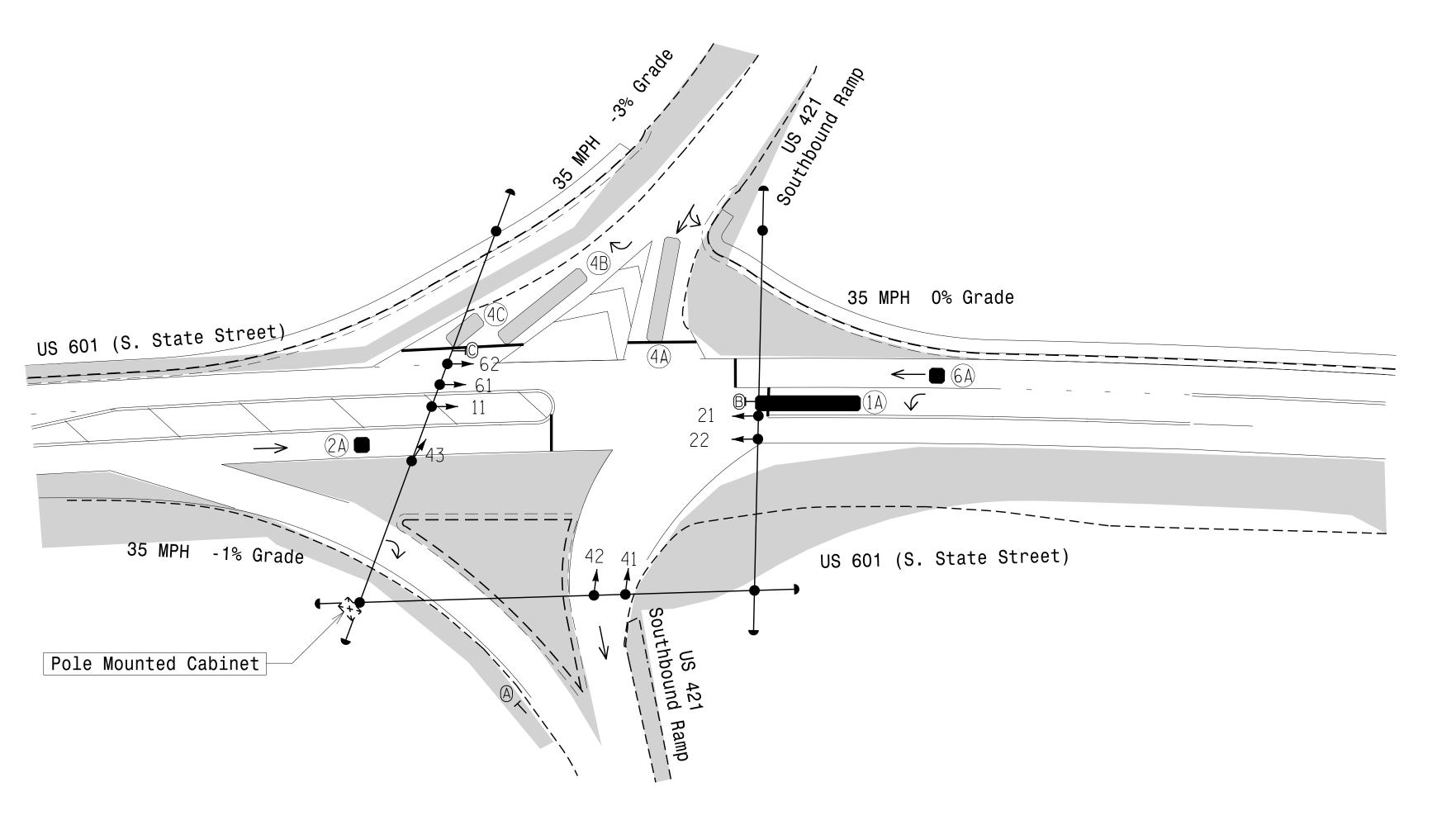
PHASING DIAGRAM DETECTION LEGEND

| ← | DETECTED MOVEMENT |
|----------|-------------------------------|
| ← | UNDETECTED MOVEMENT (OVERLAP) |
| - | UNSIGNALIZED MOVEMENT |
| <> | PEDESTRIAN MOVEMENT |

| TABLE OF OPERATION | | | | | | |
|--------------------|------------|--------------|--------|----------------|--|--|
| | | PHA | SE | | | |
| SIGNAL FACE | Ø 1 + 6 | ∞ N+6 | Ø 4 | FLGOI | | |
| 11 | ↓ | 누 | # | - Y | | |
| 2 1 | R | † | R | Υ | | |
| 2 2 | R | G | R | Υ | | |
| 41,42 | R | R | G | R | | |
| 4 3 | R | R | F | R | | |
| 6 1 | G | G | R | Υ | | |
| 6 2 | † | † | R | Y | | |

| N | SIGNAL FACE I.D. | |
|-----------|-----------------------|------|
| | All Heads L.E.D. | |
| FLAOI + > | 12" R R R 12" Y 12" G | R 12 |
| R | 11 21 22 | 4 3 |
| R | 62 41,42 61 | |
| Υ | | |

| | DETI | ECTOR | | | PRO | GRAMM | IN | G | | | | |
|------|--------------|-------------------------------------|-------|----------|---------------|---------------|----------------|--------|---------------|------|--------------------|----------|
| LOOP | SIZE (FT) | DISTANCE FROM STOPBAR (FT) | TURNS | NEW LOOP | CALL PHASE | DELAY TIME | EXTEND TIME | EXTEND | ADDED INITIAL | CALL | DELAY DURING GREEN | NEW CARD |
| 1A * | 6X40 | +5 | * | * | 1 | 15.0 | - | Χ | - | Χ | 1 | 1 |
| IA. | 0.40 | +5 | | | 6 | - | - | Χ | • | Χ | ı | ı |
| 2A * | 6X6 | 70 | * | * | 2 | - | - | Χ | - | Χ | • | ı |
| 4A * | 6X40 | 0 | * | - | 4 | - | - | Χ | - | Χ | • | ı |
| 4B * | 6X40 | 0 | * | - | 4 | 15.0 | - | Χ | - | Χ | - | - |
| 4C * | 6X15 | 0 | * | - | 4 | 15.0 | | Χ | _ | Χ | - | ı |
| 6A * | 6X6 | 70 | * | * | 6 | _ | _ | Χ | - | Х | - | - |



3 Phase Fully Actuated US 601 (Yadkinville) System 1 (TBC)

PROJECT REFERENCE NO.

U-5809

<u>NOTES</u>

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 may be lagged.
- 4. Set all detector units to presence mode.
- 5. Program controller to operate using FYA compact mode.
- 6. Refer to Pavement Marking Plan for stop line locations.
- 7. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 8. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
- 7. Time Based system data: Controller Asset #: 1208.
- 10. Reposition existing signal heads numbered 21 and 22.

| | LEGEND | |
|---------------------|---|-------------------------|
| <u>PROPOSED</u> | | EXISTING |
| \bigcirc | Traffic Signal Head | • |
| O -> | Modified Signal Head | N/A |
| <u> </u> | Sign | ⊣ |
| \downarrow | Pedestrian Signal Head With Push Button & Sign | + |
| <u> </u> | Signal Pole with Guy | • |
| | Signal Pole with Sidewalk Guy | , • |
| | Inductive Loop Detector | $\subseteq = = \supset$ |
| \boxtimes | Controller & Cabinet | ×× |
| | Junction Box | |
| | 2-in Underground Conduit | |
| N/A | Right of Way | |
| \longrightarrow | Directional Arrow | \longrightarrow |
| | Video Detection Zone | |
| | Construction Zone | N/A |
| $\langle A \rangle$ | "YIELD" Sign (R1-2) | \triangle |
| B | No Left Turn Sign (R3-2) | B |
| N/A | Guardrail | 1 1 |

| Signal | Upgrade | - | Temporary | Design | 2 | - | Phase | ΙI |
|--------|---------|---|-----------|--------|---|---|-------|----|



US 601 (S. State Street) at US 421 Southbound Ramp

Division 11 Yadkin County Yadkinville

PLAN DATE: February 2024 REVIEWED BY: R.N. Zinser

PREPARED BY: T.A. Kenion REVIEWED BY:

SCALE

PREPARED BY: T.A. Kenion REVIEWED BY:

SEAL

043914

SEAL

043914

NGINEER

NOTICE

N

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

| MAX. | TIME T | IMING | CHART | | | | | |
|-------------------------|--------|------------|-------|------------|--|--|--|--|
| FEATURE | PHASE | | | | | | | |
| FEATURE | 1 | 2 | 4 | 6 | | | | |
| Walk * | _ | _ | - | _ | | | | |
| Ped Clear | _ | _ | _ | _ | | | | |
| Min Green * | 7 | 10 | 7 | 10 | | | | |
| Passage * | 2.0 | 3.0 | 2.0 | 3.0 | | | | |
| Max 1 * | 15 | 60 | 30 | 60 | | | | |
| Yellow Change | 3.0 | 3.9 | 4.1 | 3.9 | | | | |
| Red Clear | 2.8 | 2.8 | 1.2 | 2.8 | | | | |
| Added Initial * | _ | _ | _ | _ | | | | |
| Maximum Initial * | _ | _ | _ | _ | | | | |
| Time Before Reduction * | _ | _ | _ | _ | | | | |
| Time To Reduce * | _ | _ | - | _ | | | | |
| Minimum Gap | _ | _ | _ | _ | | | | |
| Advance Walk | _ | _ | _ | _ | | | | |
| Non Lock Detector | Х | _ | Х | _ | | | | |
| Vehicle Recall | _ | MIN RECALL | - | MIN RECALL | | | | |
| Dual Entry | _ | _ | _ | _ | | | | |

* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not

REMOVE DIODE JUMPERS 1-2, 1-6, 2-6, 4-7 and 6-9. ー SF#1 POLARITY 🧖 LEDguard
 □ — FYA 5-11 ______ FYA 7-12 _____ **COMPONENT SIDE REMOVE JUMPERS AS SHOWN**

ON

= DENOTES POSITION OF SWITCH

WD ENABLE 🕥

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 4. The cabinet and controller are part of the US 601 (Yadkinville) System 1 (TBC).

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|----------|
| Cabinet | |
| Software | |
| Cabinet Mount | |
| | |
| Output File Positions | |
| Load Switches Used | |
| Phases Used | , , , |
| Overlap "1" | |
| Overlap "2" | |
| Overlap "3" | |
| Overlap "4" | Not Used |
| Overlap "7" | * |
| | |

*See overlap programming detail on sheet 2

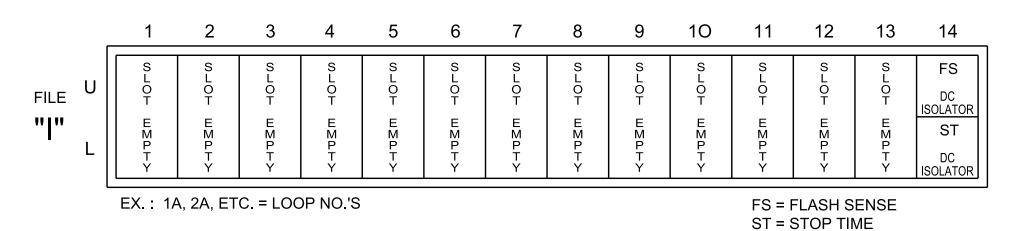
Sig. 4.1

| SIGNAL HEAD HOOK-UP CHART | | | | | | | | | | | | | | | |
|-----------------------------|----------------|-----|-----|-------|----------|----|-------|----------|----|-----|-----|----------|-----|-----|----------|
| LOAD SWITCH NO. | S1 | S | 2 | S | 3 | S4 | S5 | S6 | S7 | S | 8 | S9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 2 | 9 | 13 | 3 | 4 | 14 | 5 | (| 6 | 15 | 7 | 8 | 16 |
| PHASE | OL1 | 2 | 2 | 1 GRN | 2 PED | 3 | 4 | 4 PED | 5 | (| 6 | 6 PED | OL7 | 8 | 8 PED |
| SIGNAL HEAD NO. | ★ 11 | 21 | 22 | 11 | NU | NU | 41,42 | NU | NU | 61 | 62 | NU | 43 | NU | NU |
| RED | | 128 | 128 | | | | 101 | | | 134 | 134 | | 122 | | |
| YELLOW | | 129 | 129 | | | | 102 | | | 135 | 135 | | | | |
| GREEN | | | 130 | | | | 103 | | | 136 | | | | | |
| RED ARROW | 125 | | | | | | | | | | | | | | |
| YELLOW ARROW | 126 | | | | | | | | | | | | 123 | | |
| FLASHING YELLOW ARROW | 127 | | | | | | | | | | | | 124 | | |
| ₩ | | | | | | | | | | | | | | | |
| GREEN ARROW | | 130 | | 114 | | | | | | | 136 | | | | |
| × | | | | | * | | | | | | | | | | |
| NU = Not l | lood | | | | | | | | | | | | | | |

★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

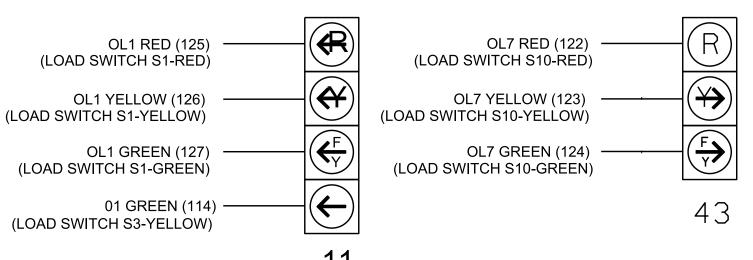
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min) Phase 2 Walk Field Terminal (115)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1208T2

DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A

Electrical Detail - Sheet 1 of 2

Prepared in the Offices of:

US 601 (S. State Street) US 421 Southbound Ramp

Yadkin County

Yadkinville PLAN DATE: November 2023 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

031001 D. told Joyce 03/06/2024 SIG. INVENTORY NO. 11-1208T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

NOTES:

^{*}Denotes install load resistor. See load resistor installation detail this sheet.

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

| | Channel | Control Type | Control Source | Flash Yellow | Flash Red | Flash Alt | MMU Channel |
|-----------------------|---------|---------------|----------------|--------------|-----------|-----------|-------------|
| NOTE: Phase Vehicle 1 | 1 | Overlap | 1 | Х | | Χ | 1 |
| Changed to Overlap 1 | 2 | Phase Vehicle | 2 | Х | | | 2 |
| | 3 | Phase Vehicle | 3 | | Х | Х | 3 |
| | 4 | Phase Vehicle | 4 | | Х | | 4 |
| | 5 | Phase Vehicle | 5 | | Х | | 5 |
| | 6 | Phase Vehicle | 6 | Х | | Х | 6 |
| OTE: Phase Vehicle 7 | 7 | Overlap | 7 | | Х | | 7 |
| Changed to Overlap 7 | 8 | Phase Vehicle | 8 | | Х | Х | 8 |
| | 9 | Overlap | 1 | Х | | Х | 9 |
| | 10 | Overlap | 2 | | Х | Х | 10 |
| | 11 | Overlap | 3 | Х | | | 11 |
| | 12 | Overlap | 4 | | Х | | 12 |
| | 13 | Phase Ped | 2 | | | | 13 |
| | 14 | Phase Ped | 4 | | | | 14 |
| | 15 | Phase Ped | 6 | | | | 15 |
| | 16 | Phase Ped | 8 | | | | 16 |
| | 17 | Overlap | 5 | | Х | Х | 17 |
| | 18 | Overlap | 6 | | Х | | 18 |

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to Channel 9 Green (monitor pin 13).

Follow the instructions below to make the appropriate connections:

STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

| 1-2PY 2-4PY 3-6PY | 1-CMU-13 2-CMU-16 3-CMU-R |
|-------------------------|---------------------------------|
| 4 - 8PY | 4-CMU-U |

FYA SIGNAL OUTPUT REMAPPING ASSIGNMENT PROGRAMMING DETAIL FOR SIGNAL HEAD 11

Front Panel

Main Menu >Controller >More >Advanced IO >Output Points

Web Interface

Home >Controller >Advanced IO >Cabinet Configuration >Output Points

IO Module 1

NOTICE OUTPUT POINT 33
CONTROL TYPE & INDEX
REASSIGNMENT

| Output Point | Descripton | Output Control Type | Index |
|--------------|------------|---------------------|-------|
| 33 | C1-35 | Phase Green | 1 |
| 34 | C1-36 | Not Active | 15 |
| 35 | C1-37 | Not Active | 14 |
| 36 | C1-38 | Not Active | 16 |

OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

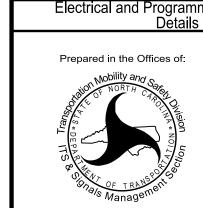
Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| 1 | 7 |
|-----------------|-------------------------|
| FYA 4 - Section | FYA 4 - Section |
| 2 | 4 |
| 1 | - |
| - | - |
| 0 | 0 |
| 0.0 | 0.0 |
| 0.0 | 0.0 |
| | 2 1 - 0 0.0 |

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1208T2 DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A Electrical Detail - Sheet 2 of 2



US 601 (S. State Street) at US 421 Southbound Ramp

Division 11 Yadkin County Yadkinville

PLAN DATE: November 2023 REVIEWED BY:
PREPARED BY: Zarrar Zafar REVIEWED BY:
REVISIONS INIT. DATE

SEAL
031001

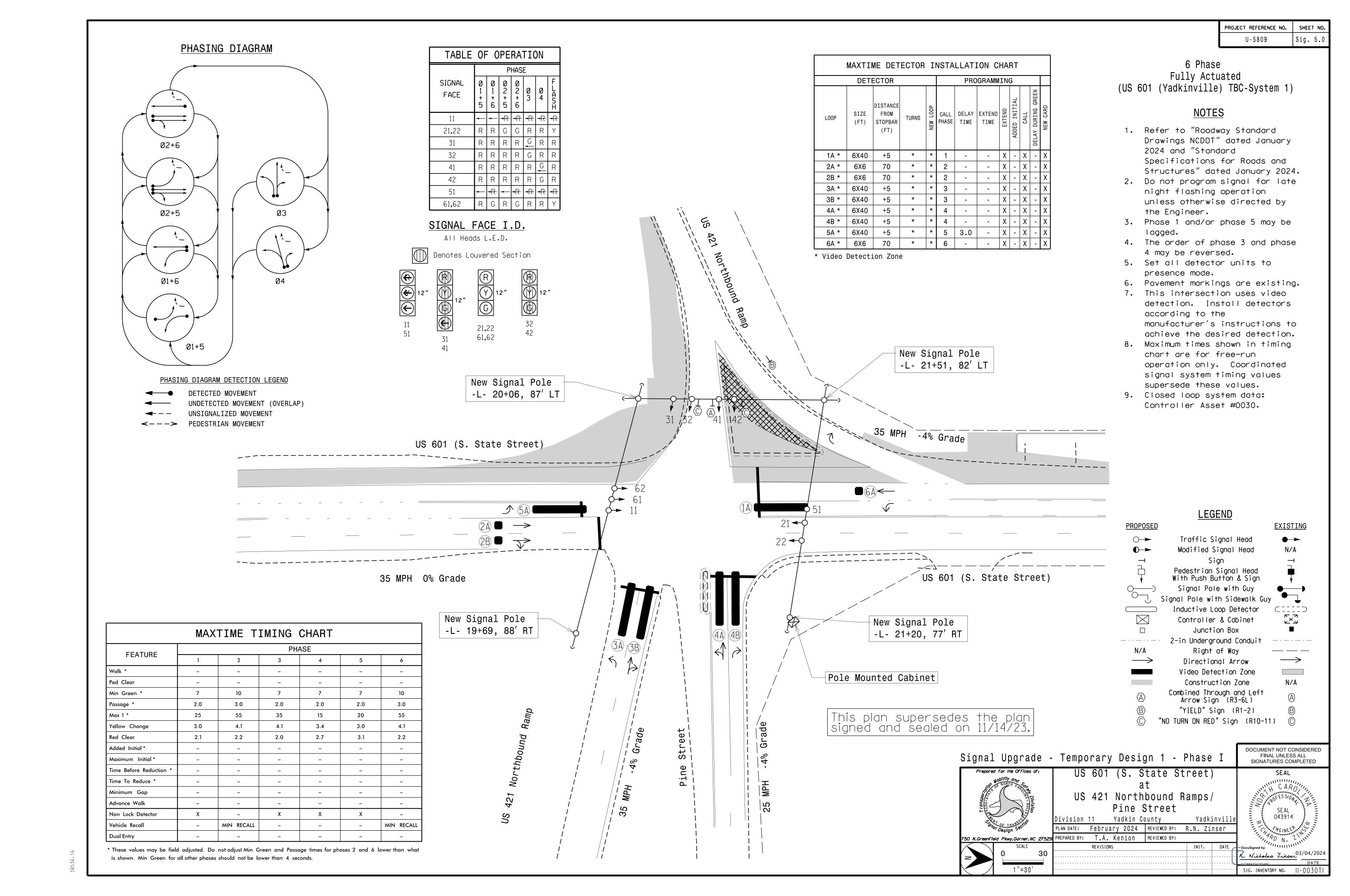
Docusigned by:

0. Todd Joya 03/06/2024

SIG. INVENTORY NO. 11-1208T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

S:*ITS&SU*ITS Signals*Workgroups*Sig Man*Zafar*Plans*Div



- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 4. The cabinet and controller are part of the US 601 (Yadkinville) TBC-System 1.

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|------------------------|
| Cabinet | .336 |
| Software | .Q-Free MAXTIME |
| Cabinet Mount | .Pole |
| Output File Positions | .12 |
| Load Switches Used | S1, S2, S4, S5, S7, S8 |
| Phases Used | 1, 2, 3, 4, 5, 6 |
| Overlaps | None |

| PROJECT REFERENCE NO. | SHEET NO |
|-----------------------|----------|
| U-5809 | Sig. 5.1 |

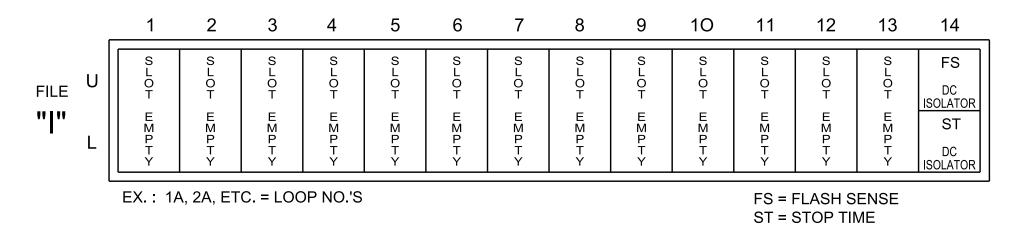
| | SIGNAL HEAD HOOK-UP CHART | | | | | | | | | | | | | |
|-----------------------------|---------------------------|-------|----------|-----|-----|-----|-----|----------|-----|-------|----------|-----|-----|-------------|
| LOAD SWITCH NO. | S1 | S2 | S3 | S | 4 | S | 5 | S6 | S7 | S8 | S9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 3 | | 1 | 14 | 5 | 6 | 15 | 7 | 8 | 16 |
| PHASE | 1 | 2 | 2 PED | 3 | 3 | 4 | | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED |
| SIGNAL HEAD NO. | 11 | 21.22 | NU | 31 | 32 | 41 | 42 | NU | 51 | 61,62 | NU | NU | NU | P81, P82 |
| RED | | 128 | | 116 | 116 | 101 | 101 | | | 134 | | | | |
| YELLOW | | 129 | | 117 | 117 | 102 | 102 | | | 135 | | | | |
| GREEN | | 130 | | 118 | 118 | 103 | 103 | | | 136 | | | | |
| RED ARROW | 125 | | | | | | | | 131 | | | | | |
| YELLOW ARROW | 126 | | | | | | | | 132 | | | | | |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | | |
| GREEN ARROW | 127 | | | 118 | | 103 | | | 133 | | | | | |

NU = Not Used

INPUT FILE POSITION LAYOUT

= DENOTES POSITION OF SWITCH

(front view)



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

This Plan Supersedes Electrical Detail Sealed on 11/16/2023

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0030T1 DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A

Electrical Detail

Electrical and Programming Details For: Prepared in the Offices of:

US 601 (S. State Street) US 421 Northbound Ramps/

Pine Street

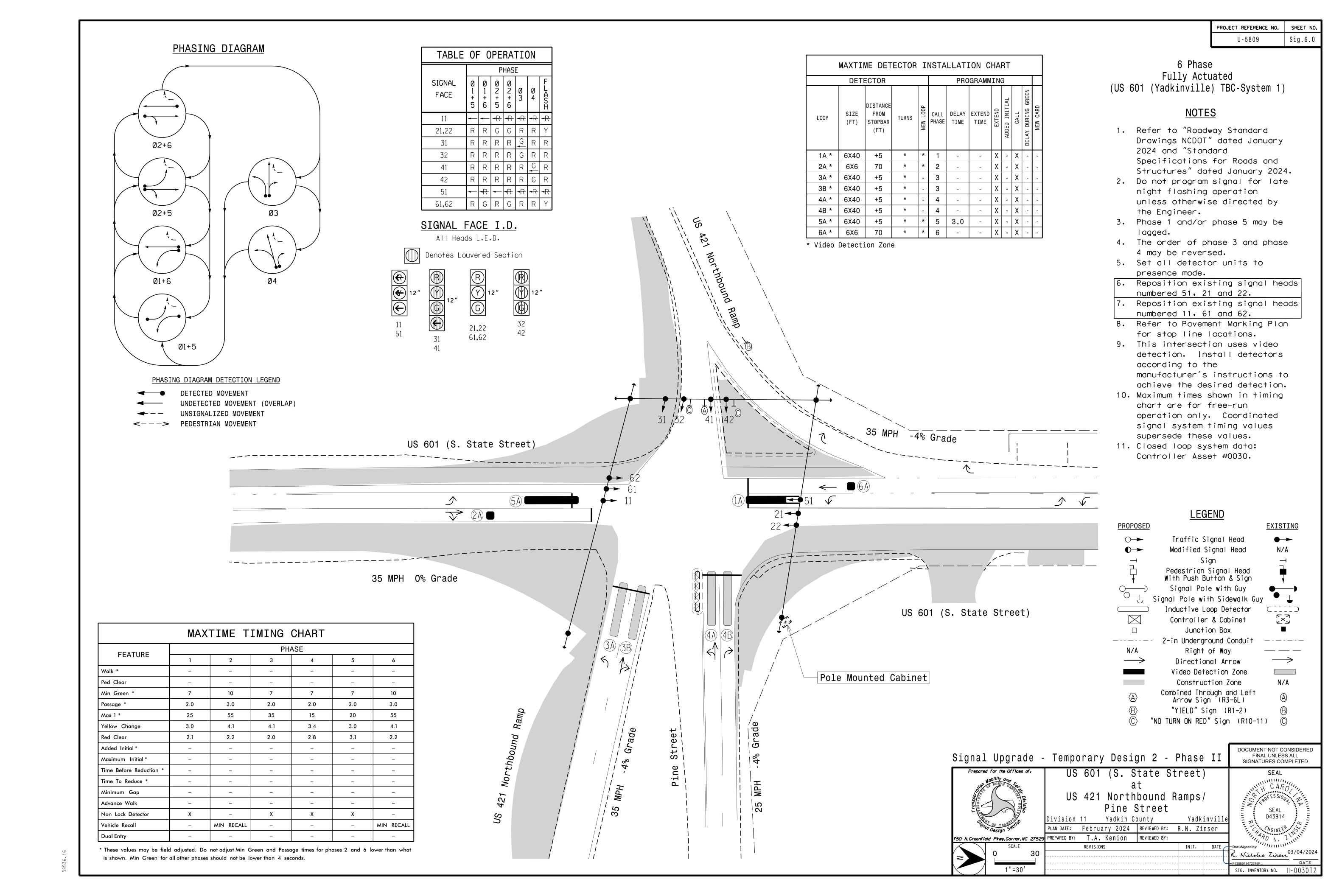
—Docusigned by:

1. Told Joya 03/06/2024 SIG. INVENTORY NO. 11-0030T1

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

031001

Yadkinville PLAN DATE: February 2024 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS



- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 4. The cabinet and controller are part of the US 601 (Yadkinville) TBC-System 1.

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|------------------------|
| Cabinet | 336 |
| Software | Q-Free MAXTIME |
| Cabinet Mount | Pole |
| Output File Positions | 12 |
| oad Switches Used | S1, S2, S4, S5, S7, S8 |
| Phases Used | 1, 2, 3, 4, 5, 6 |
| Overlaps | None |

Sig. 6.1

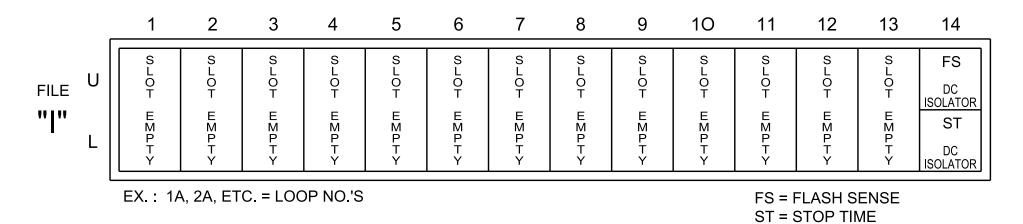
| | SIGNAL HEAD HOOK-UP CHART | | | | | | | | | | | | | |
|-----------------------------|---------------------------|-------|----------|-----|-----|-----|-----|----------|-----|-------|----------|-----|-----|-------------|
| LOAD SWITCH NO. | S1 | S2 | S3 | S | 4 | S5 | | S6 | S7 | S8 | S9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 3 | | 1 | 14 | 5 | 6 | 15 | 7 | 8 | 16 |
| PHASE | 1 | 2 | 2 PED | 3 | 3 | 4 | | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED |
| SIGNAL HEAD NO. | 11 | 21.22 | NU | 31 | 32 | 41 | 42 | NU | 51 | 61,62 | NU | NU | NU | P81, P82 |
| RED | | 128 | | 116 | 116 | 101 | 101 | | | 134 | | | | |
| YELLOW | | 129 | | 117 | 117 | 102 | 102 | | | 135 | | | | |
| GREEN | | 130 | | 118 | 118 | 103 | 103 | | | 136 | | | | |
| RED ARROW | 125 | | | | | | | | 131 | | | | | |
| YELLOW ARROW | 126 | | | | | | | | 132 | | | | | |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | | |
| GREEN ARROW | 127 | | | 118 | | 103 | | | 133 | | | | | |

NU = Not Used

INPUT FILE POSITION LAYOUT

= DENOTES POSITION OF SWITCH

(front view)



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0030T2 DESIGNED: February 2024

SEALED: 3/4/2024 REVISED: N/A

Electrical Detail

Prepared in the Offices of:

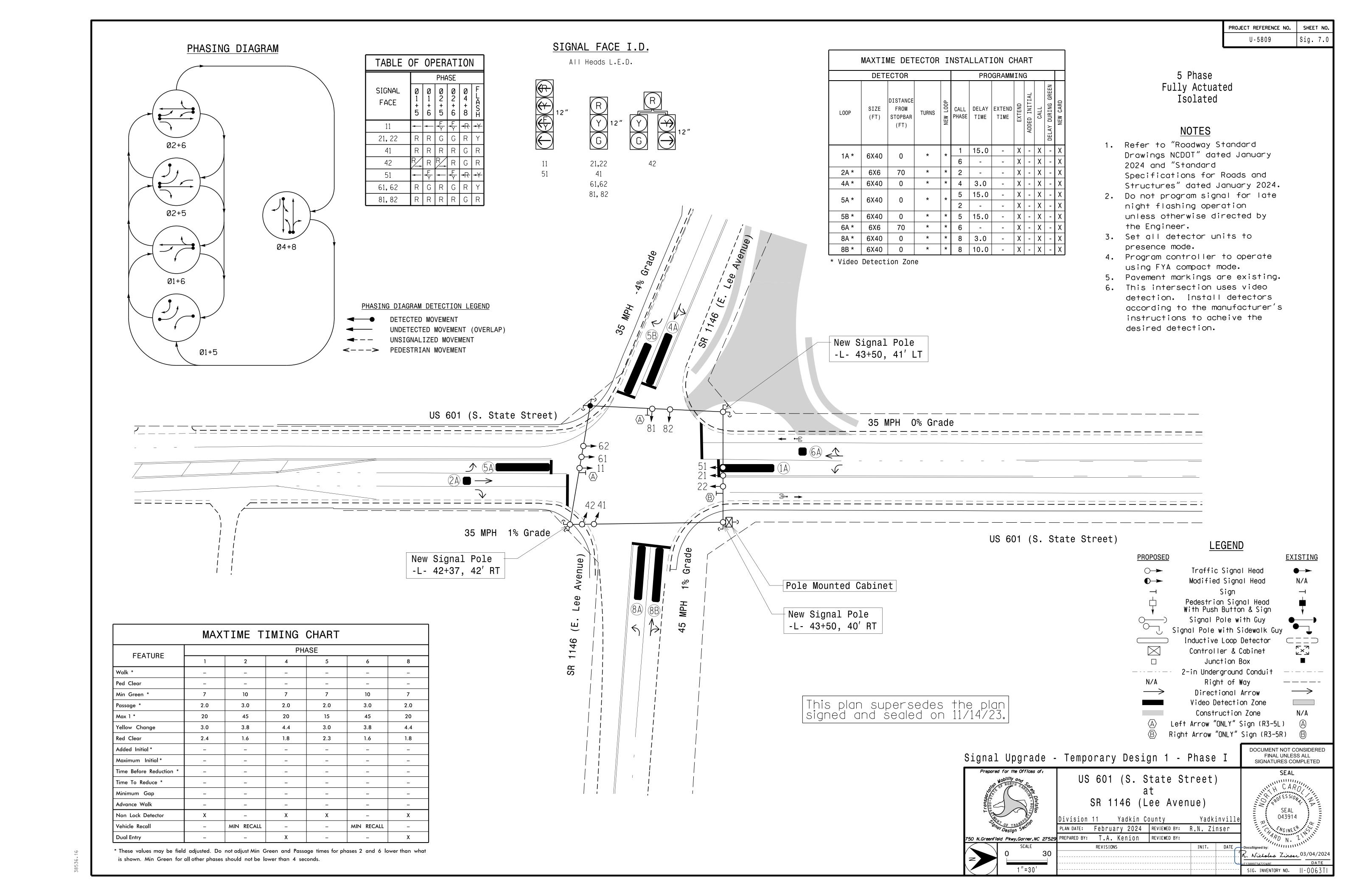
US 601 (S. State Street) US 421 Northbound Ramps/

Pine Street Yadkin County

Yadkinville PLAN DATE: February 2024 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 031001 D. told Joya 03/06/2024

SIG. INVENTORY NO. 11-0030T2



- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|--------------------------------------|
| Cabinet | 336 |
| Software | Q-Free MAXTIME |
| Cabinet Mount | Pole |
| Output File Positions | 18 With Aux. Output File |
| Load Switches Used | S1, S2, S3, S5, S7, S8, S9, S10, S11 |
| Phases Used | 1, 2, 4, 5, 6, 8 |
| Overlap "1" | * |
| Overlap "2" | Not Used |
| Overlap "3" | * |
| Overlap "4" | Not Used |
| Overlap "7" | * |

*See overlap programming detail on sheet 2

| | SIGNAL HEAD HOOK-UP CHART | | | | | | | | | | | | | |
|-----------------------------|---------------------------|-------|----------------|----------|----|-------|----------|----------------|-------|----------------|----------|-----|-------|----------|
| LOAD SWITCH NO. | S1 | S2 | S | 3 | S4 | S5 | S6 | S7 | S8 | S | 9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 9 | 13 | 3 | 4 | 14 | 5 | 6 | 11 | 15 | 7 | 8 | 16 |
| PHASE | OL1 | 2 | 1 GRN | 2 PED | 3 | 4 | 4 PED | OL3 | 6 | 5 GRN | 6 PED | OL7 | 8 | 8 PED |
| SIGNAL HEAD NO. | 11 | 21,22 | ★ 11 | NU | NU | 41,42 | NU | ★ 51 | 61,62 | ★ 51 | NU | 42 | 81,82 | NU |
| RED | | 128 | | | | 101 | | | 134 | | | * | 107 | |
| YELLOW | | 129 | | | | 102 | | | 135 | | | | 108 | |
| GREEN | | 130 | | | | 103 | | | 136 | | | | 109 | |
| RED ARROW | 125 | | | | | | | 131 | | | | | | |
| YELLOW ARROW | 126 | | | | | | | 132 | | | | 123 | | |
| FLASHING YELLOW ARROW | 127 | | | | | | | 133 | | | | | | |
| ₩ | | | | | | | | | | | | | | |
| GREEN ARROW | | | 114 | | | | | | | 120 | | 124 | | |
| × | | | | * | | | | | | | * | | | |
| NU = Not I | lead | | | | | | | | • | | | | | |

★See pictorial of head wiring in detail this sheet.

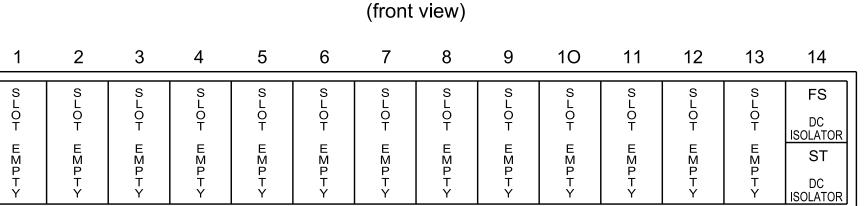
INPUT FILE POSITION LAYOUT

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



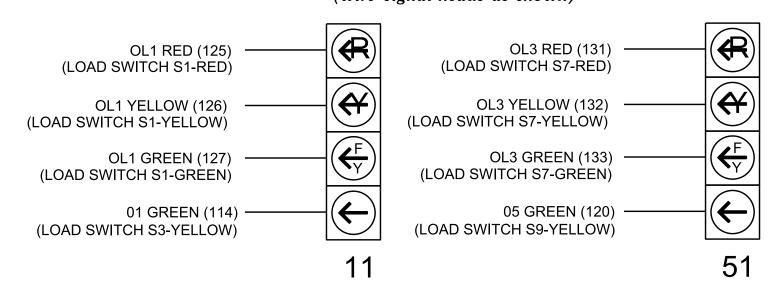
FS = FLASH SENSE ST = STOP TIME

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

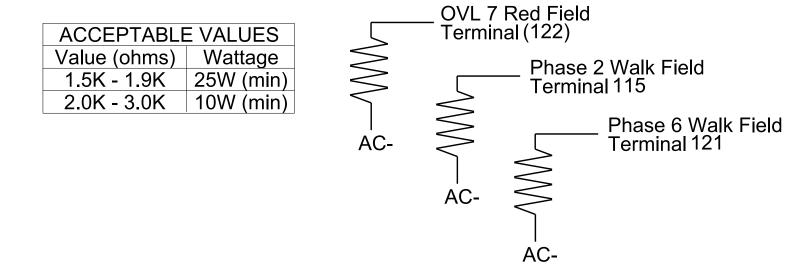
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



This Plan Supersedes Electrical Detail Sealed on 11/16/2023

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0063T1 DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A

| Electrical Detail - Sheet 1 of 2

Prepared in the Offices of:

US 601 (S. State Street) SR 1146 (Lee Avenue)

Yadkin County

Yadkinville February 2024 PLAN DATE: REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

D. Told Joya 03/06/2024 SIG. INVENTORY NO. 11-0063T1

031001

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

NOTES:

FILE

EX.: 1A, 2A, ETC. = LOOP NO.'S

^{*}Denotes install load resistor. See load resistor installation detail this sheet.

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

| | Channel | Control Type | Control Source | Flash Yellow | Flash Red | Flash Alt | MMU Channel |
|-----------------------|---------|---------------|----------------|--------------|-----------|-----------|-------------|
| NOTE: Phase Vehicle 1 | 1 | Overlap | 1 | Х | | Х | 1 |
| Changed to Overlap 1 | 2 | Phase Vehicle | 2 | Х | | | 2 |
| | 3 | Phase Vehicle | 3 | | Х | Х | 3 |
| | 4 | Phase Vehicle | 4 | | Х | | 4 |
| NOTE: Phase Vehicle 5 | 5 | Overlap | 3 | Х | | | 5 |
| Changed to Overlap 3 | 6 | Phase Vehicle | 6 | Х | | Х | 6 |
| NOTE: Phase Vehicle 7 | 7 | Overlap | 7 | | Х | | 7 |
| Changed to Overlap 7 | 8 | Phase Vehicle | 8 | | Х | Х | 8 |
| | 9 | Overlap | 1 | Х | | Х | 9 |
| | 10 | Overlap | 2 | | Х | Х | 10 |
| | 11 | Overlap | 3 | Х | | | 11 |
| | 12 | Overlap | 4 | | Х | | 12 |
| | 13 | Phase Ped | 2 | | | | 13 |
| | 14 | Phase Ped | 4 | | | | 14 |
| | 15 | Phase Ped | 6 | | | | 15 |
| | 16 | Phase Ped | 8 | | | | 16 |
| | 17 | Overlap | 5 | | Х | Х | 17 |
| | 18 | Overlap | 6 | | Х | | 18 |

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to Channel 9 Green (monitor pin 13), from 6 PY (field term. 120) to Channel 10 Green (monitor pin R).

Follow the instructions below to make the appropriate connections:

STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

CMU-13 2PY (term. 114)

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

FYA SIGNAL OUTPUT REMAPPING ASSIGNMENT PROGRAMMING DETAIL FOR SIGNAL HEADS 11 & 51

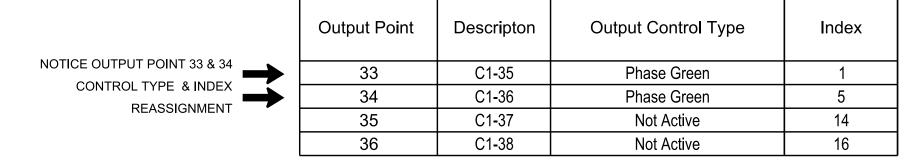
Front Panel

Main Menu >Controller >More >Advanced IO >Output Points

Web Interface

Home >Controller >Advanced IO >Cabinet Configuration >Output Points

IO Module 1



OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

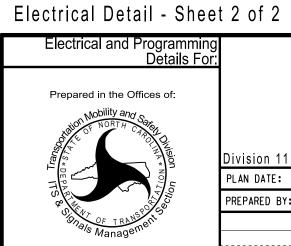
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| Overlap | 1 | 3 | 7 |
|-------------------|-----------------|-----------------|--------|
| Type | FYA 4 - Section | FYA 4 - Section | Normal |
| Included Phases | 2 | 6 | 5 |
| Modifier Phases | 1 | 5 | - |
| Modifier Overlaps | - | - | • |
| Trail Green | 0 | 0 | 0 |
| Trail Yellow | 0.0 | 0.0 | 0.0 |
| Trail Red | 0.0 | 0.0 | 0.0 |

This Plan Supersedes Electrical Detail Sealed on 11/16/2023

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0063T1 DESIGNED: February 2024 SEALED: 3/4/2024 REVISED: N/A



US 601 (S. State Street) at SR 1146 (Lee Avenue)

vision 11 Yadkin County Yadkinville

AN DATE: February 2024 REVIEWED BY:

Division 11 Yadkin County Yadkin
PLAN DATE: February 2024 REVIEWED BY:
PREPARED BY: Zarrar Zafar REVIEWED BY:
REVISIONS INIT. DA

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL

O31001

Docusigned by:

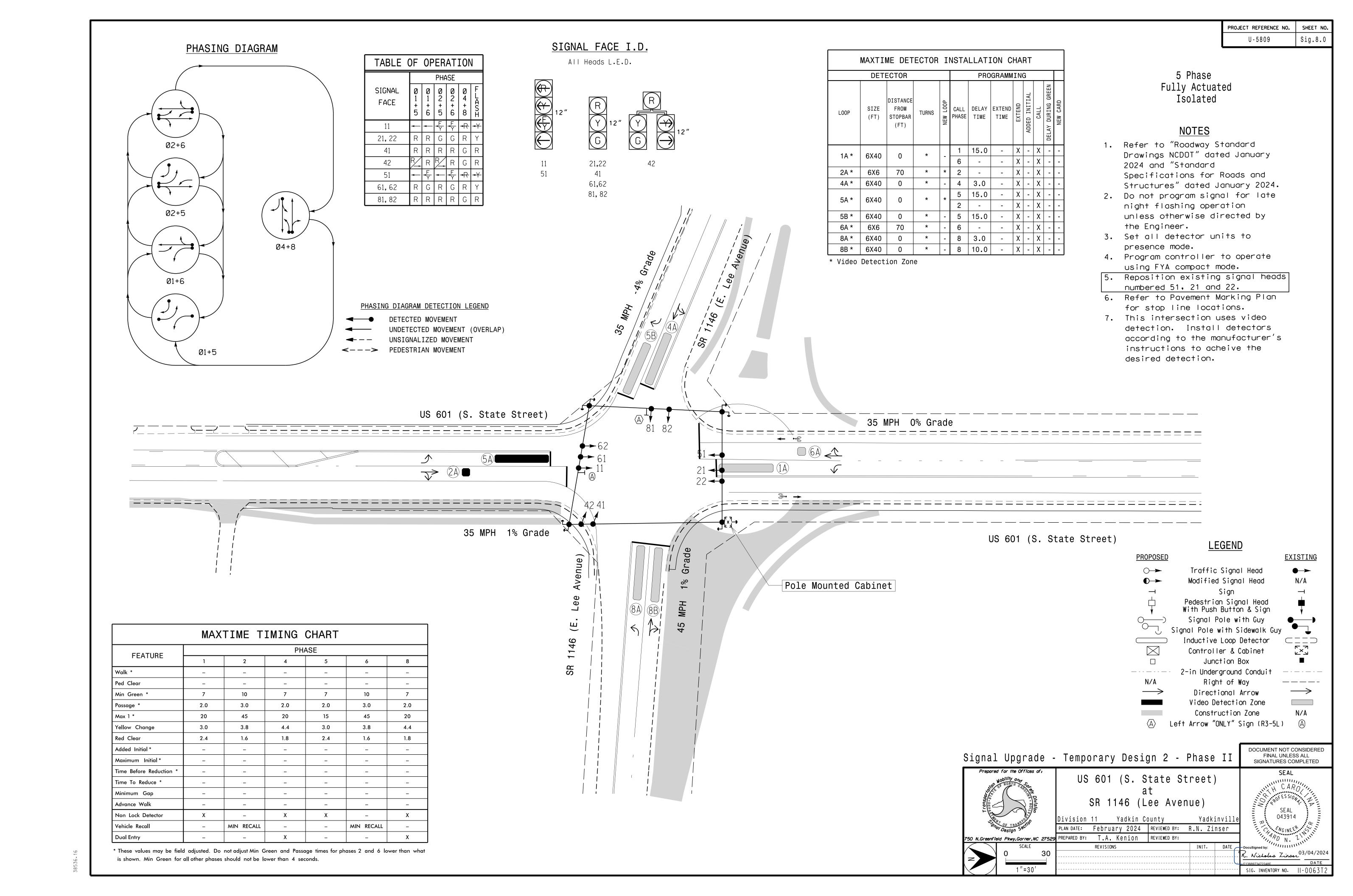
D. Told Joya 03/06/2024

A90CADEDBD4241D DATE

SIG. INVENTORY NO. 11-0063T1

PROJECT REFERENCE NO.

:*|TS&SU*|TS Signals*Workgroups*Sig Man*Zafar*Plans*Division 11*U-5809*1100



- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

| Controller | 2070LX |
|-----------------------|--------------------------------------|
| Cabinet | 336 |
| Software | Q-Free MAXTIME |
| Cabinet Mount | Pole |
| Output File Positions | 18 With Aux. Output File |
| Load Switches Used | S1, S2, S3, S5, S7, S8, S9, S10, S11 |
| Phases Used | 1, 2, 4, 5, 6, 8 |
| Overlap "1" | * |
| Overlap "2" | Not Used |
| Overlap "3" | * |
| Overlap "4" | Not Used |
| Overlap "7" | * |

*See overlap programming detail on sheet 2

| PROJECT REFERENCE NO. | SHEET |
|-----------------------|--------|
| U-5809 | Sig. 8 |
| | |

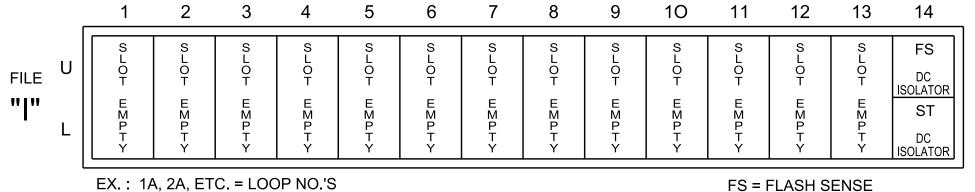
| | SIGNAL HEAD HOOK-UP CHART | | | | | | | | | | | | | |
|-----------------------------|---------------------------|-------|----------------|----------|----|-------|----------|----------------|-------|----------------|----------|-----|-------|----------|
| LOAD SWITCH NO. | S1 | S2 | S | 3 | S4 | S5 | S6 | S7 | S8 | S | 9 | S10 | S11 | S12 |
| CMU CHANNEL NO. | 1 | 2 | 9 | 13 | 3 | 4 | 14 | 5 | 6 | 11 | 15 | 7 | 8 | 16 |
| PHASE | OL1 | 2 | 1 GRN | 2 PED | 3 | 4 | 4 PED | OL3 | 6 | 5 GRN | 6 PED | OL7 | 8 | 8 PED |
| SIGNAL HEAD NO. | 11 | 21,22 | ★ 11 | NU | NU | 41,42 | NU | ★ 51 | 61,62 | ★ 51 | NU | 42 | 81,82 | NU |
| RED | | 128 | | | | 101 | | | 134 | | | * | 107 | |
| YELLOW | | 129 | | | | 102 | | | 135 | | | | 108 | |
| GREEN | | 130 | | | | 103 | | | 136 | | | | 109 | |
| RED ARROW | 125 | | | | | | | 131 | | | | | | |
| YELLOW ARROW | 126 | | | | | | | 132 | | | | 123 | | |
| FLASHING YELLOW ARROW | 127 | | | | | | | 133 | | | | | | |
| ₩ | | | | | | | | | | | | | | |
| GREEN ARROW | | | 114 | | | | | | | 120 | | 124 | | |
| Ϋ́ | | | | * | | | | | | | * | | | |

NU = Not Used

★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

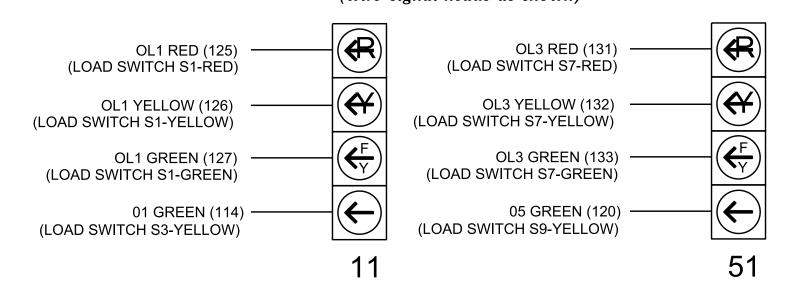
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

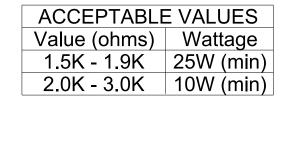
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

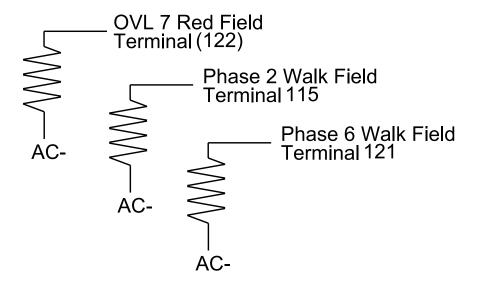


LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



NOTES:



ST = STOP TIME

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0063T2 DESIGNED: February 2024

SEALED: 3/4/2024 REVISED: N/A

| Electrical Detail - Sheet 1 of 2

US 601 (S. State Street) Prepared in the Offices of: SR 1146 (Lee Avenue)

Yadkin County Yadkinville PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

031001 D. Told Joyce 03/06/2024

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 11-0063T2

^{*}Denotes install load resistor. See load resistor installation detail this sheet.

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

| | Channel | Control Type | Control Source | Flash Yellow | Flash Red | Flash Alt | MMU Channel |
|-----------------------|---------|---------------|----------------|--------------|-----------|-----------|-------------|
| NOTE: Phase Vehicle 1 | 1 | Overlap | 1 | Х | | X | 1 |
| Changed to Overlap 1 | 2 | Phase Vehicle | 2 | Х | | | 2 |
| | 3 | Phase Vehicle | 3 | | Х | Х | 3 |
| | 4 | Phase Vehicle | 4 | | Х | | 4 |
| NOTE: Phase Vehicle 5 | 5 | Overlap | 3 | Х | | | 5 |
| Changed to Overlap 3 | 6 | Phase Vehicle | 6 | Х | | Х | 6 |
| NOTE: Phase Vehicle 7 | 7 | Overlap | 7 | | Х | | 7 |
| Changed to Overlap 7 | 8 | Phase Vehicle | 8 | | Х | Х | 8 |
| | 9 | Overlap | 1 | Х | | Х | 9 |
| | 10 | Overlap | 2 | | Х | Х | 10 |
| | 11 | Overlap | 3 | Х | | | 11 |
| | 12 | Overlap | 4 | | Х | | 12 |
| | 13 | Phase Ped | 2 | | | | 13 |
| | 14 | Phase Ped | 4 | | | | 14 |
| | 15 | Phase Ped | 6 | | | | 15 |
| | 16 | Phase Ped | 8 | | | | 16 |
| | 17 | Overlap | 5 | | Х | Х | 17 |
| | 18 | Overlap | 6 | | Х | | 18 |

18

PROJECT REFERENCE NO. SHEET

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to Channel 9 Green (monitor pin 13), from 6 PY (field term. 120) to Channel 10 Green (monitor pin R).

Follow the instructions below to make the appropriate connections:

STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

CMU-13 — 2PY (term. 114)
CMU-R — 6PY (term. 120)

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

| | _ | |
|--------------------|---|---------------------|
| | | |
| 1 - 2PY | | 1-CMU-13 |
| 2 - 4PY | | 2-CMU-16 |
| 3-6PY | | 2-CMU-16 3-CMU-R |
| 4-8PY | | 4-CMU-U |

FYA SIGNAL OUTPUT REMAPPING ASSIGNMENT PROGRAMMING DETAIL FOR SIGNAL HEADS 11 & 51

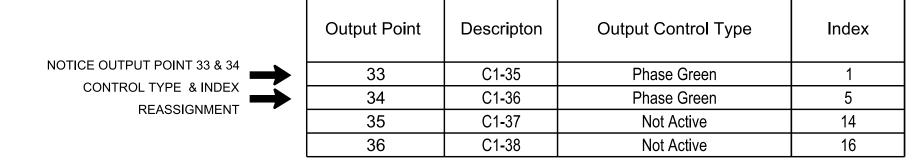
Front Panel

Main Menu >Controller >More >Advanced IO >Output Points

Web Interface

Home >Controller >Advanced IO >Cabinet Configuration >Output Points

IO Module 1



OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

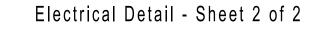
Home >Controller >Overlap Configuration >Overlaps

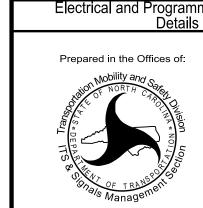
Overlap Plan 1

| Overlap | 1 | 3 | 7 |
|-------------------|-----------------|-----------------|--------|
| Type | FYA 4 - Section | FYA 4 - Section | Normal |
| Included Phases | 2 | 6 | 5 |
| Modifier Phases | 1 | 5 | |
| Modifier Overlaps | - | - | 1 |
| Trail Green | 0 | 0 | 0 |
| Trail Yellow | 0.0 | 0.0 | 0.0 |
| Trail Red | 0.0 | 0.0 | 0.0 |

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0063T2 DESIGNED: February 2024 SEALED: 3/4/2024

REVISED: N/A





US 601 (S. State Street) at SR 1146 (Lee Avenue)

Division 11 Yadkin County Yadkinville
PLAN DATE: February 2024 REVIEWED BY:
PREPARED BY: 7 arrar 7 afar REVIEWED BY:

PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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Docusigned by:

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