

I/O LOGIC PROGRAMMING DETAIL FOR VEHICLE LOOP 4A TO CALL TWO PHASES

This logic will allow loop 4A to call vehicle phase 7 when Preempt 1 is active.

(program controller as shown below)

FROM MAIN MENU, KEYSTROKES 1-8

Channel & I/O		
1.Chan 1-16	4.Chan+ 1-16	7.I/O Logic
2.chan 17-24	5.chan+ 17-24	8.I/O Viewer
3.Chan Parm	6.I/O Parm	9.I/O UserMap

Result	Src.Fcn	Op	Src.Fcn	Op	Src.Fcn	>
I 21	=	01 8 &	01198		01 0	
I 0	=	01 0	01 0		01 0	
I 0	=	01 0	01 0		01 0	
I 0	=	01 0	01 0		01 0	
I 0	=	01 0	01 0		01 0	
I 0	=	01 0	01 0		01 0	
I 0	=	01 0	01 0		01 0	

I/O REFERENCE SCHEDULE	
INPUT FUNCTION 8	= Vehicle 4 Call
INPUT FUNCTION 21	= Vehicle 7 Call
INPUT FUNCTION 198	= Preempt 1 In

STARTUP PHASES PROGRAMMING DETAIL

(program controller as shown below)

This signal omits phases 3 and 7 during normal operation and serves them only during preemption. In order to make sure phases 3 and 7 are not served during normal phasing, the controller startup phases must be specified by the user. This is accomplished as shown in the programming steps below.

STEP 1

From the Main Menu, keystrokes 1-2-1 (Unit Parameters). Press the down arrow key until the "StartupCalls" is displayed on the left of the display. Change the value to "UsePrg" and press the "ent" key. The controller is now ready for the user to specify exactly which phases should be called for service at startup (see Step 2).

STEP 2

From the Main Menu, keystrokes 1-1-3 (Phase Options+). Press the down arrow key until "StartupVehCall" and "StartupPedCall" are displayed on the left of the display. Select phases 2, 4, 6, and 8 for "StartupVehCall" and "StartupPedCall". The controller will now serve these vehicle and ped phases exclusively at startup.

PHASE 4 INHIBIT PROGRAMMING DETAIL

(program controller as shown below)

This programming prevents the controller from serving phase 4 and 7 simultaneously in the event preemption ends while phase 7 is being served.

From Main Menu press '1' (Controller), then '1' (Phases), then '5' (Call, Inh, Redirect).

P	..Call.Ps..	Inhibit Ps	1111111	>
1	0 0 0 0	12345678	90123456	
2	0 0 0 0	
3	0 0 0 0	
4	0 0 0 0	
5	0 0 0 0	
6	0 0 0 0	
7	0 0 0 0	...X...	
8	0 0 0 0	

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:


- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0424
 DESIGNED: November 2023
 SEALED: 1/17/2024
 REVISED: N/A

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 JIPeter@son

Electrical Detail - Sheet 5 of 5

Prepared In the Offices of:

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Electrical and Programming Details For: SR 3163 (E. Market Street) at Sykes Ave. & Lowdermilk Street

Division 7 Guilford County Greensboro

PLAN DATE: January 2024 REVIEWED BY:

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by: Ryan W. Hough 01/19/2024

SIG. INVENTORY NO. 07-0424

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

