

SEQUENCE DETAIL

Front Panel
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	2,a,9,b,3,4,c
2	6,a,b,c

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4	7	8
Type	FYA 4 - Section	Normal	FYA 4 - Section	Normal	Normal	Normal
Included Phases	2	4,6,9	6	2,3,9	4,9	3,9
Modifier Phases	-	-	-	-	-	-
Modifier Overlaps	7	-	8	-	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7	X	-	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	-	X	X	3
4	Phase Vehicle	4	-	X	-	4
5	Phase Vehicle	5	-	X	-	5
6	Phase Vehicle	6	X	-	X	6
7	Overlap	8	X	-	-	7
8	Phase Vehicle	8	-	X	X	8
9	Overlap	1	X	-	X	9
10	Overlap	2	-	X	X	10
11	Overlap	3	X	-	-	11
12	Overlap	4	-	X	-	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	-	X	X	17
18	Overlap	6	-	X	-	18

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 1. →

NOTICE OVERLAP 8 ASSIGNED TO CHANNEL 7. →

← NOTICE CHANNEL 1 YELLOW FLASH

← NOTICE CHANNEL 7 YELLOW FLASH

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-1100T1
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:



Signal Upgrade - Temporary Design 1
(TMP Phase I Step 1) Electrical Detail - Sheet 2 of 2

<p>Electrical and Programming Details For:</p> <p>Prepared for the Offices of:</p> <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.)/ SR 1965 (Belevs Creek Rd.)</p> <p>Division 9 Forsyth County Walkertown</p> <p>PLAN DATE: February 2024 REVIEWED BY: DT Sears</p> <p>PREPARED BY: WP Erickson-Jones REVIEWED BY:</p> <table border="1"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table> <p>DocuSigned by: Porter Jones 2/12/2024</p>	REVISIONS	INIT.	DATE				<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p>SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 056142 PORTER JONES</p> <p>SIG. INVENTORY NO. 09-1100T1</p>
REVISIONS	INIT.	DATE						