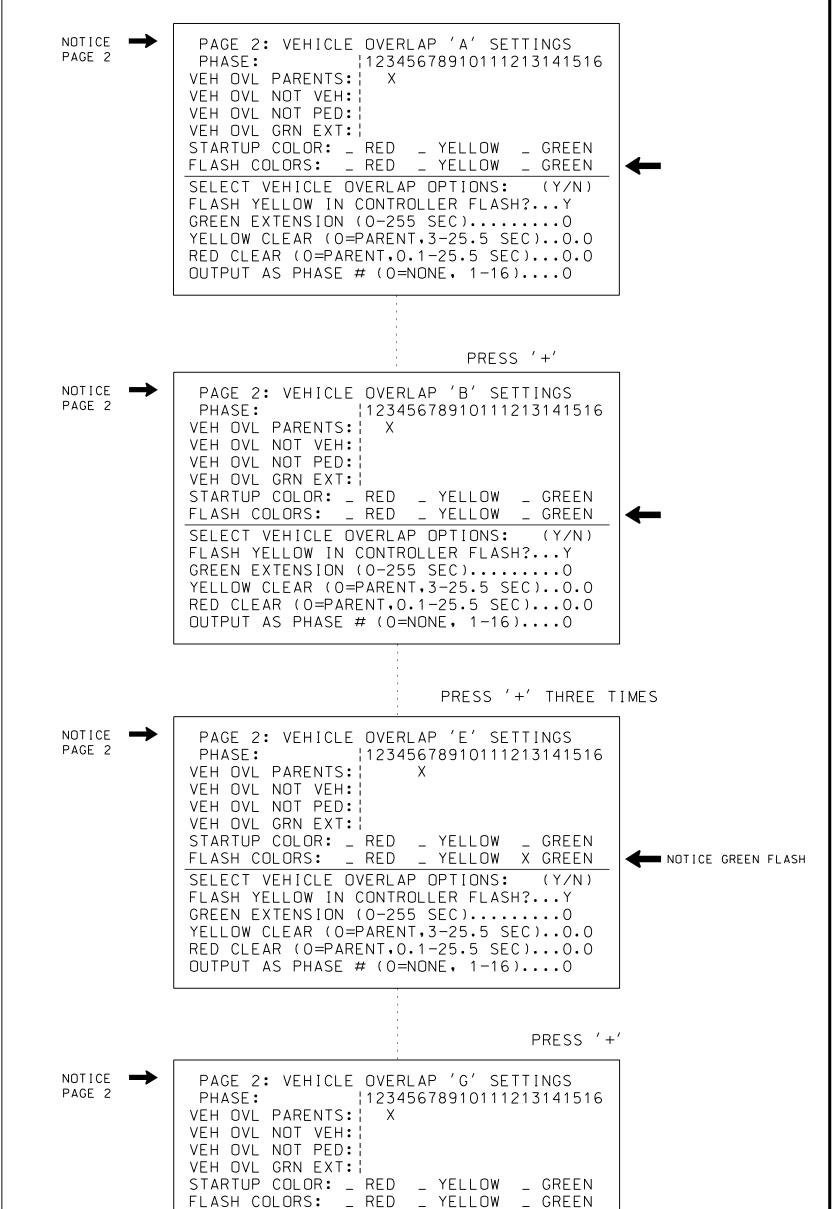
R-2577A Sig. 3.2

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0

RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0

OVERLAP PROGRAMMING COMPLETE

US 158 EB (Reidsville Rd.)

Old Greensboro Rd.

FLASH YELLOW IN CONTROLLER FLASH?...N

GREEN EXTENSION (0-255 SEC).....

OUTPUT AS PHASE # (0=NONE, 1-16)....0

New Installation - Electrical Detail - Sheet 2 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:



Winston-Salem PLAN DATE: February 2024 Reviewed By: DT Sears PREPARED BY: WP Erickson-Jones REVIEWED BY: REVISIONS INIT. DATE

SIGNATURES COMPLETED FESSION 056142 Porter Jones

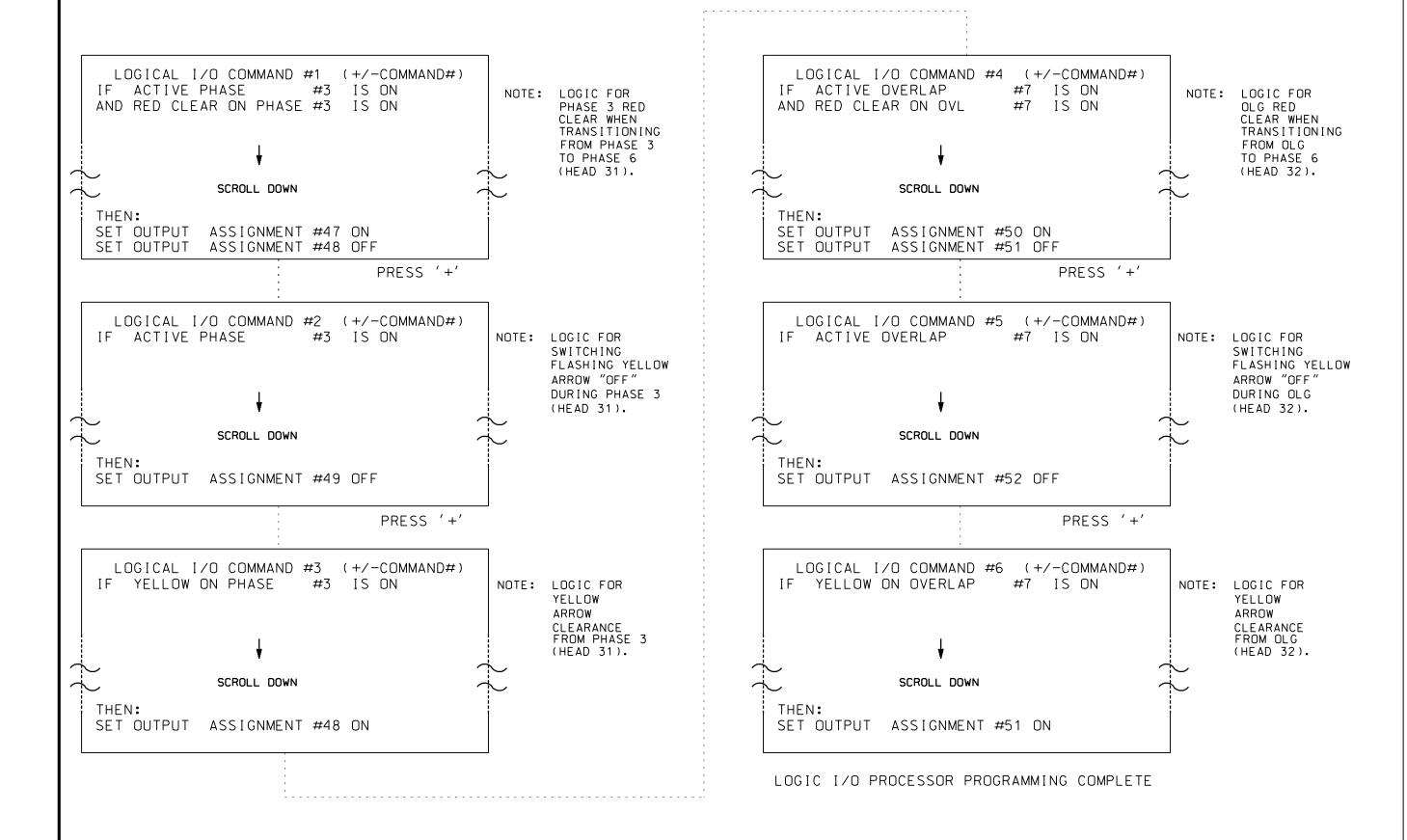
SIG. INVENTORY NO. 09-0981

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1,2,3,4,5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

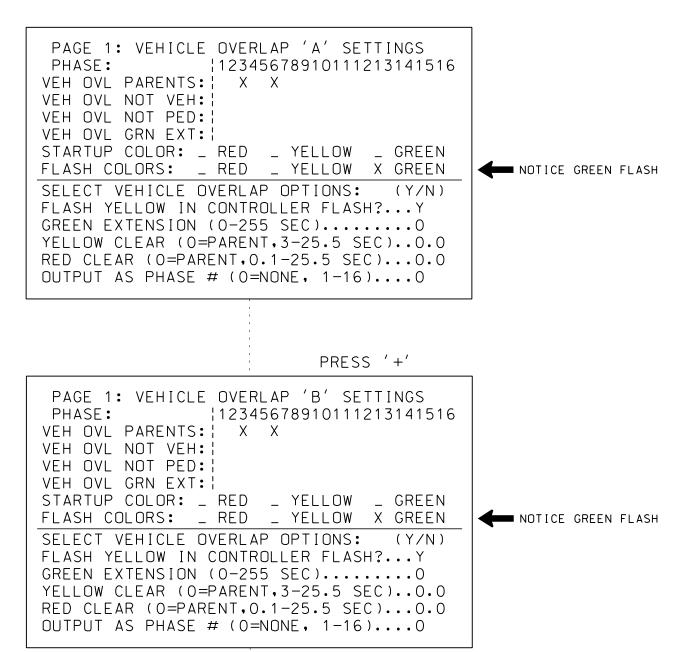


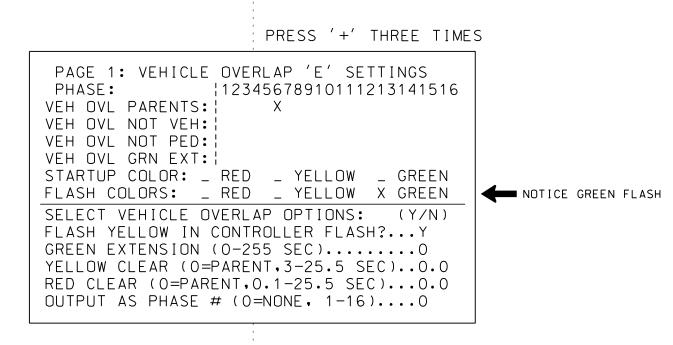
OUTPUT REFERENCE SCHEDULE OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).





PRESS '+' PAGE 1: VEHICLE OVERLAP 'G' SETTINGS ¦12345678910111213141516 VEH OVL PARENTS: | X VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0981 DESIGNED: February 2024 SEALED: February 12, 2024 REVISED:

RKK

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