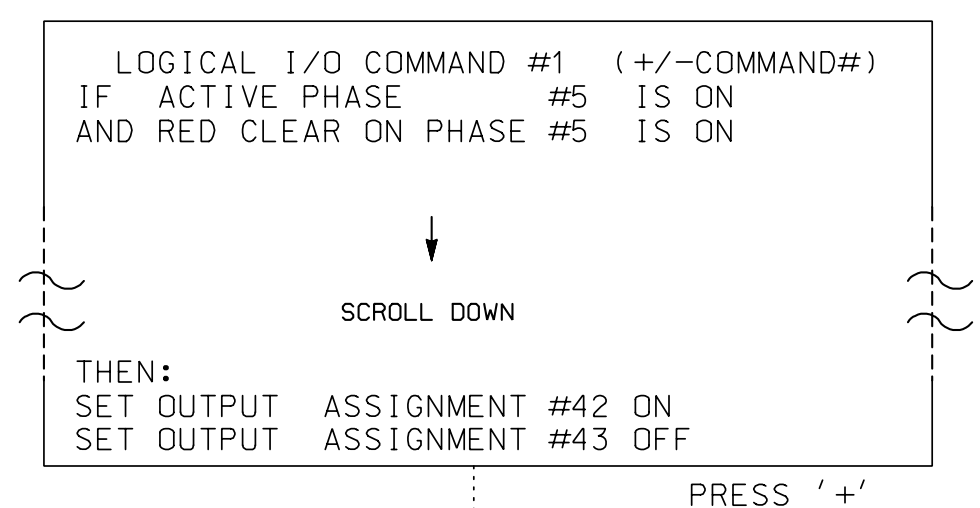


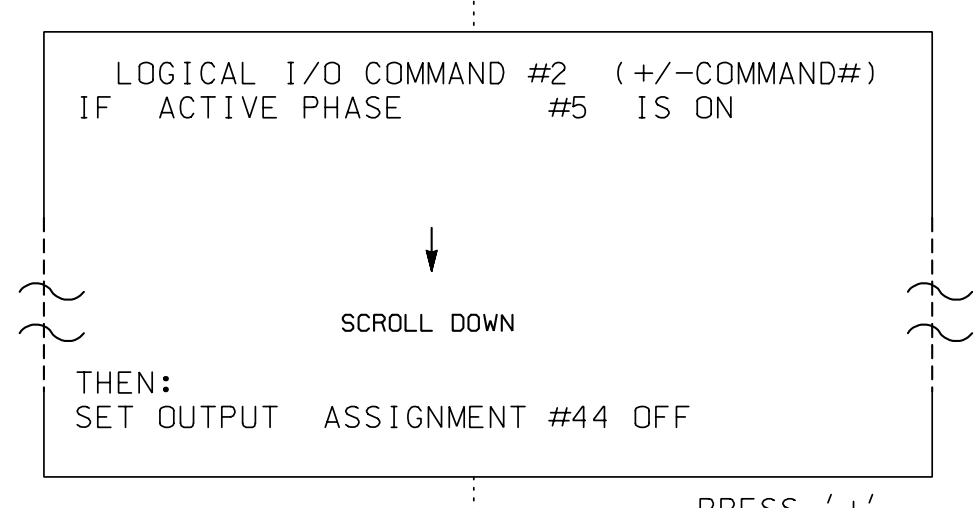
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

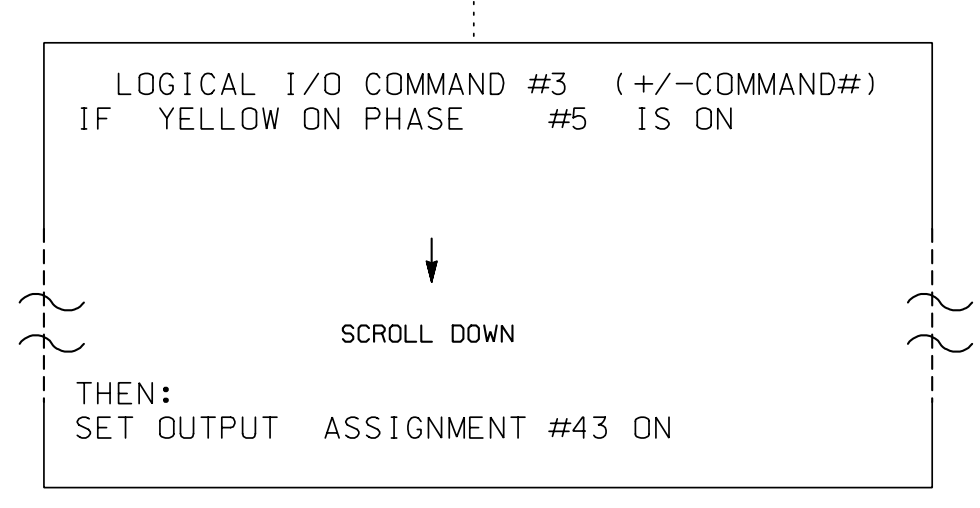
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' THREE TIMES

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: | XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```


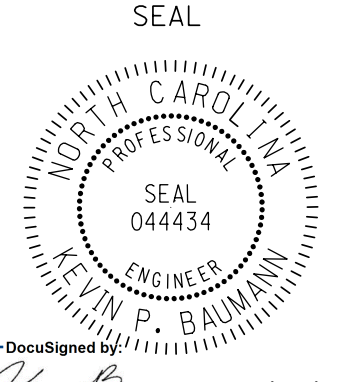
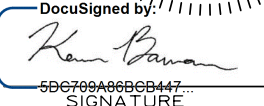
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: DAVI-1T1
 DESIGNED: July 2023
 SEALED: 9/19/2023
 REVISED: N/A

Electrical Detail - Temporary Design 1
 Sheet 2 of 2

DOCUMENT NOT CONSIDERED
 FINAL UNLESS ALL
 SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared For: 	Griffith Street at Sloan Street/Beaty Street		
	Division 10 Mecklenburg Davidson	PLAN DATE: July 2023 REVIEWED BY: KP Baumann	
PREPARED BY: SP Pennington	REVIEWED BY:	REVISIONS	INIT. DATE
DocuSigned by: 		9/19/2023	DATE
SIG. INVENTORY NO. DAVI-1T1			

PLANS PREPARED IN THE OFFICE OF:
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9/19/2023 9:16:48 AM susan.pennington K:\RAL\PTD\SIGNALS\01036360_U-5907_Ports\Sloan EX1MS4 - Signal Design\2.1 DAVI-1_2023e-T1.dgn