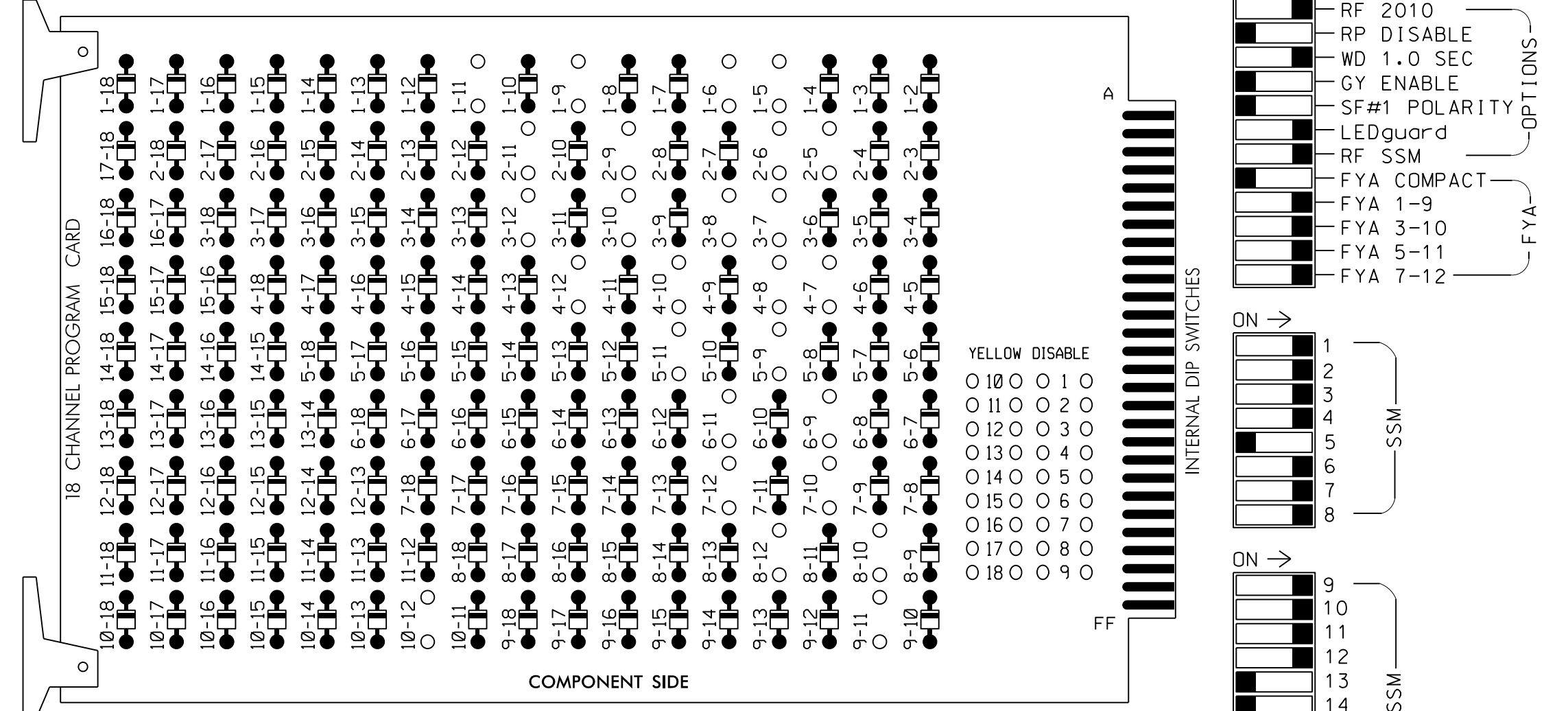


### EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11, and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green and 6 Green.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

### EQUIPMENT INFORMATION

CONTROLLER.....2070LX  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,  
 AUX S1,AUX S2,AUX S4,AUX S5  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP "A".....\*  
 OVERLAP "B".....\*  
 OVERLAP "C".....\*  
 OVERLAP "D".....\*  
 \* See overlap programming detail on sheet 2

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11★	82	21,22 23	NU	22	★	41,42 43	NU	★	61,62 63	NU	62	★	81,82 83	NU	★	31★	51★	71★
RED	*	128		*	101		134	*	107										
YELLOW		129			102		* 135		108										
GREEN		130			103		136		109										
RED ARROW																			
YELLOW ARROW	126			117					123										
FLASHING YELLOW ARROW																			
GREEN ARROW	127	127		118	118		133		124	124									

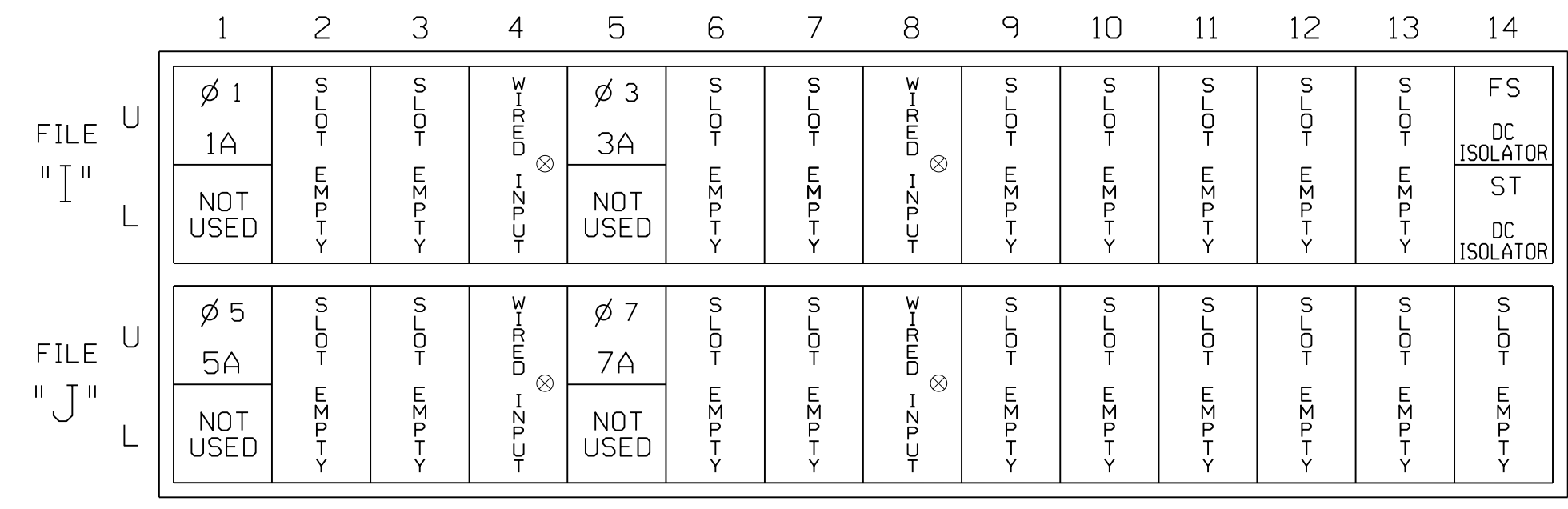
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

### INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

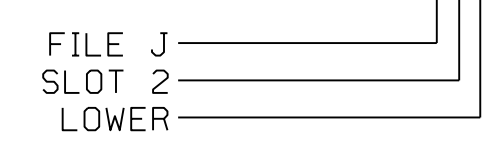
⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A <sup>1</sup>	TB2-1,2	I1U	56	1★	1	YES		15		S
	-	J4U	48	26★	6	YES		3		G
3A <sup>2</sup>	TB4-5,6	I5U	58	3★	3	YES		15		S
	-	J8U	50	28★	8	YES		3		G
5A <sup>3</sup>	TB3-1,2	J1U	55	5★	5	YES		15		S
	-	I4U	47	22★	2	YES		3		G
7A <sup>4</sup>	TB5-5,6	J5U	57	7★	7	YES		15		S
	-	I8U	49	24★	4	YES		3		G

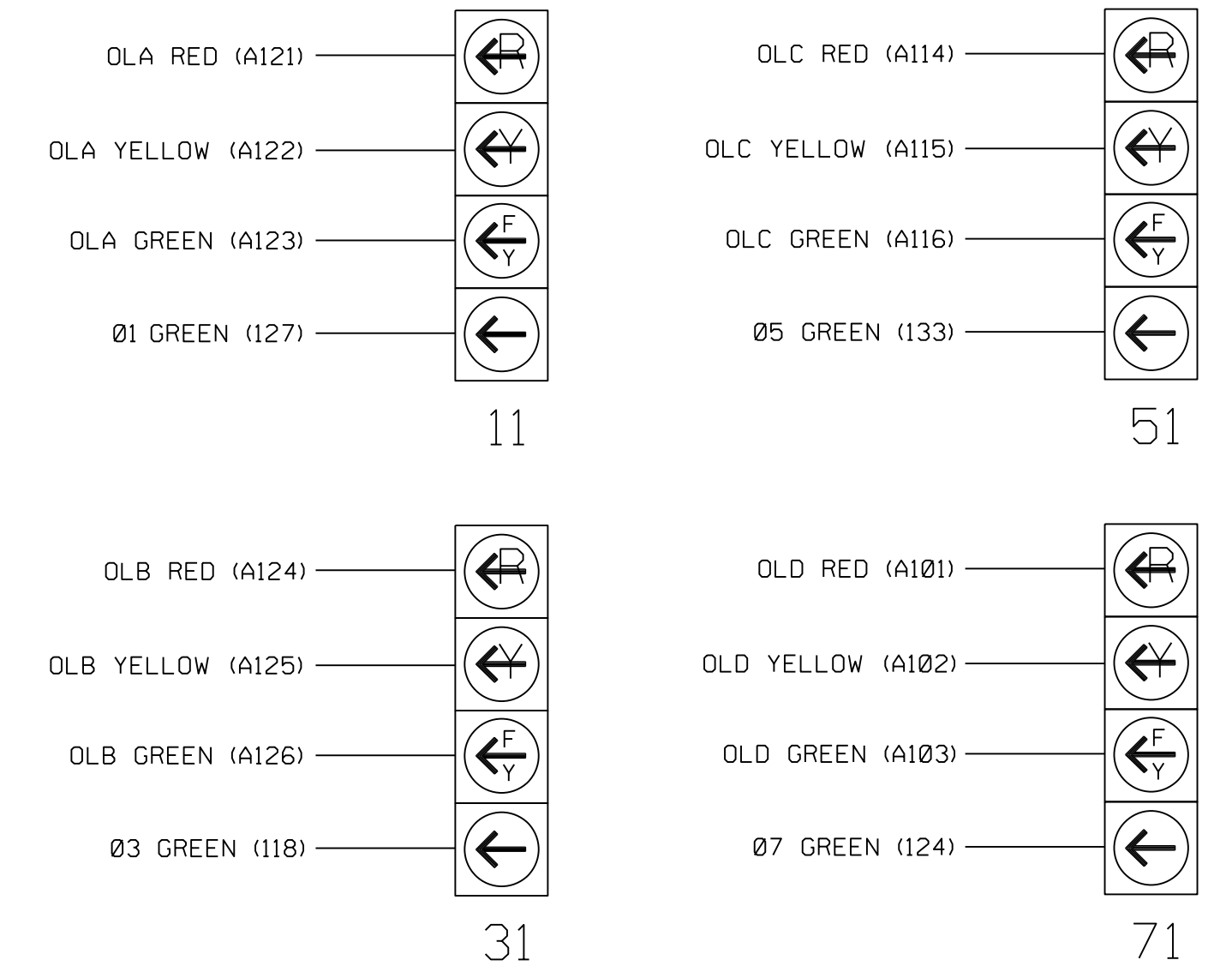
- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

#### INPUT FILE POSITION LEGEND: J2L



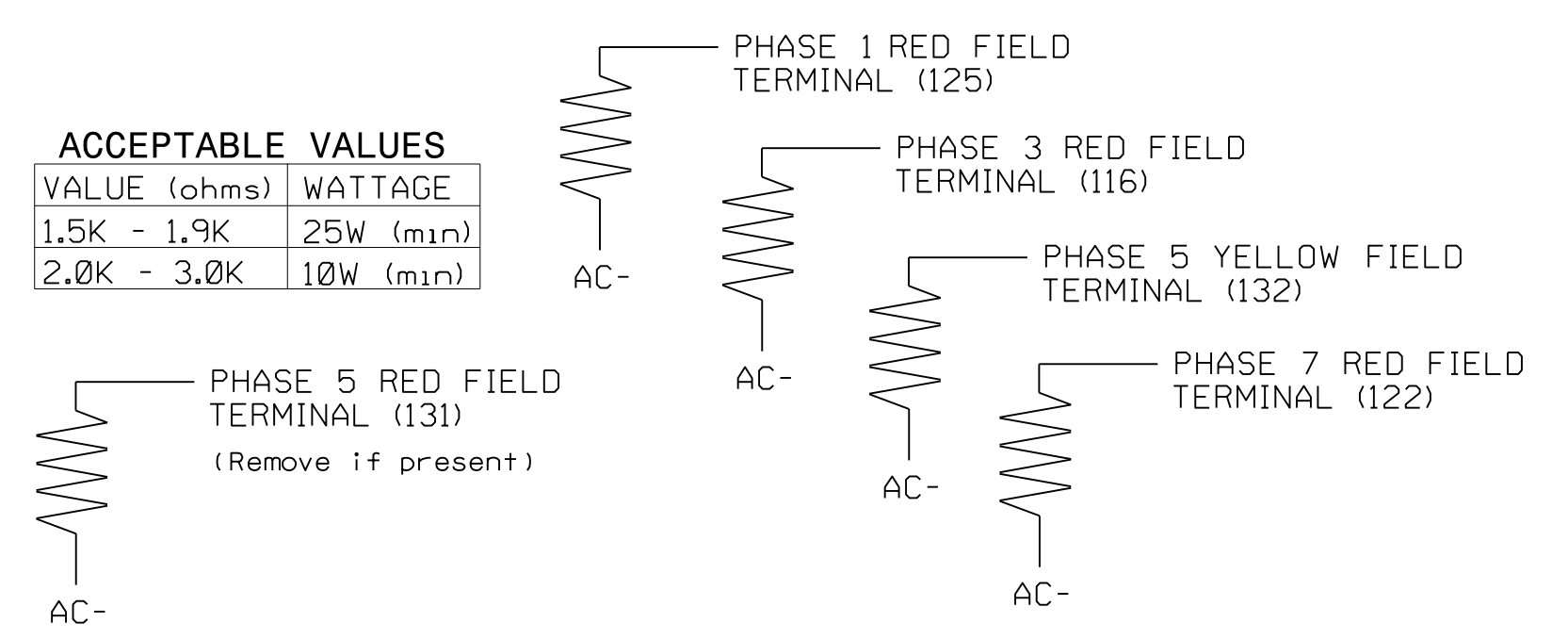
### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



### SPECIAL DETECTOR NOTE

Install a temporary video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zones 1A, 3A, 5A, and 7A, the equipment placement and slots reserved for wired inputs are typical for a NCDOT installation.

### Temporary Design 3 - TMP Phase II Electrical Detail - Sheet 1 of 2

US 21 (Charlotte Highway) at SR 1100 (Brawley School Road) / SR 1117 (Wilson Avenue)

Division 12 Iredell County Mooresville

PLAN DATE: May 2022 REVIEWED BY: E D Harris

PREPARED BY: D A Waller REVIEWED BY: R M Muncey

REVISIONS	INIT.	DATE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-1369T3  
 DESIGNED: MAY 2022  
 SEALED: 3/24/2023  
 REVISED: N/A

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Seal of Derrick Waller, Professional Engineer, License No. 042678, dated 3/24/2023.