(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS
- 3. Press "Toggle" until positioned on Overlap G

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

TMG VEH OVLP...[G] TYPE: NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 Press Toggle until positioned over Overlap B

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP...[B] TYPE: PPLT FYA PROTECTED LEFT TURN.... OVERLAP G OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT....CH10 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

Toggle Twice

Select TMG VEH OVLP [D] and 'NORMAL'

TMG VEH OVLP...[D] TYPE:NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 Toggle Once

OVERLAP D

OVERLAP E

Select TMG VEH OVLP [E] and 'NORMAL'

TMG VEH OVLP...[E] TYPE:NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 Toggle Once

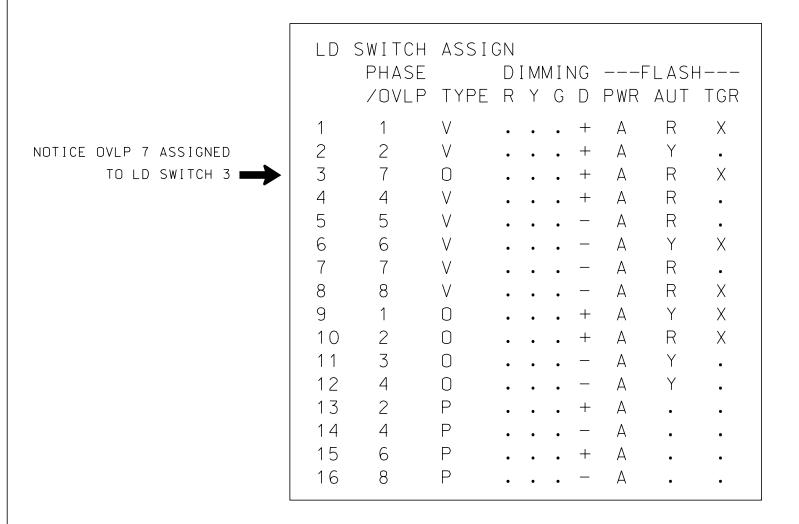
ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S4 as OLG, program LD SWITCH 3 as OVLP '7' TYPE '0' as shown below.

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.
- THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-0885T3 DESIGNED: February 2023 SEALED: February 21, 2023 REVISED: N/A

This plan supersedes the signal plan signed and sealed by Steven G. Haynie, PE on 05/31/2022.

8521 SIX FORKS ROAD, SUITE 400

PROJECT REFERENCE NO. | SHEET NO Sig. 4.2 C-5621

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING FOR FYA SUPPRESSION DURING THE DELAYED GREEN PERIOD

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F) PED ON PH WALK AND VEH GREEN ON PH IS OFF THEN SIG SET OLP RED SIG SET OLP YELLOW SIG SET OVLP GREEN ELSE

LOGIC STATEMENT FOR ADVANCE WALK WITH FYA'S. TURN FYA HEAD 63 OFF DURING PED 6 ADVANCE WALK.

END PROGRAMMING

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5

END PROGRAMMING

Temporary Signal 3 - TCP Phase 3A Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



US 21 (Catawba Avenue)/ Catawba Avenue at US 21 (Statesville Road)/

Holiday Lane
Division 10 Mecklenburg County Cornelius PLAN DATE: March 2023 Reviewed By: V. Kaiser

PREPARED BY: S.G. Haynie REVIEWED BY: REVISIONS INIT. DATE

029531 Steven G. Haynie3/8/2023 — D633DCC9A5D44B6_F Sig. Inventory No. 10-0885T3

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

OVERLAP F

TMG VEH OVLP...[F] TYPE: OTHER/ECONOLITE PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

Select TMG VEH OVLP [F] and 'OTHER/ECONOLITE'

END PROGRAMMING