

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**
- Press "Toggle" until positioned on Overlap G

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

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TMG VEH OVLP...[G] TYPE: ....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED. . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
  
```

Press Toggle until positioned over Overlap A

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2
FLASHING ARROW OUTPUT....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
  
```

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6
FLASHING ARROW OUTPUT....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
  
```

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```

TMG VEH OVLP...[D] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... OVERLAP G
OPPOSING THROUGH..... PHASE 2
FLASHING ARROW OUTPUT....CH12 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
  
```

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-088511
 DESIGNED: February 2023
 SEALED: February 21, 2023
 REVISED: N/A

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING FOR FYA SUPPRESSION DURING THE DELAYED GREEN PERIOD

(program controller as shown)

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From LOGIC PROCESSOR Submenu select **2. LOGIC STATEMENTS**

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
IF PED ON PH WALK 2 IS ON
AND VEH GREEN ON PH 2 IS OFF
THEN SIG SET OLP RED 1 ON
SIG SET OLP YELLOW 1 OFF
SIG SET OVLP GREEN 1 OFF
ELSE
  
```

← LOGIC STATEMENT FOR ADVANCE WALK WITH FYA'S. TURN FYA HEAD 11 OFF DURING PED 2 ADVANCE WALK.

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 2 COPY FROM: 1 ACTIVE: M (T/F)
IF PED ON PH WALK 2 IS ON
AND VEH GREEN ON PH 2 IS OFF
THEN SIG SET OLP RED 4 ON
SIG SET OLP YELLOW 4 OFF
SIG SET OVLP GREEN 4 OFF
ELSE
  
```

← LOGIC STATEMENT FOR ADVANCE WALK WITH FYA'S. TURN FYA HEAD 23 OFF DURING PED 2 ADVANCE WALK.

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 3 COPY FROM: 1 ACTIVE: M (T/F)
IF PED ON PH WALK 6 IS ON
AND VEH GREEN ON PH 6 IS OFF
THEN SIG SET OLP RED 3 ON
SIG SET OLP YELLOW 3 OFF
SIG SET OVLP GREEN 3 OFF
ELSE
  
```

← LOGIC STATEMENT FOR ADVANCE WALK WITH FYA'S. TURN FYA HEAD 51 OFF DURING PED 6 ADVANCE WALK.

END PROGRAMMING

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From LOGIC PROCESSOR Submenu select **1. LOGIC STATEMENT CONTROL**

ENABLE LOGIC PROCESSOR STATEMENTS 1, 2 & 3 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5
LP 1-15	E	E	E
LP 16-30
LP 31-45
LP 46-60
LP 61-75
LP 76-90

END PROGRAMMING

ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S10 as OLG, program LD SWITCH 7 as OVLP '7' TYPE '0' as shown below.

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **3. LOAD SW ASSIGN**

LD SWITCH ASSIGN									
PHASE	DIMMING	---	FLASH	---					
/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR	
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	Y	.
3	3	V	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	Y	X
7	7	O	.	.	.	-	A	R	.
8	8	V	.	.	.	-	A	R	X
9	1	O	.	.	.	+	A	Y	X
10	2	O	.	.	.	+	A	R	X
11	3	O	.	.	.	-	A	Y	.
12	4	O	.	.	.	-	A	Y	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	-	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	-	A	.	.

NOTICE OVLP 7 ASSIGNED TO LD SWITCH 7

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

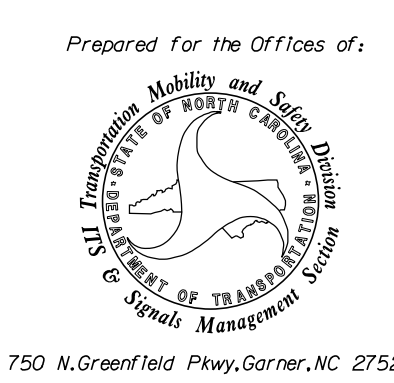
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

This plan supersedes the electrical plan signed and sealed by Steven G. Haynie, PE on 05/31/2022.

Temporary Signal 1
Electrical Detail Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

23-Feb-23 2:02:52 PM
R:\Projects\10-088511\Signal Design\10-088511_Sm.dwg
20-52-55



US 21 (Catawba Avenue) /
Catawba Avenue at
US 21 (Statesville Road) /
Holiday Lane
Division 10 Mecklenburg County Cornelius

PLAN DATE: February 2023 REVIEWED BY: V. Kaiser
 PREPARED BY: S.G. Haynie REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL

STEVEN G. HAYNIE
 PROFESSIONAL ENGINEER
 STATE OF NORTH CAROLINA
 LICENSE NO. 029531

DocuSign
 Steven G. Haynie 2/21/2023
 SIGNATURE DATE
 Sig. Inventory No. 10-088511