

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 32 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

ASSIGN CHANNEL 1 TO OVERLAP 7 →

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7		X	X	1
2	Phase Vehicle	2	X			2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6	X		X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1	X		X	9
10	Overlap	2	X		X	10
11	Overlap	3	X			11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5	X		X	17
18	Overlap	6		X		18

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
3A	7	3
		-

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	3	3	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	-	6	3
Modifier Phases	3	3	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

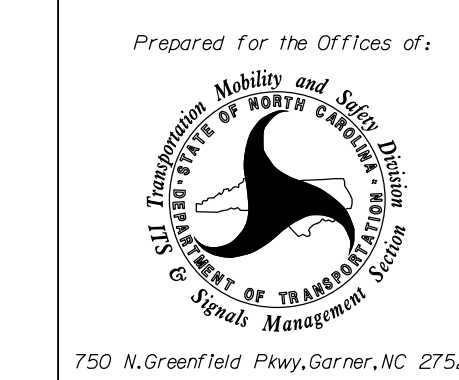
← NOTICE INCLUDED PHASE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1332
DESIGNED: May 2023
SEALED: 5/24/2023
REVISED:



Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:



US 421-NC 16 at SR 1323 (Dancy Road)

Division 11 Wilkes County Wilkesboro

PLAN DATE: May 2023 REVIEWED BY: J. Ma

PREPARED BY: M.L. Stygles REVIEWED BY: S.R. Chiluka

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

STATE OF NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 046057 MATTHEW L. STYGLES

DocuSigned by: *Matthew L. Stygles* 5/24/2023

SIGNATURE DATE

SIG. INVENTORY NO. 11-1332

5/23/2019 3:15:01 PM ***VHB-IMP ***PCJ ***161 gpm38621.03 NDDOT U-5312 Wilkes County Traffic Signal Design Plans#U5312_11-1332_Sig_ele_046057.ppt.dwg sch:tlukc