### ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

(program controller as shown)

# I MPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu select 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR |
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... . PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - Place cursor in VEH DETECTOR [ ] position and enter "5". - Set delay time to "3".

VEH DETECTOR [ 5] VEH DET PLAN [ 2] TYPE: N-NTCIP TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... 0.0 DELAY TIME... 3.0 

ENSURE DELAY

IS SET TO '3' USE ADDED INITIAL . CROSS SWITCH PH.. 0 LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [ ] position and enter "22". - Set assigned phase to "0".

VEH DETECTOR [22] VEH DET PLAN [2] TYPE: N-NTCIP TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE
IS SET TO "O" EXTEND TIME... 0.0 DELAY TIME... 3.0 

ENSURE DELAY USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

END PROGRAMMING

#### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle To "Overlap G"

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

TMG VEH OVLP...[G] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 Toggle To "Overlap A"

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

OVERLAP A

TMG VEH OVLP...[A] TYPE: OTHER/ECONOLITE PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0 Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .....PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH.... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... OVERLAP G OPPOSING THROUGH..... PHASE 4 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø6-Ø193 DESIGNED: JULY 2022 SEALED: FEBRUARY 2023 REVISED: N/A

## LOAD SWITCH ASSIGNMENT DETAIL (program controller as shown)

ECONOLITE ASC/3-2070

To assign load switch S10 as OLG, program LD SWITCH 7 as OVLP '7' TYPE '0' as shown below.

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN DIMMING ---FLASH---/OVLP TYPE R Y G D PWR AUT TGR NOTICE OVERLAP G 16 3 P ... - A . .

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

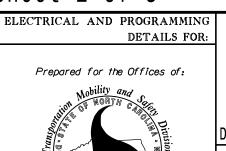
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Final Design Electrical Detail - Sheet 2 of 3



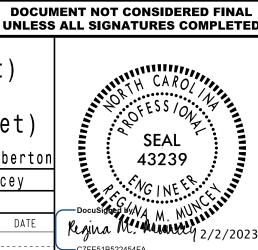
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NC 41-72 (West 2nd Street) at SR 1536 ( North Water Street)

Division 6 Robeson County Lumberton PLAN DATE: FEBRUARY 2023 REVIEWED BY: R M Muncey PREPARED BY: D. Waller REVIEWED BY: REVISIONS



SIG. INVENTORY NO.