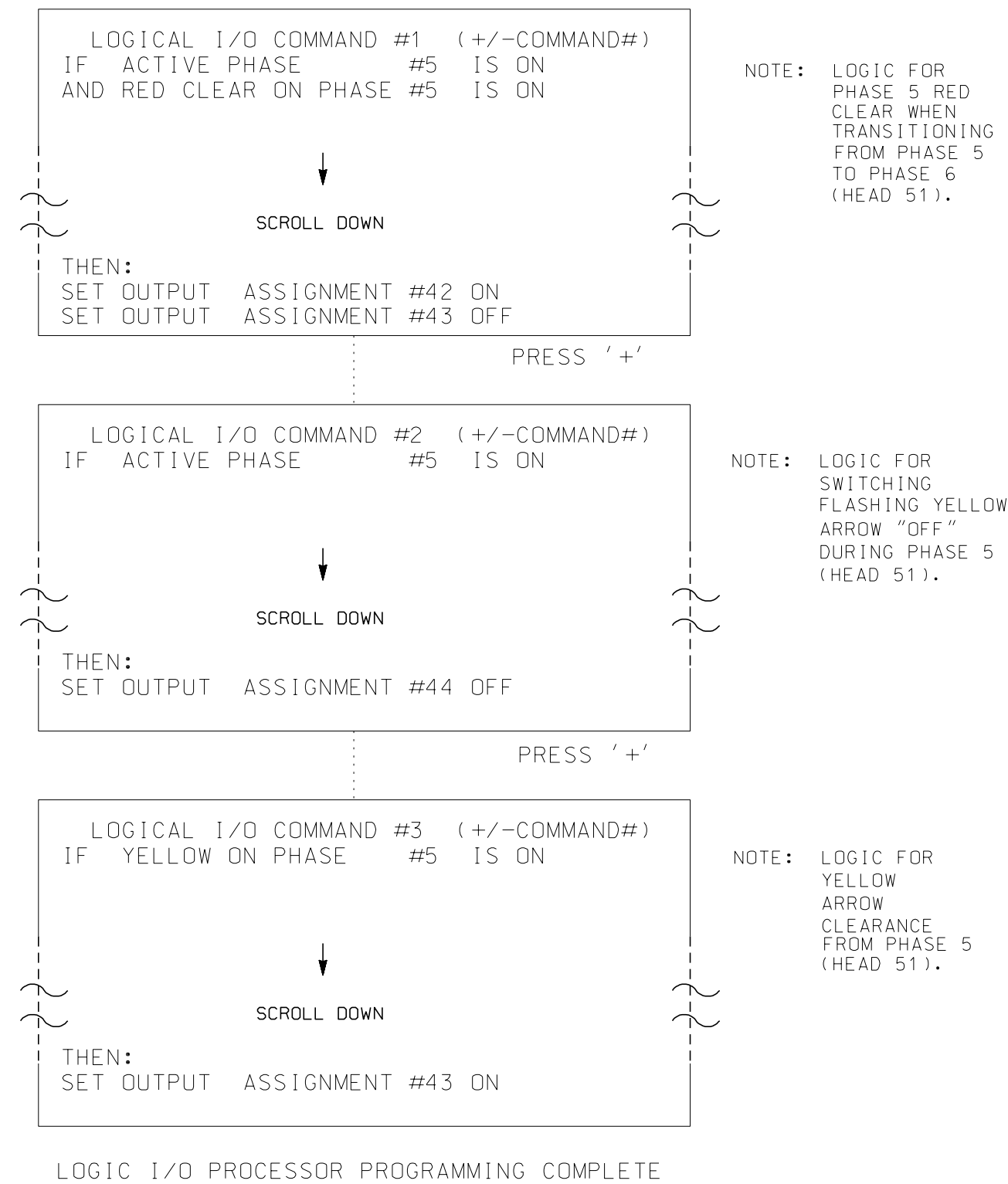


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



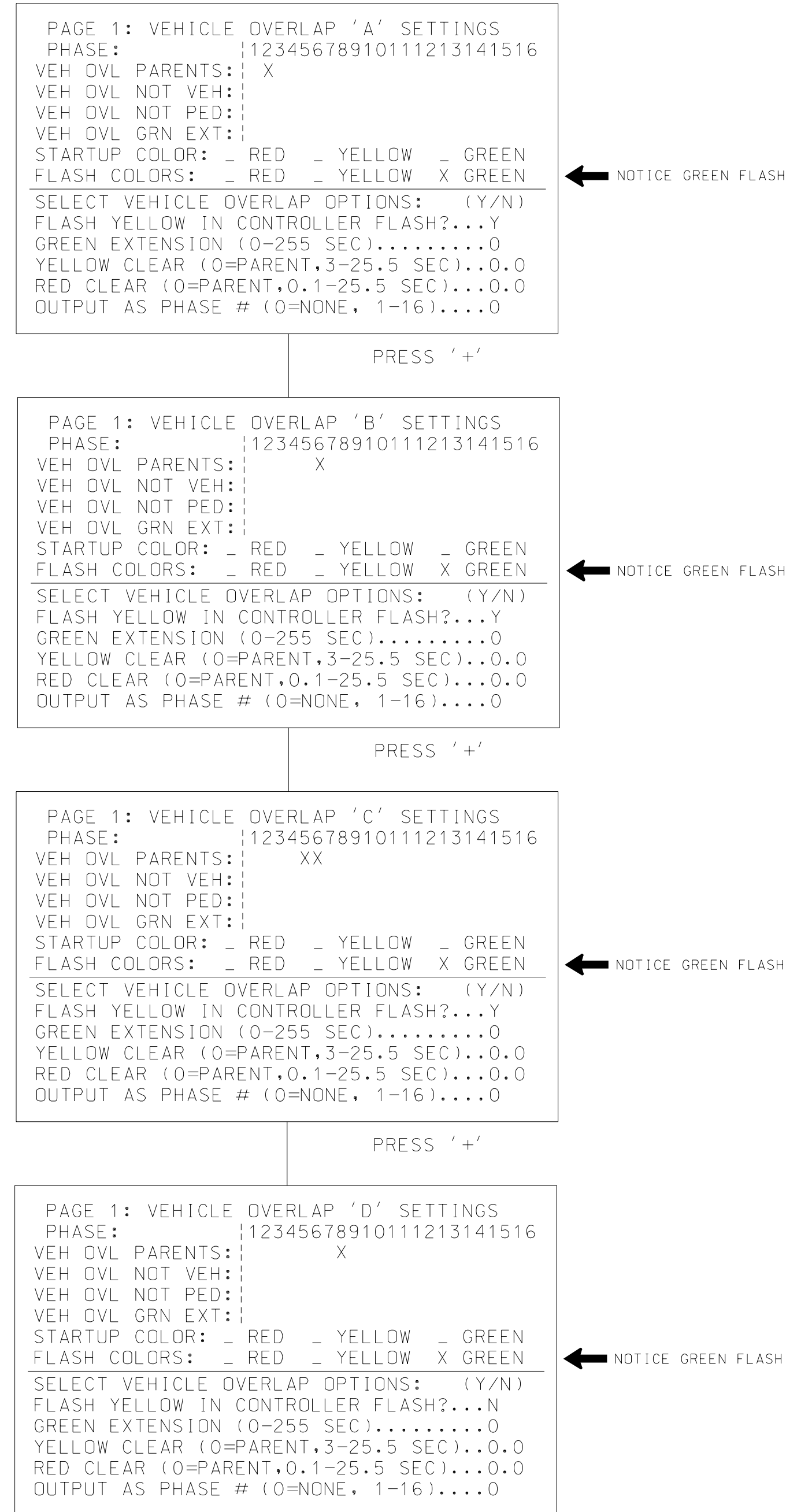
OUTPUT REFERENCE SCHEDULE

OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

**OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

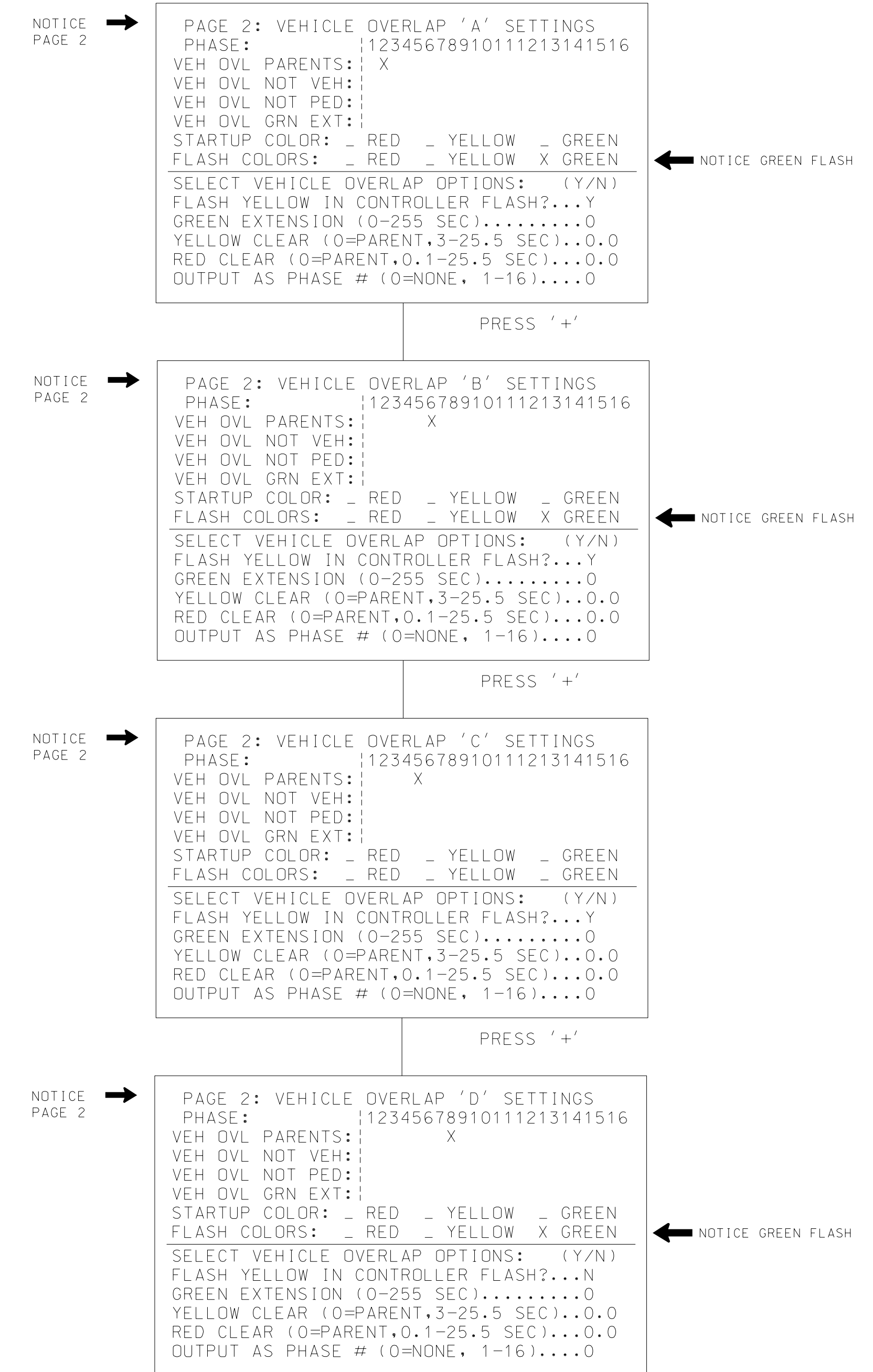


THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1105
DESIGNED: March 2023
SEALED: April 25, 2023
REVISED:

**OVERLAP PROGRAMMING DETAIL
FOR ALTERNATE PHASING**

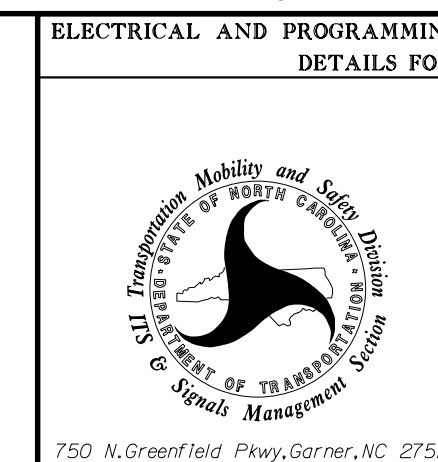
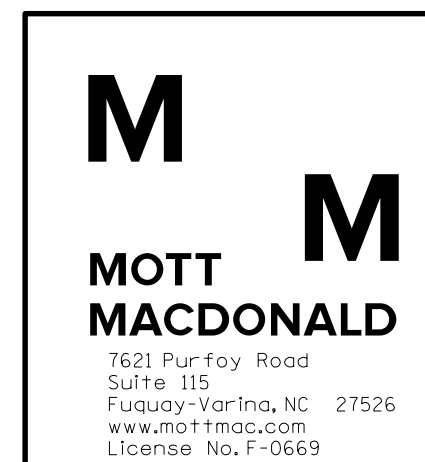
(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



4/25/2023 0:43:03.50.DOC: 12*MC_U-2729*Traffic*0451gnals*09-1105*260_320_091105-20230425e2.dgn User: STDB627

Electrical Detail - Final Design - Sheet 2 of 4

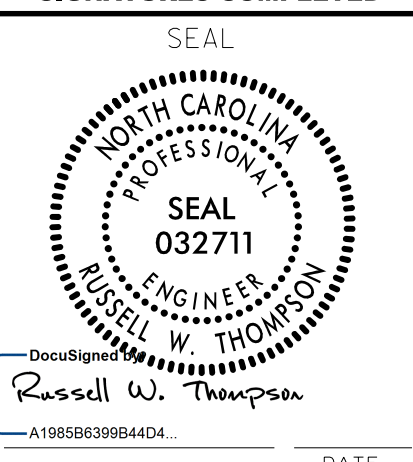


SR 1672 (Hanes Mill Rd) at US 52 NB Ramps

Division 9 Forsyth County Winston-Salem

PLAN DATE: March 2023	REVIEWED BY: RW Thompson
PREPARED BY: LD Stouchko	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



SIG. INVENTORY NO. 09-1105