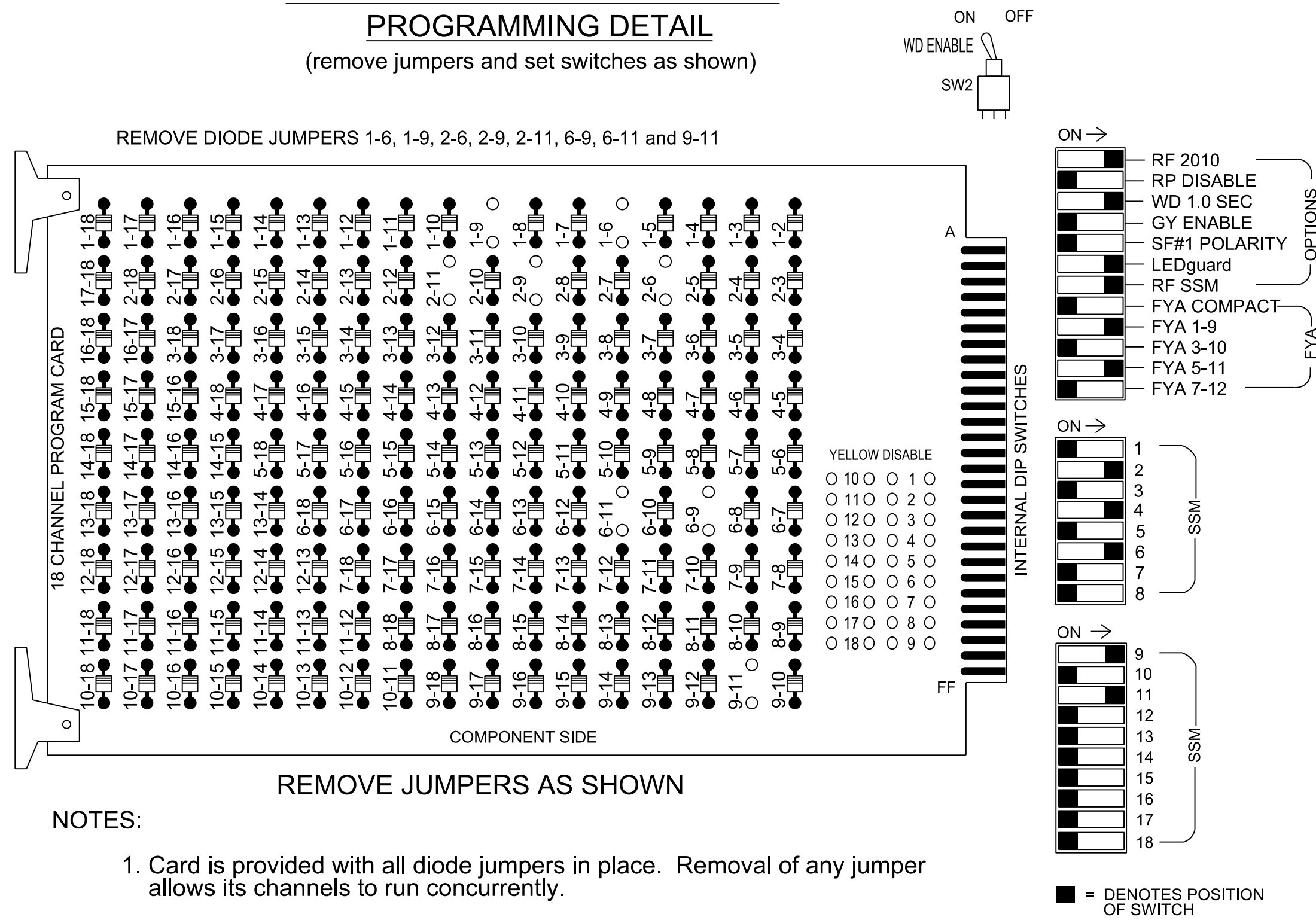


18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
4. The cabinet and controller are part of NC 87/ SR 2817 (Barnes Street) Closed Loop System. Signal System #: D07-10 Reidsville.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....BASE
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1, S2, S5, S8, AUX S1, AUX S4
Phases Used.....1, 2, 4, 6
Overlap "1".....*
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....NOT USED

*See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

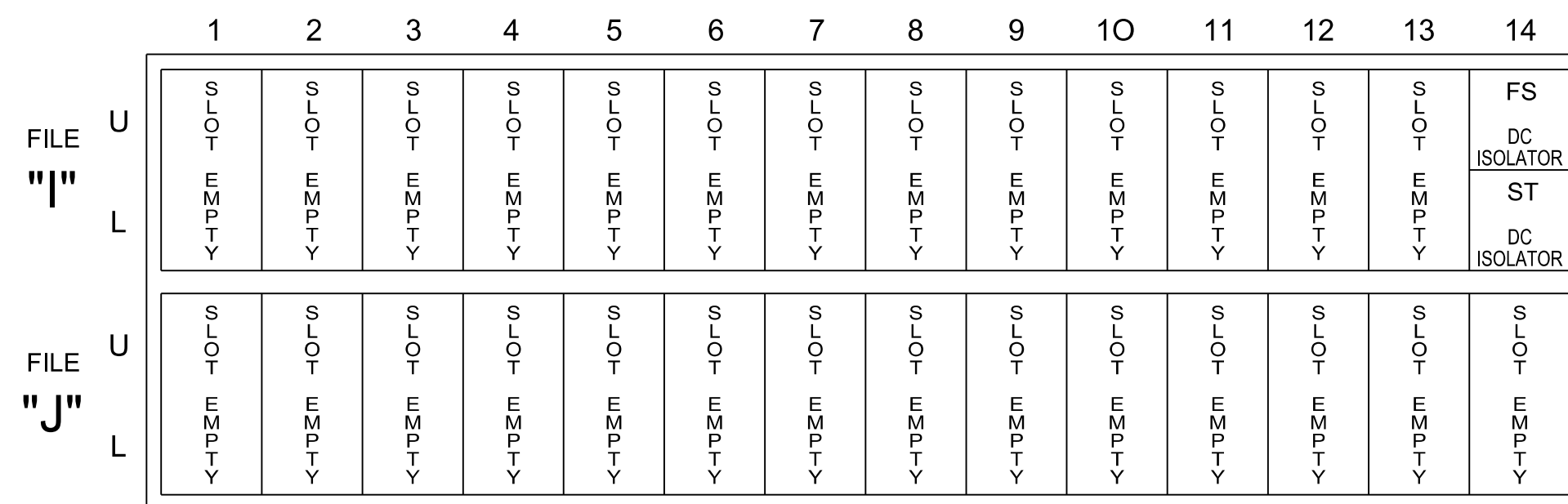
Table with columns for LOAD SWITCH NO., S1-S10, AUX S1-S6, PHASE, SIGNAL HEAD NO., and signal colors (RED, YELLOW, GREEN, RED ARROW, YELLOW ARROW, FLASHING YELLOW ARROW, GREEN ARROW).

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
* See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

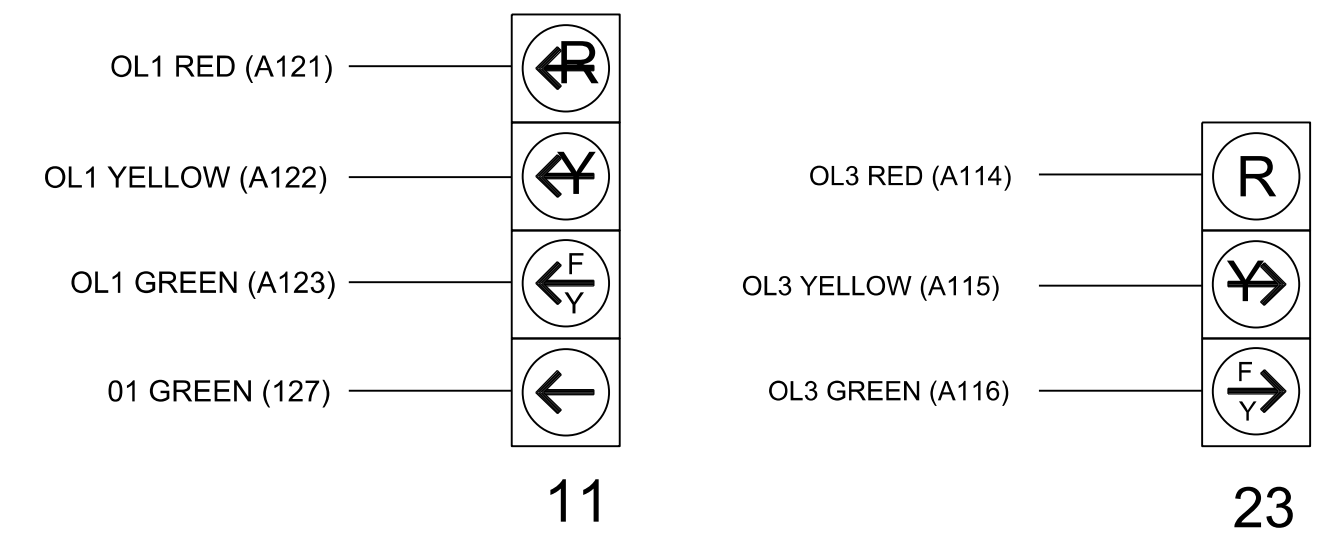
FS = FLASH SENSE
ST = STOP TIME

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

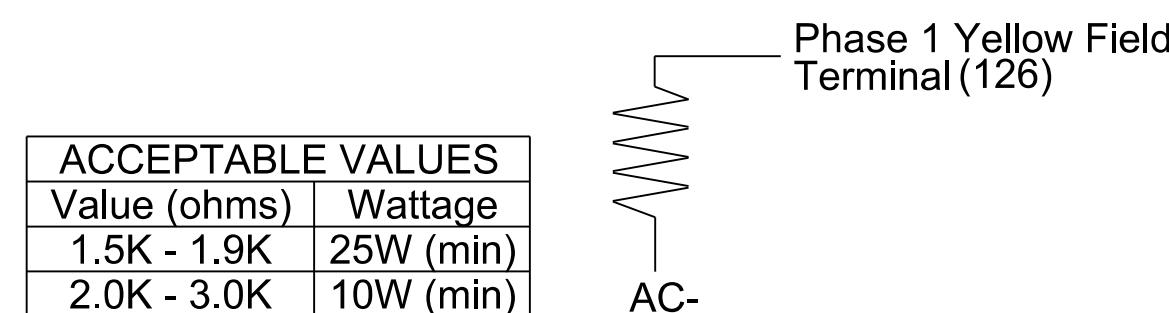


Table with columns for Value (ohms) and Wattage. Values: 1.5K - 1.9K (25W min), 2.0K - 3.0K (10W min).

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

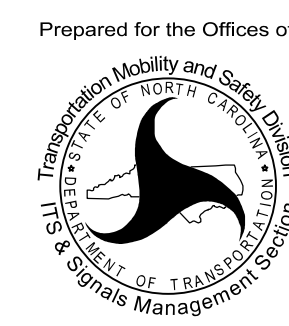
Overlap Plan 1

Table with columns for Overlap (1, 2, 3, 4) and rows for Type, Included Phases, Modifier Phases, Trail Green, Trail Yellow, Trail Red.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1665T3
DESIGNED: Jan 2023
SEALED: 3/10/2023
REVISED:

Temporary Design 3 (TMP Phase III)
Electrical Detail - Sheet

Electrical and Programming Details For:



750 N. Greenfield Pkwy, Garner, NC 27529

SR 2817 (Barnes Street) at US 29 SB Ramps

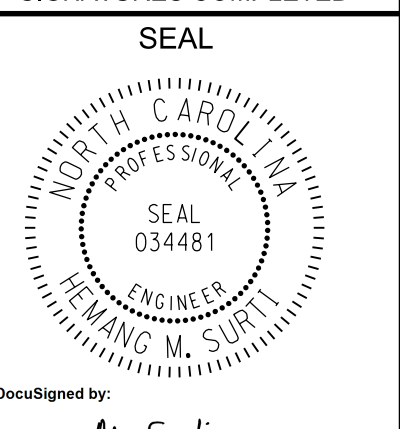
Division 7 Rockingham County Reidsville

PLAN DATE: January 2023 REVIEWED BY: H M Surti

PREPARED BY: A Raviapati REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



DocuSigned by: Hemang M. Surti 3/10/2023

SIG. INVENTORY NO. 07-1665T3