ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING <u>COORDINATION</u> - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

PHASING

ACTIVE PAGES REQUIRED TO RUN DEFAULT ACTIVE PAGES REQUIRED TO RUN ALTERNA

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE	PHASIN
THE FOLLOWING IS / THESE OVERLAP/INPO "ALTERNATE PHASING	JT PAGE
OVERLAPS PAGE 2:	Modifie for hec turns c
INPUTS PAGE 2:	Disable and red call on
	Disable and rec call or

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	INPUTS PAGE	OVERLAPS PAGE
<u> </u>	1	1
ATE PHASING	2	2

NG PAGE CHANGE SUMMARY

ARY OF WHAT TAKES PLACE WHEN CHANGES ACTIVATE TO CALL THE

es overlap parent phases ads 11 and 51 to run protected only.

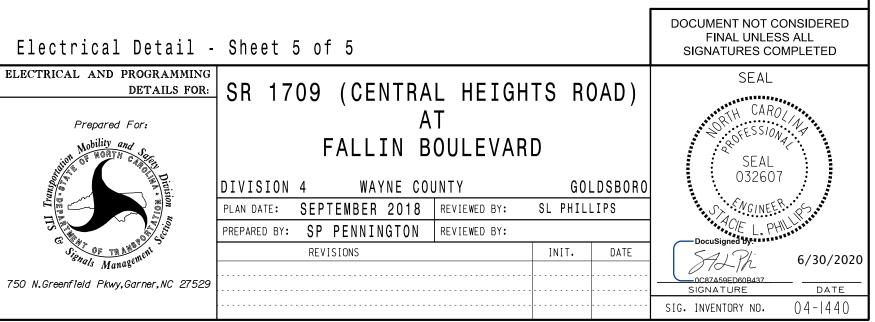
es phase 6 call on loop 1A duces delay time for phase 1 loop 1A to 3 seconds.

es phase 2 call on loop 5A duces delay time for phase 5 loop 5A to 3 seconds.

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.





PROJECT REFERENCE NO.	SHEET NO.
U - 5724	SIG. 2.5

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø4-144Ø DESIGNED: SEPTEMBER 2018 SEALED: 6/30/20 REVISED: N/A