ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select [8. UTILITIES]
2. From UTILITIES Submenu select [1. COPY/CLEAR]
3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

```
COPY / CLEAR UTILITY
      FROM                TO
PHASE TIMING.... . > PHASE TIMING.... .
TIMING PLAN..... . > TIMING PLAN..... .
PH DET OPT PLAN. . > PH DET OPT PLAN. .
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
  TOGGLE TO SELECT A "FROM" AND A "TO"
          THEN PRESS ENTER
```

4. From Main Menu select [6. DETECTORS]
5. From DETECTOR Submenu select [2. VEHICLE DETECTOR SETUP]
6. Place cursor in VEH DET PLAN [ ] position and enter "2".

   - Place cursor in VEH DETECTOR [ ] position and enter "3".
   - Set delay time to "0".

```
VEH DETECTOR [ 3]   VEH DET PLAN [ 2]
TYPE: N-NTCIP
TS2 DETECTOR..... . ECPI LOG.......   NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
 3  3   . . . . . . . . . . . . . . . .
EXTEND TIME...  0.0 DELAY TIME...    0.0
USE ADDED INITIAL . CROSS SWITCH PH..  0
LOCK IN....... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
```

← NOTICE VEH DET PLAN 2

← ENSURE DELAY IS SET TO '0'

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [2. CONTROLLER]
2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

Toggle to 'Overlap G'

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```
TMG VEH OVLP...[G] TYPE: .....NORMAL
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

↓ Toggle To 'Overlap B'

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```
TMG VEH OVLP...[B] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....   OVERLAP  G
OPPOSING THROUGH.......   PHASE    6

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S4 as OLG, program LD SWITCH 3 as OVLP '7' TYPE 'O' as shown below.

1. From Main Menu select [1. CONFIGURATION]
2. From CONFIGURATION Submenu select [3. LOAD SW ASSIGN]

NOTEICE OVERLAP G ASSIGNED TO LD SWITCH 3 →

LD SWITCH ASSIGN

	PHASE /OVLP	TYPE	DIMMING R	Y	G	D	FLASH PWR	AUT	TGR
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	R	.
3	7	O	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	−	A	R	.
6	6	V	.	.	.	−	A	Y	X
7	7	V	.	.	.	−	A	R	.
8	8	V	.	.	.	−	A	R	X
9	1	O	.	.	.	+	A	R	X
10	2	O	.	.	.	+	A	Y	X
11	3	O	.	.	.	−	A	R	.
12	4	O	.	.	.	−	A	R	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	−	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	−	A	.	.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1624
DESIGNED: July 2022
SEALED: 07/12/2022
REVISED:

VHB Engineering NC, P.C. (C-3705)
940 Main Campus Drive, Suite 500
Raleigh, NC 27606
919.829.0328

Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 55 EB
at
Bitter Melon Drive
(North Intersection)

Division 5 Wake County Angier

Prepared for the Offices of:
Traffic Engineering and Safety Systems Branch
Signal Management Section
750 N. Greenfield Pwky, Garner, NC 27529

PLAN DATE:	July 2022	REVIEWED BY:	J. Ma
PREPARED BY:	J. Townsend	REVIEWED BY:	M.L. Stygles

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 046057 ENGINEER MATTHEW L. STYGLES

DocuSigned by:
304B61E94497450...
SIGNATURE 7/12/2022 DATE

SIG. INVENTORY NO. 05-1624

$$$$SYSTIME$$$$
$$$$USERID$$$$
$$$$SERNAME$$$$