

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 7A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select 8. UTILITIES
2. From UTILITIES Submenu select 1. COPY/CLEAR
3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY
FROM TO
PHASE TIMING.... > PHASE TIMING....
TIMING PLAN..... > TIMING PLAN.....
PH DET OPT PLAN. . > PH DET OPT PLAN. .
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO"
THEN PRESS ENTER

4. From Main Menu select 6. DETECTORS
5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
6. Place cursor in VEH DET PLAN [] position and enter "2".

- Place cursor in VEH DETECTOR [] position and enter "7".
- Set delay time to "0".

```

VEH DETECTOR [ 7]  VEH DET PLAN [ 2]
TYPE: N-NTCIP
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
7 7 . . . . .
EXTEND TIME... 0.0 DELAY TIME... 0.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

← NOTICE VEH DET PLAN 2

← ENSURE DELAY IS SET TO '0'

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to 'Overlap G'

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVLP...[G] TYPE: ....[NORMAL]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle to 'Overlap C'

OVERLAP C

Select TMG VEH OVLP [C] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[C] TYPE: [OTHER/ECONOLITE]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
    
```

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```

TMG VEH OVLP...[D] TYPE: ....[PPLT FYA]
PROTECTED LEFT TURN.... OVERLAP G
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH12 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 7
    
```

← NOTICE ACTION PLAN SF BIT '7'

END PROGRAMMING

ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S10 as OLG, program LD SWITCH 7 as OVLP '7' TYPE 'O' as shown below.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

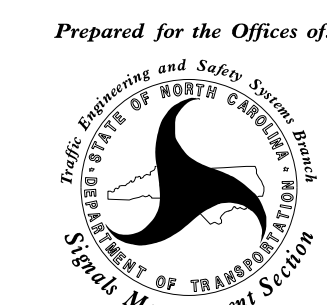
NOTICE OVERLAP G ASSIGNED TO LD SWITCH 7 →

LD SWITCH ASSIGN									
PHASE	DIMMING	---	FLASH	---					
/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR	
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	R	.
3	3	V	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	Y	X
7	7	O	.	.	.	-	A	R	.
8	8	V	.	.	.	-	A	R	X
9	1	O	.	.	.	+	A	R	X
10	2	O	.	.	.	+	A	R	X
11	3	O	.	.	.	-	A	Y	.
12	4	O	.	.	.	-	A	Y	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	-	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	-	A	.	.

Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING
DETAILS FOR:



750 N. Greenfield Parkway, Garner, NC 27529

NC 55 at SR 2763 (Maude Stewart Road)/ Bitter Melon Drive	
Division 5 Wake County Angier	
PLAN DATE: July 2022	REVIEWED BY: J. Ma
PREPARED BY: J. Townsend	REVIEWED BY: M.L. Stygles
REVISIONS	INIT. DATE

SEAL

NORTH CAROLINA
PROFESSIONAL
ENGINEER
SEAL
046057
MATTHEW L. STYGLES

7/12/2022

SIGNATURE DATE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1625
DESIGNED: July 2022
SEALED: 07/12/2022
REVISED:

