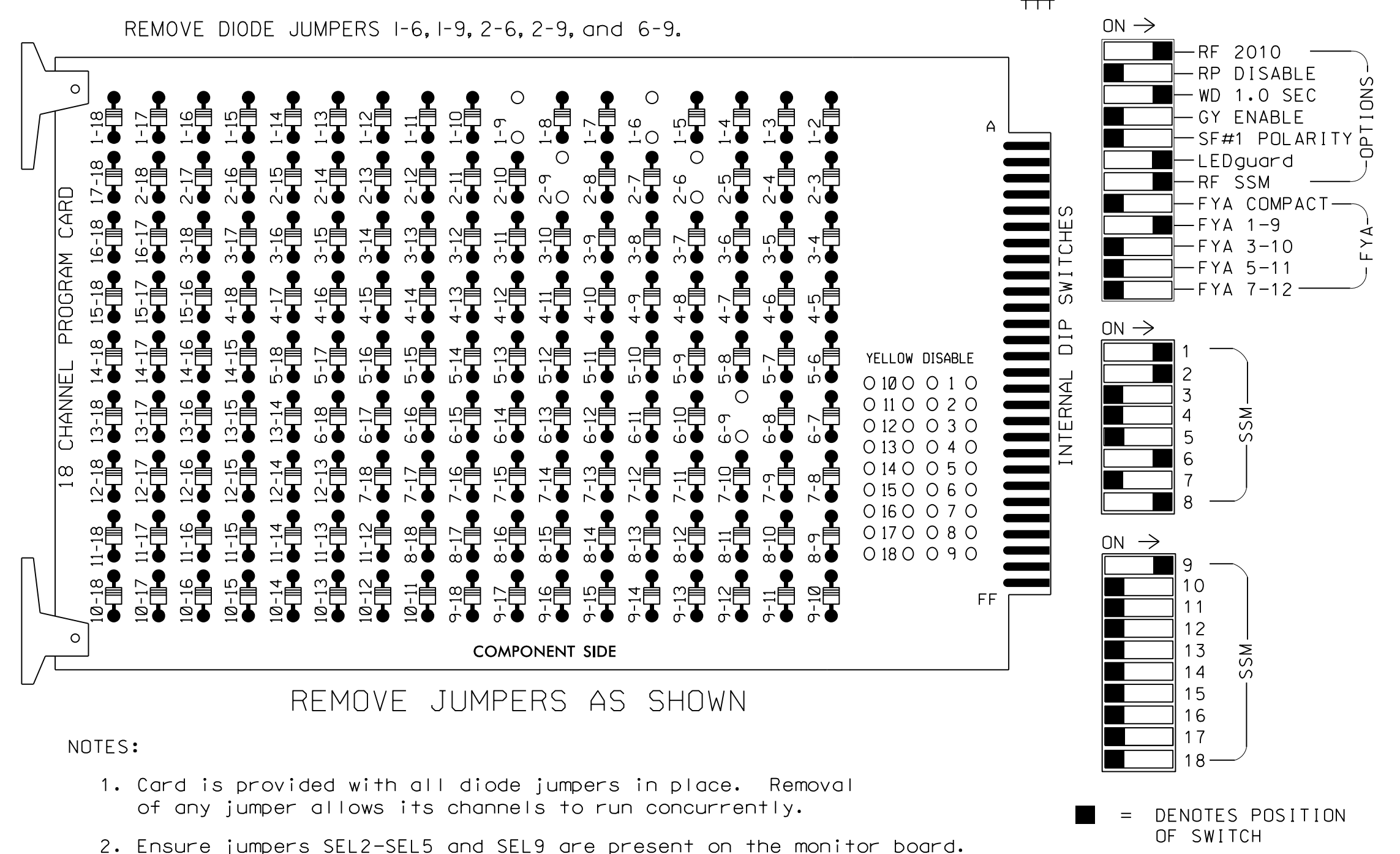


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 3. Ensure that Red Enable is active at all times during normal operation.
 4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phases 2 and 6 for Gap Reduction.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

EQUIPMENT INFORMATION

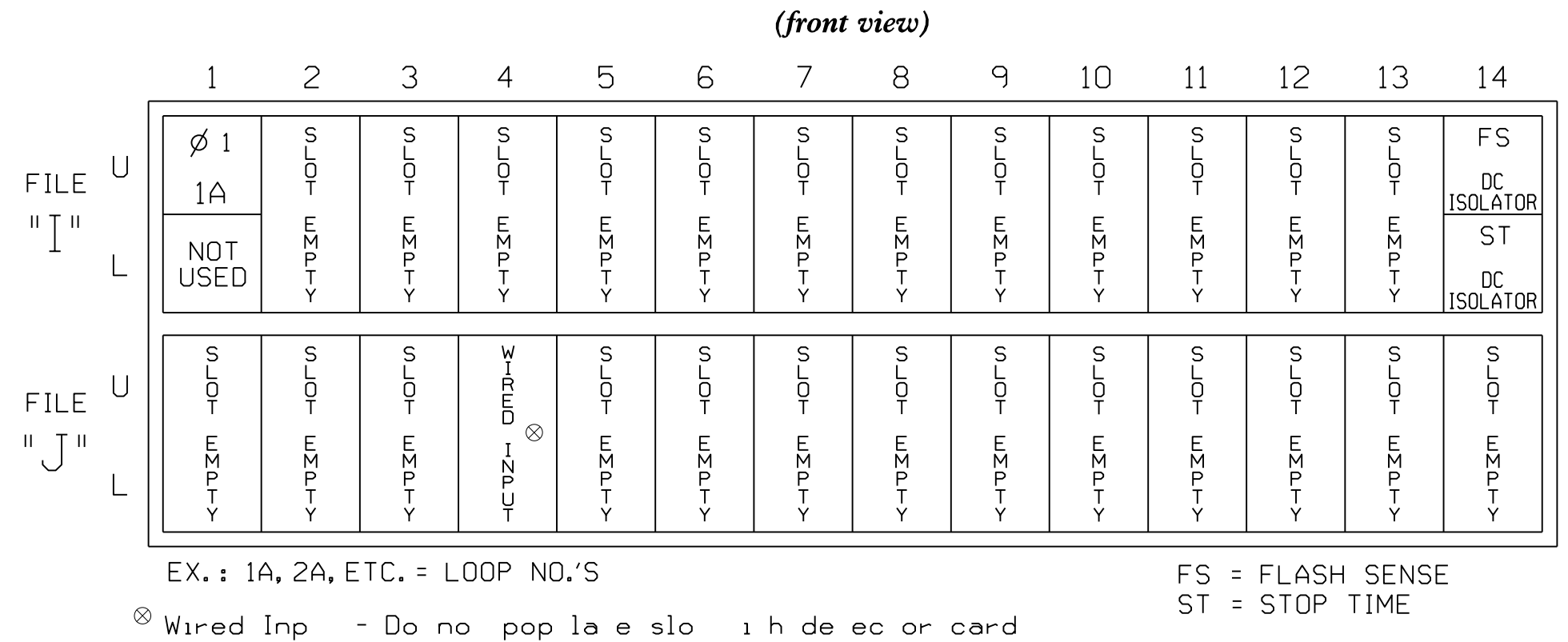
CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUXS1
 PHASES USED.....1,2,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21,22	NU	NU	NU	NU	61,62	NU	NU	81,82, 83	22	NU	11★	NU	NU	NU	NU
RED		*	128					134			107							
YELLOW			129					135			108							
GREEN			130					136			109							
RED ARROW													A121					
YELLOW ARROW			126								108		A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127	127									109							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT



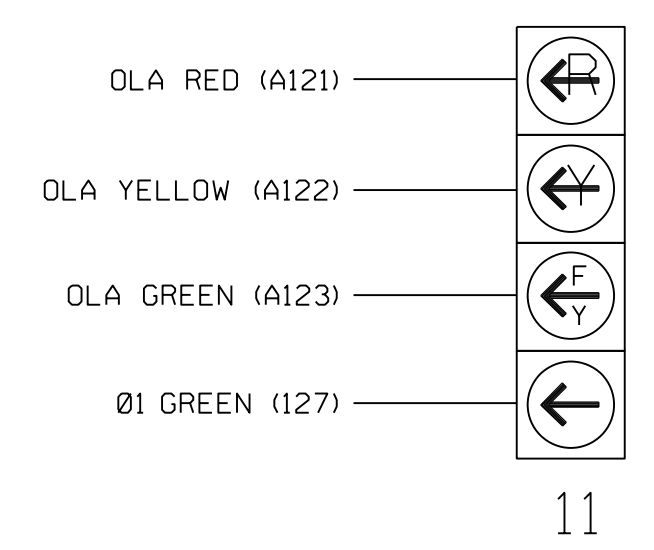
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			3

¹Add jumper from I1-W to J4-W, on rear of input file.
 ★See Input Page Assignment programming details on sheet 3.

FYA SIGNAL WIRING DETAIL

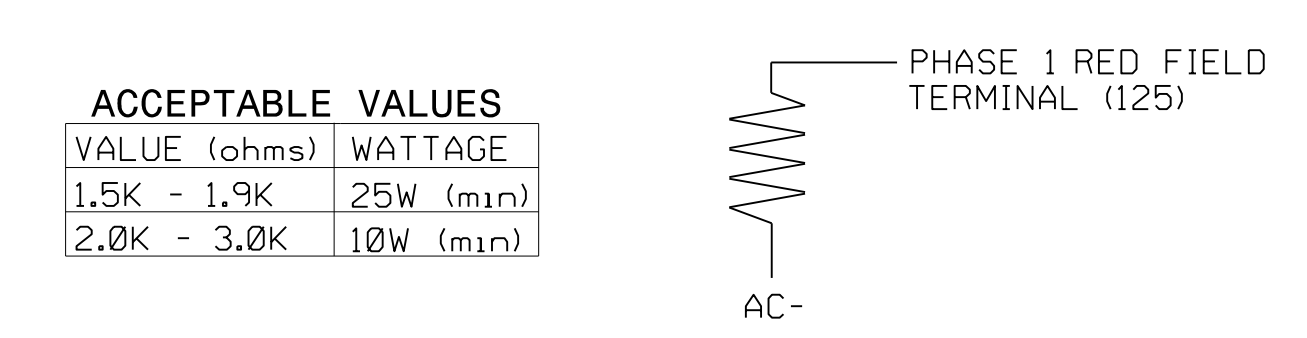
(wire signal heads as shown)



NOTE
 The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



DETECTOR NOTES

1. For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
2. For loop 5A, detector card placement and slot reserved for wired input are typical for a NCDOT installation. Input associated with this slot are compatible with time of day instructions located on sheet 3 of this electrical detail.

Temporary Design 1 - (TMP Phase I)
 Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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 at
 SR 1529 (Cox Mill Road)

Division 8 Lee County Sanford

PLAN DATE: October 2019 REVIEWED BY: E D Harris
 PREPARED BY: R M Muncey REVIEWED BY:

REVISIONS	INIT.	DATE

Seal of Regina M. Muncey, Professional Engineer, License No. 43239, State of North Carolina.

DocuSigned by:
 Regina M. Muncey 10/3/2019
 DATE

SIG. INVENTORY NO. 08-0649T1