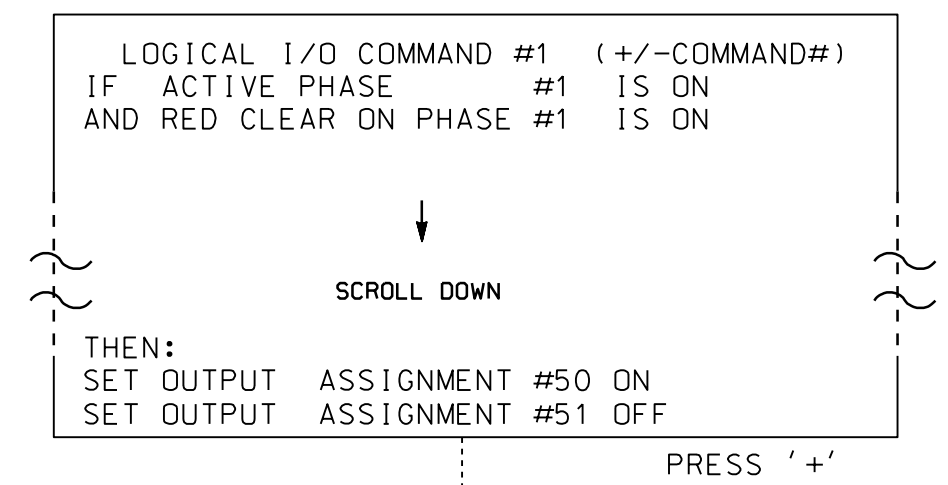


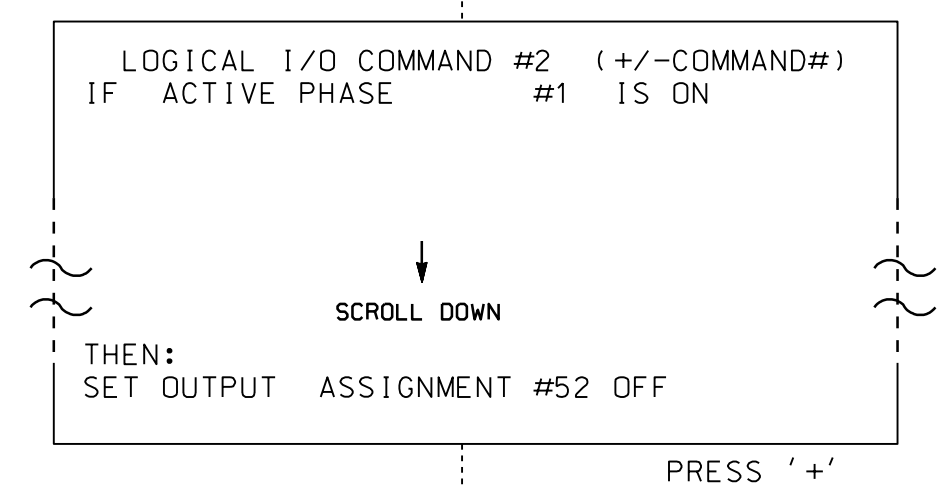
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

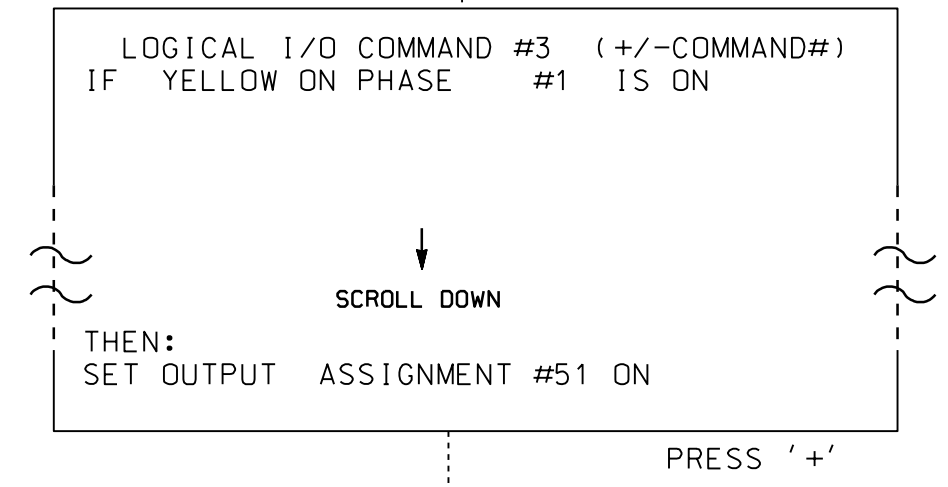
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, AND 9.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



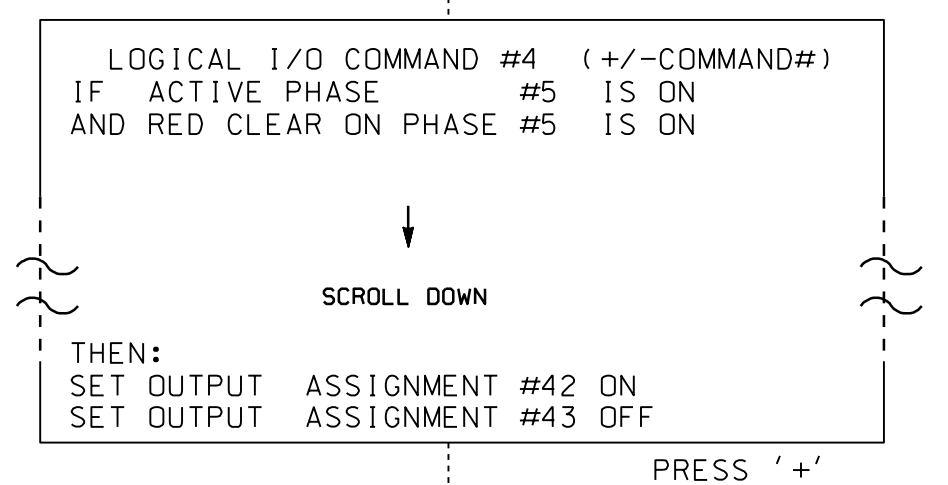
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



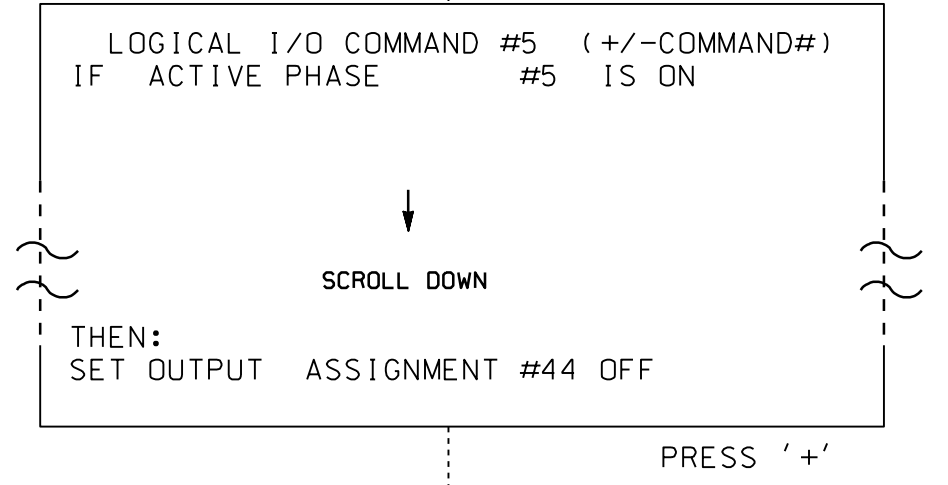
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).



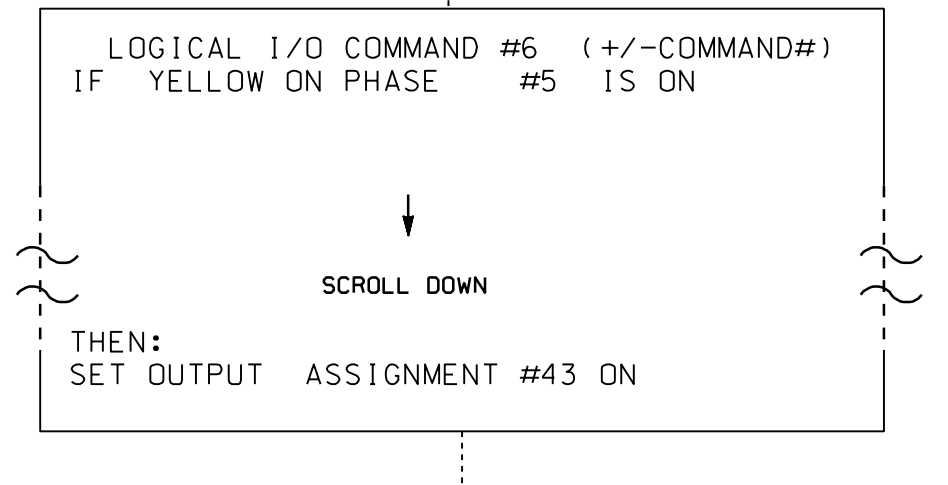
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



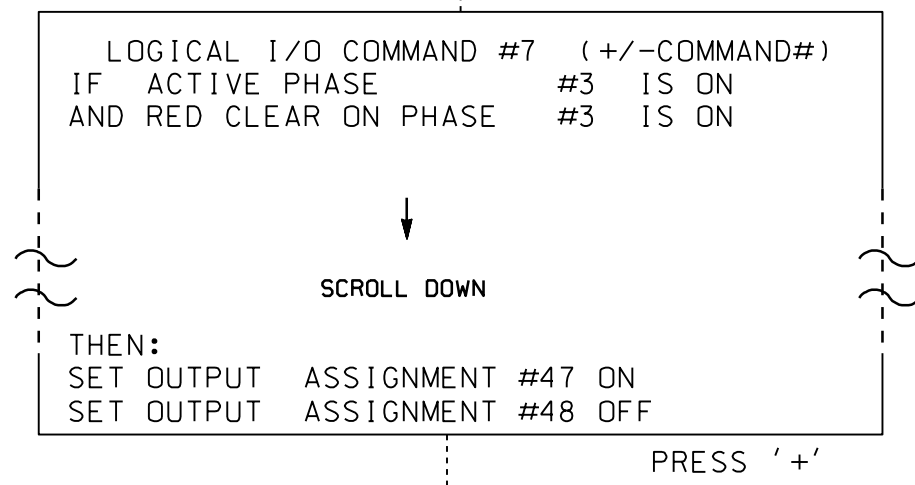
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



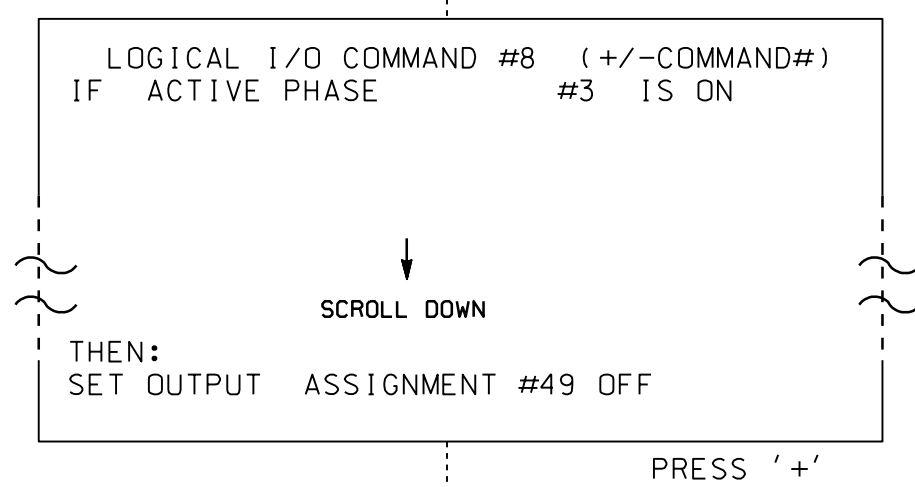
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).



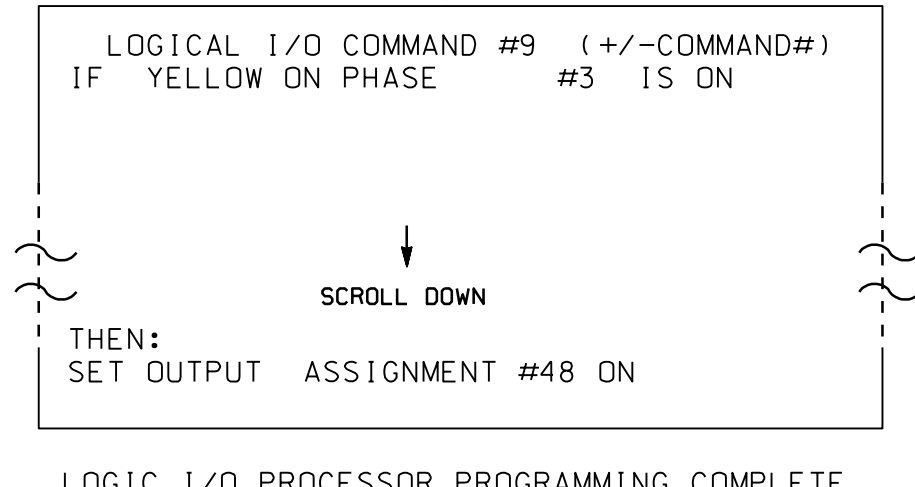
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).



NOTE: LOGIC FOR OLG RED CLEAR (HEAD 63).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" (HEAD 63).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE (HEAD 63).

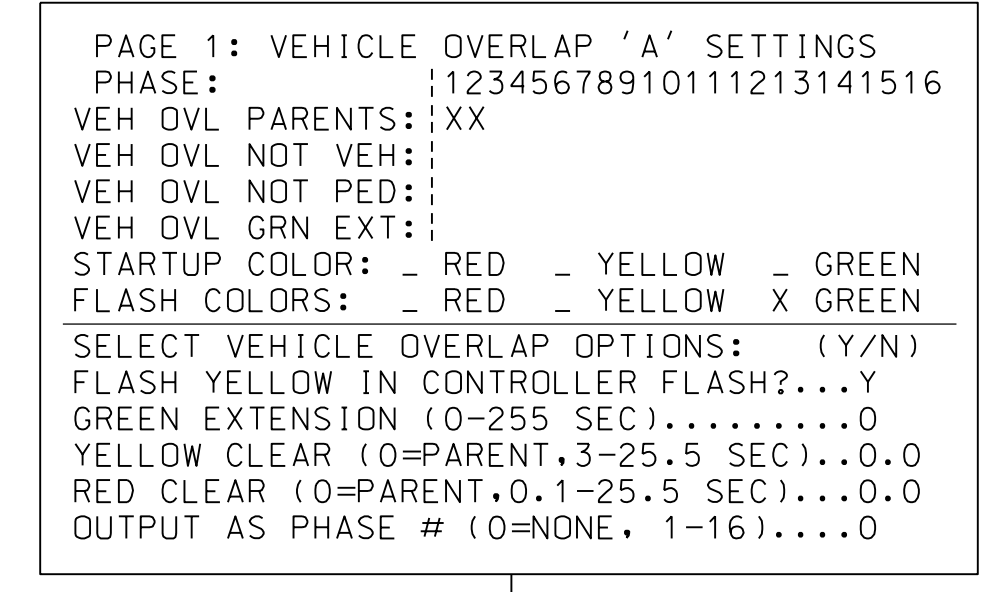
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 47 =	Overlap B Red
OUTPUT 48 =	Overlap B Yellow
OUTPUT 49 =	Overlap B Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

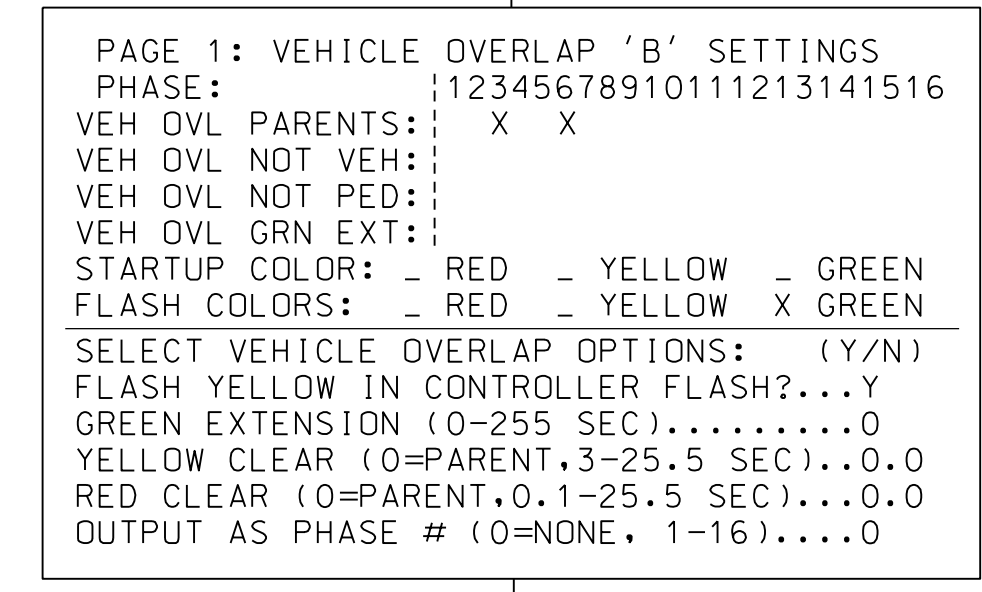
OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

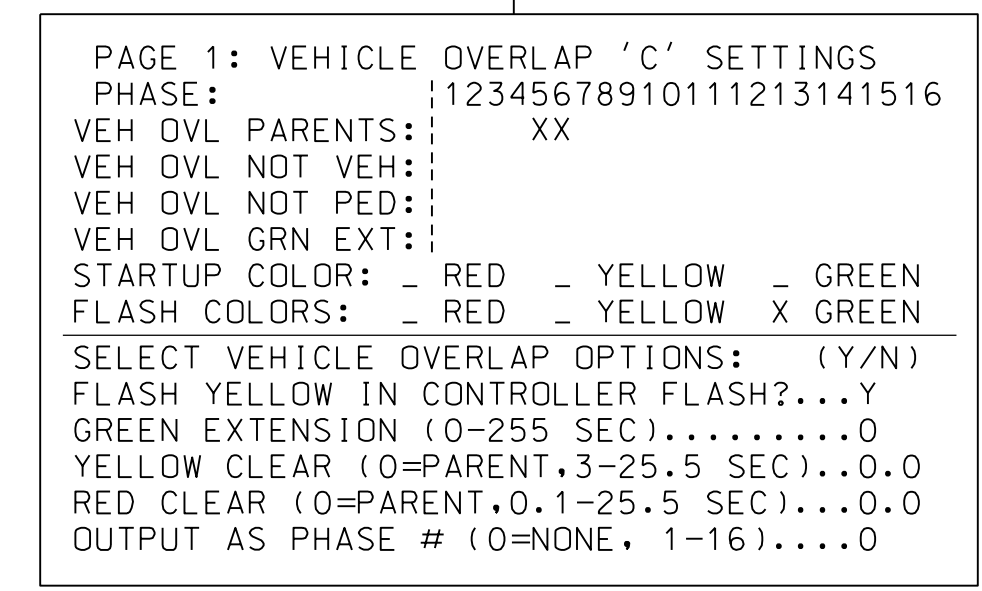
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



← NOTICE GREEN FLASH



← NOTICE GREEN FLASH



← NOTICE GREEN FLASH

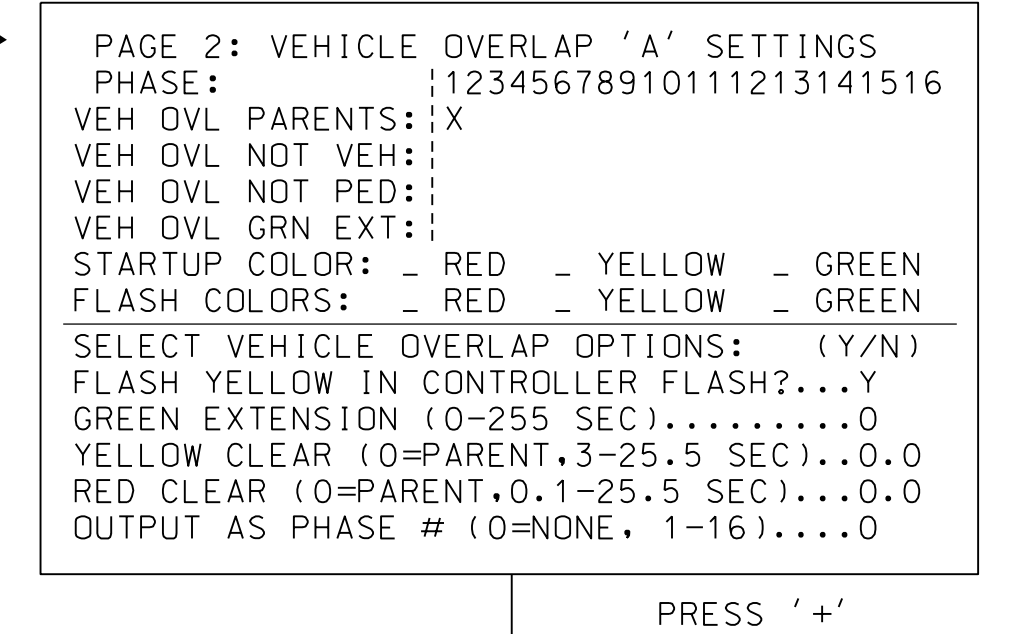
OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

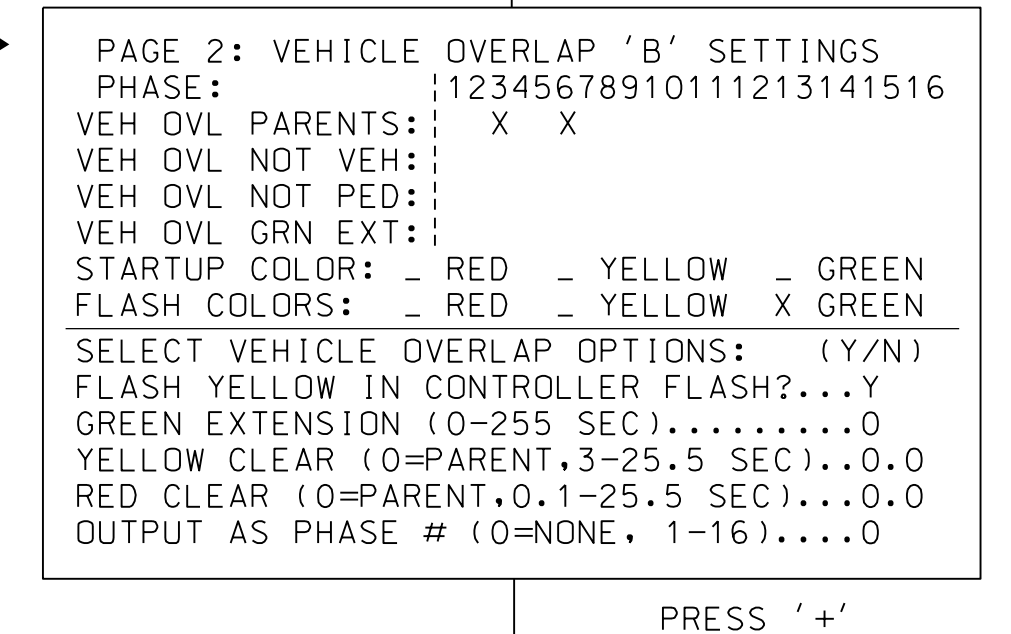
(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

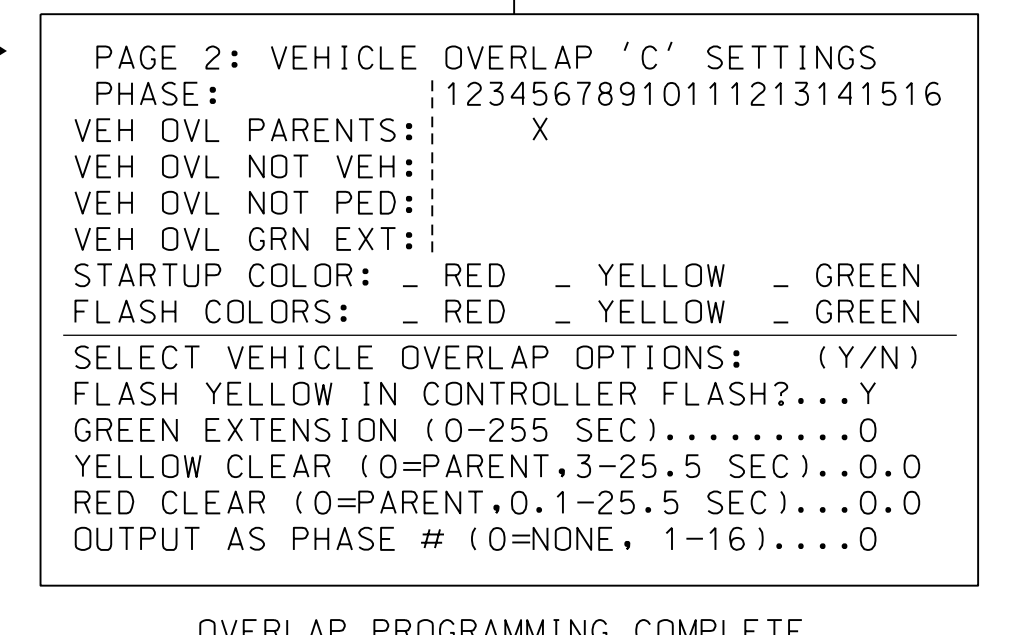
NOTICE PAGE 2 →



NOTICE PAGE 2 →



NOTICE PAGE 2 →



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1065T1
DESIGNED: JANUARY 2020
SEALED: 11-05-2019
REVISED:

Temporary Design 1 - (TMP Phase I)
Electrical Detail - Sheet 2 of 5

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

NC 42 (Broadway Road) at US 421/NC 87 Northbound Ramps/ Coty Plant Entrance	
Division 8	Lee County Sanford
PLAN DATE: JANUARY 2020	REVIEWED BY: E D Harris
PREPARED BY: R M Muncey	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL

REGINA M. MUNCEY
ENGINEER
1/7/2020

SIG. INVENTORY NO. 08-1065T1

I:\00131_Plan... User: rlmuncey