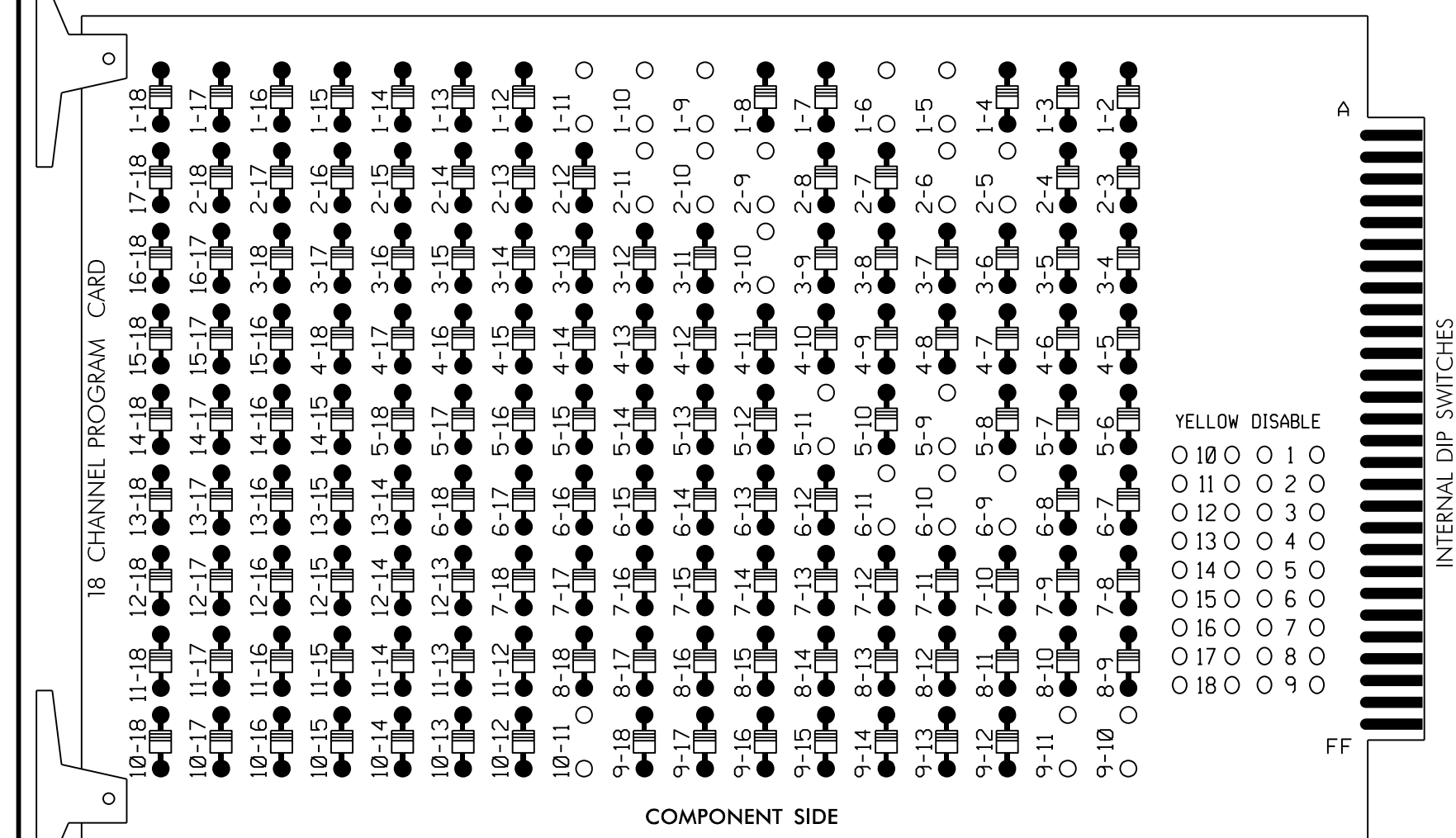


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

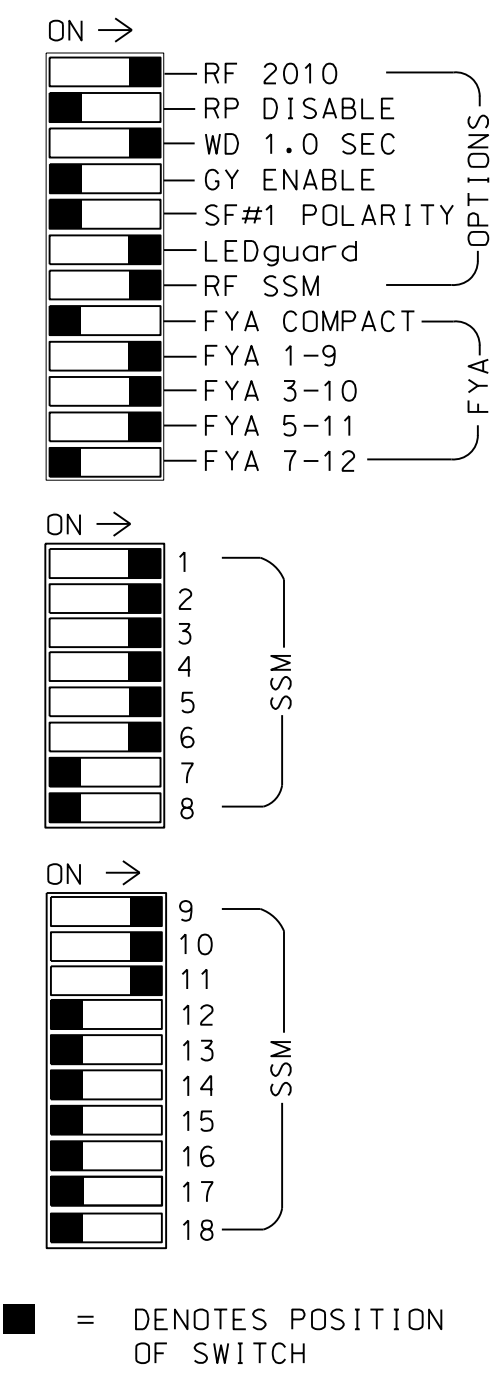
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-10, 1-11, 2-5, 2-6, 2-9, 2-10, 2-11, 3-10, 5-9, 5-11, 6-9, 6-10, 6-11, 9-10, 9-11, and 10-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,AUXS1,AUXS2,AUXS4
 PHASES USED.....1,2,3,4,5,6
 OVERLAP "A".....1+2
 OVERLAP "B".....3+6
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

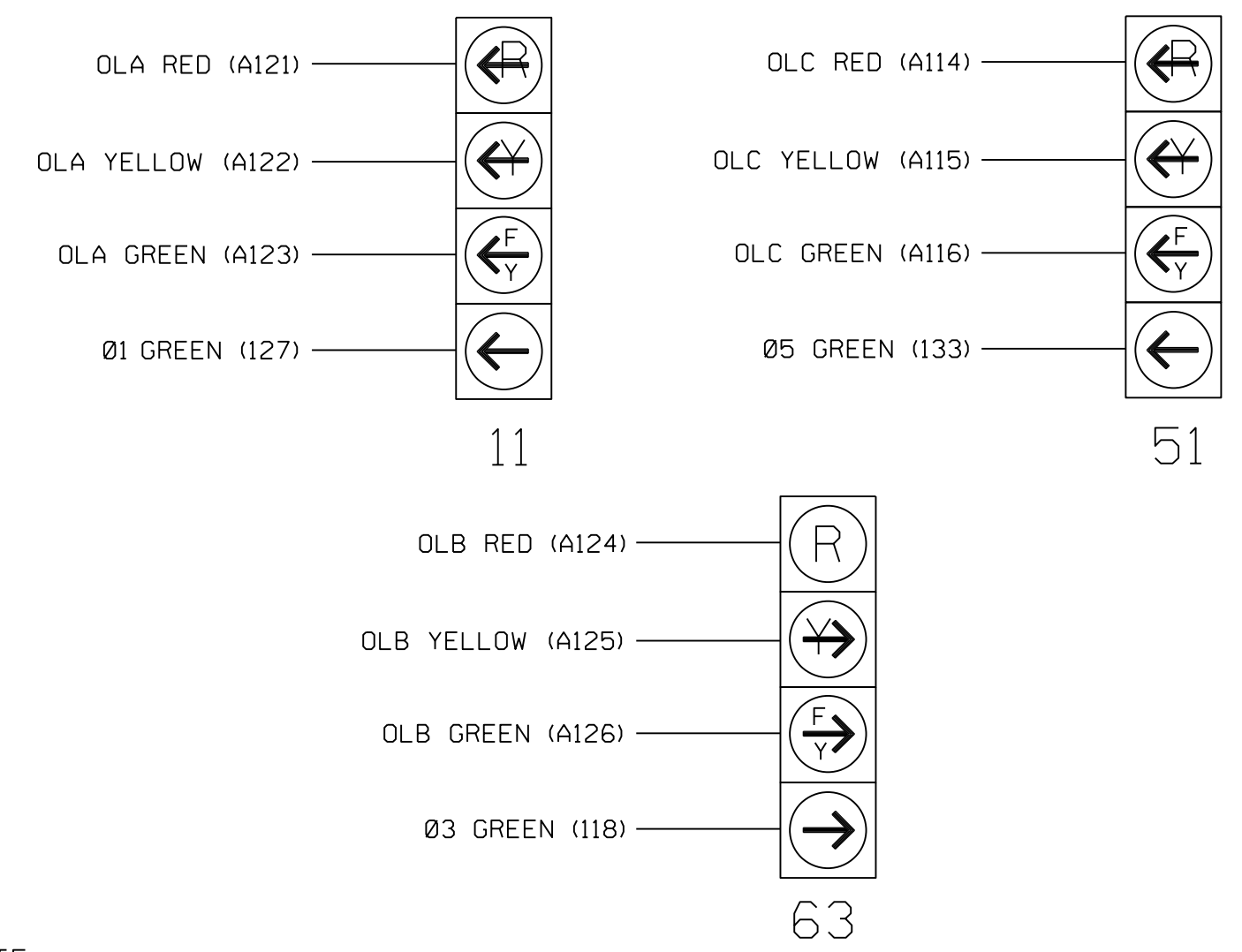
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	42	21,22	31	32	63★	41	42	22	NU	51★	32	61,62	NU	NU	NU	NU	NU
RED	*	128		116	116		101	101			*	134				A124		
YELLOW		129		117	117		102	102				135						
GREEN		130		118	118		103	103				136						
RED ARROW																A121		A114
YELLOW ARROW	126							102		132						A122	A125	A115
FLASHING YELLOW ARROW																A123	A126	A116
GREEN ARROW	127	127		118	118	103	103	133	133									

NU = Not Used
 ★ See pictorial of head wiring in detail this sheet.
 * Denotes install load resistor. See load resistor installation detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11, 51, and 63 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	FS
L	1A	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	DC ISOLATOR
L	NOT USED	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	ST
L		10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	DC ISOLATOR
U	Ø 5	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	S
L	5A	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	OT
L	NOT USED	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	10S	RTY

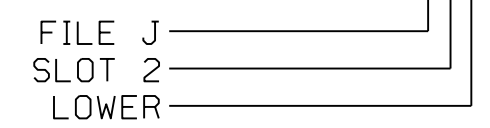
EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	11U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	11U	56	18★	51	1	Y	Y			3
5A ²	TB3-1,2	11U	55	17	5	5	Y	Y			15
	-	14U	47	9★	22	2	Y	Y	Y		3
	-	11U	55	17★	55	5	Y	Y			3

¹Add jumper from I1-W to J4-W, on rear of input file.
²Add jumper from J1-W to I4-W, on rear of input file.
 ★See Input Page Assignment programming details on sheets 3 and 4.

INPUT FILE POSITION LEGEND: J2L

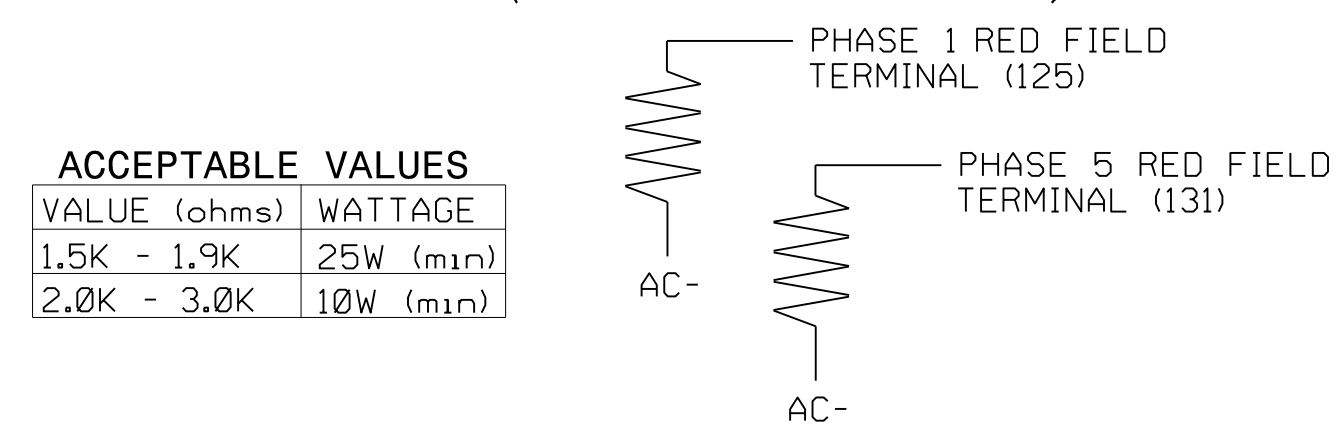


DETECTOR NOTES

- For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 5A, detector card placement and slot reserved for wired input are typical for a NCDOT installation. Input associated with this slot are compatible with time of day instructions located on sheets 3 and 4 of this electrical detail.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



Temporary Design 1 - (TMP Phase I)
 Electrical Detail - Sheet 1 of 5

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:	
NC 42 (Broadway Road) at US 421/NC 87 Northbound Ramps/ Coty Plant Entrance	
Division 8	Lee County Sanford
PLAN DATE: JANUARY 2020	REVIEWED BY: E D Harris
PREPARED BY: R M Muncey	REVIEWED BY:
REVISIONS	INIT. DATE