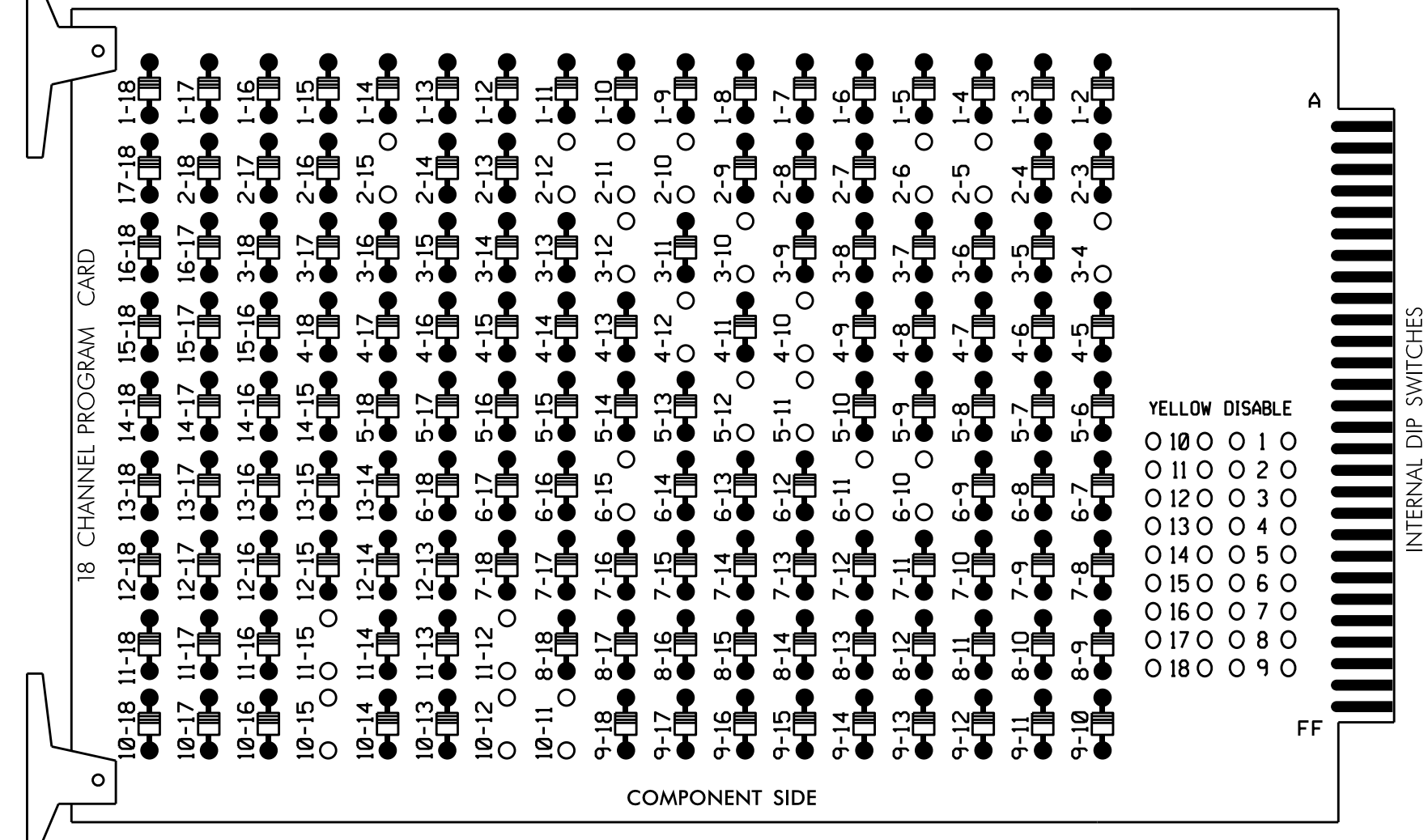


### EDI MODEL 2018ECL-NC CONFLICT MONITOR

#### PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-10, 2-11, 2-12, 2-15, 3-4, 3-10, 3-12, 4-10, 4-12, 5-11, 5-12, 6-10, 6-11, 6-15, 10-11, 10-12, 10-15, 11-2 and 11-5.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phase 6 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlap 2 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of Signal System #: 10823.

### EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE AUXS4, AUX S5  
 LOAD SWITCHES USED.....S2,S4,S5,S7,S8,S9,AUXS2, AUXS4,AUX S5  
 PHASES USED.....2,4,5,6,6PED  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....4+6  
 OVERLAP "C".....5+6  
 OVERLAP "D".....4+5  
 OVERLAP "E".....NOT USED  
 OVERLAP "F".....NOT USED  
 OVERLAP "G".....4

### SIGNAL HEAD HOOK-UP CHART

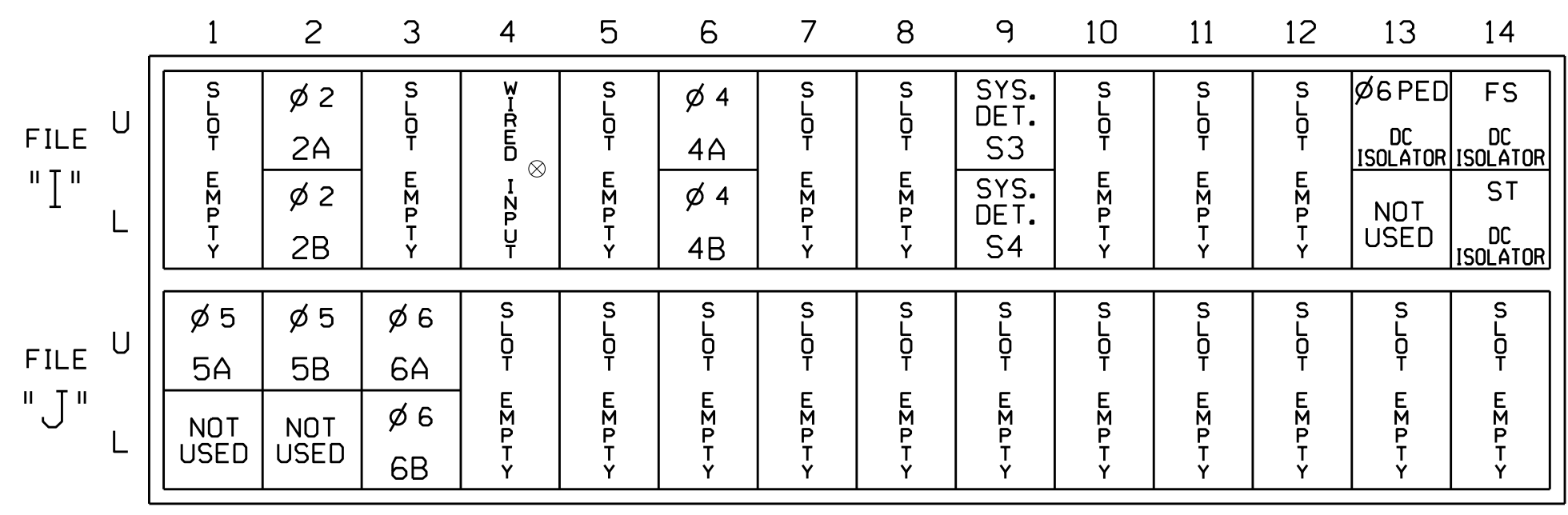
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OLG	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	63	41,42	NU	51	61,62	P61, P62	NU	NU	NU	NU	63	NU	51	43	NU
RED		128								134				A124				A101
YELLOW		129		*			*	135										
GREEN		130						136										
RED ARROW					101													A114
YELLOW ARROW					102									A125		A115	A102	
FLASHING YELLOW ARROW														A126		A116		
GREEN ARROW				118	103		133											A103
Hand									119									
Person									121									

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail this sheet.

NOTE: Load Switch S4 requires output remapping. See sheet 4 of this electrical detail for instructions.

### INPUT FILE POSITION LAYOUT

(front view)



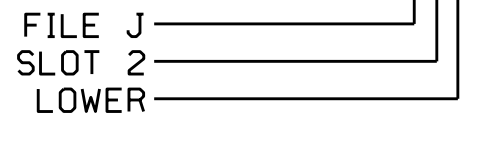
EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9 ★	22	2	Y	Y	Y		3
	-	J1U	55	17 ★	55	5	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
* S3	TB6-9,10	I9U	60	22	11	SYS					
* S4	TB6-11,12	I9L	62	24	13	SYS					
PED PUSH BUTTONS											
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.  
 ★ See Input Page Assignment programming details on sheet 3.  
 \* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

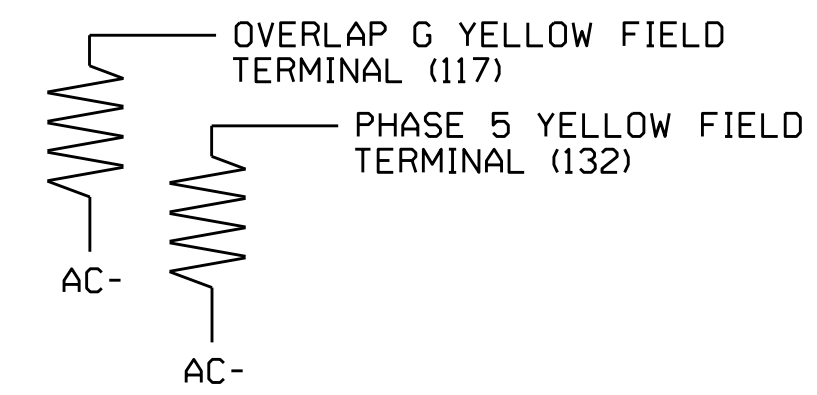
#### INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL

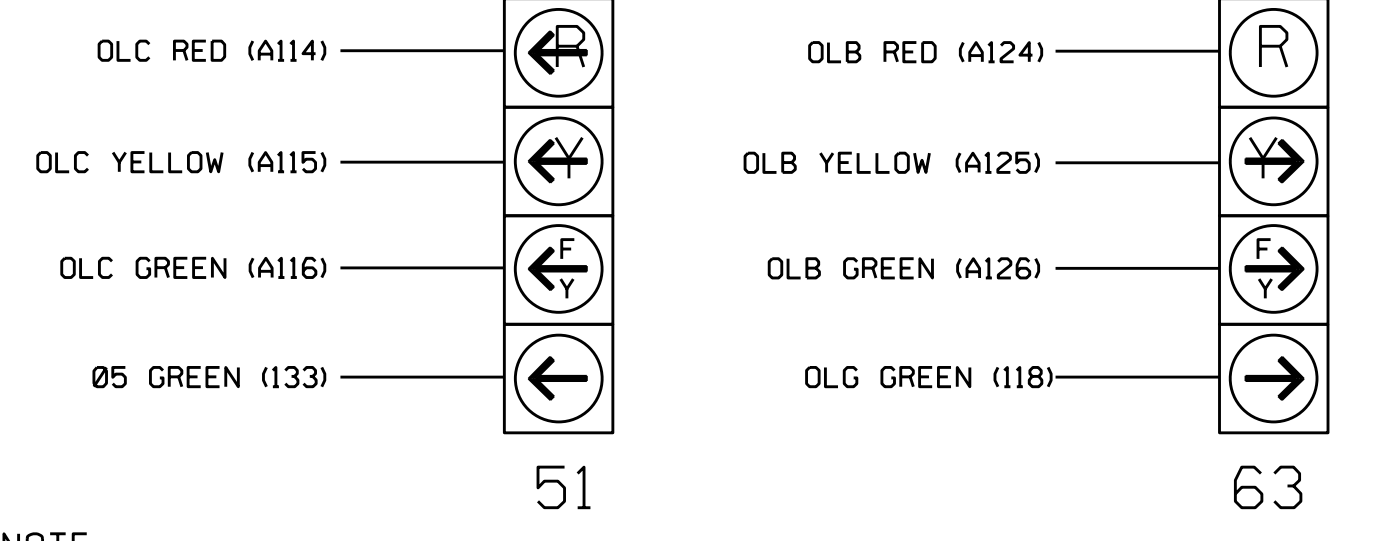
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE: The sequence display for signal heads 51 and 63 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1064  
 DESIGNED: March 2022  
 SEALED: 4/21/2022  
 REVISED: N/A

Electrical Detail - Sheet 1 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	NC 42 (Broadway Road) at US 421/NC 87 Southbound Ramps		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 031001 D. Todd Joyce 04/22/2022 DATE
	Division 8 Lee County Sanford	PLAN DATE: April 2022 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY:	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 08-1064