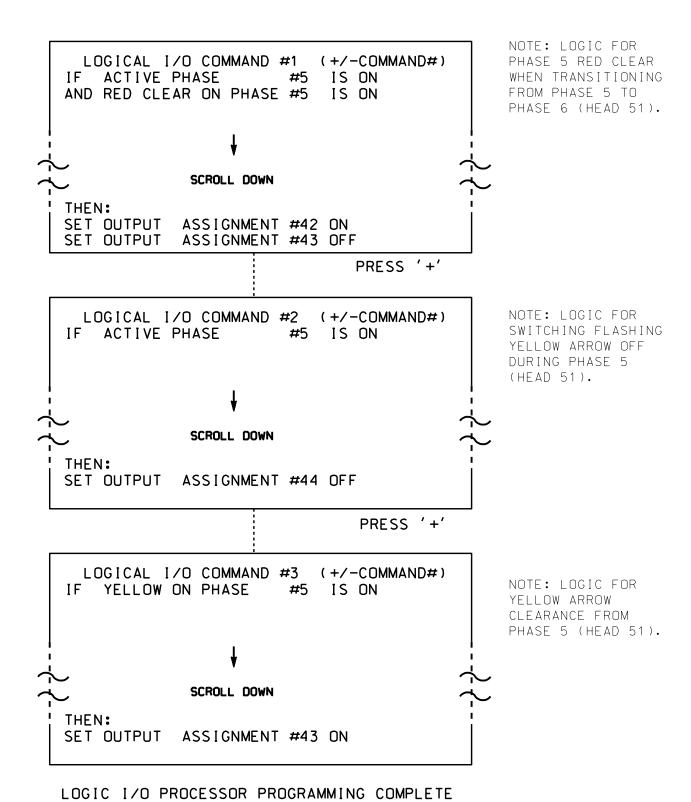## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON
              |
              ↓
         SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #42 ON
SET OUTPUT  ASSIGNMENT #43 OFF
```
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
              |
              ↓
         SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #44 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #5  IS ON
              |
              ↓
         SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #43 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE
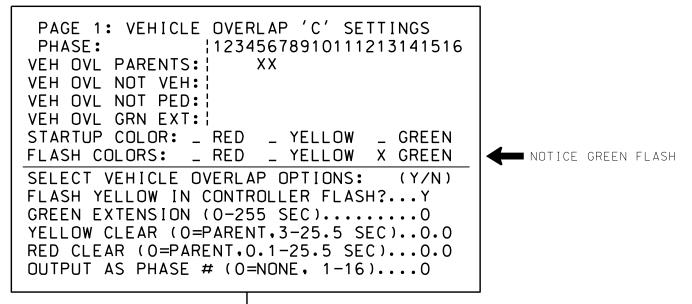
```
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
```

OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE:              123456789101112131415 16
VEH OVL PARENTS:|    XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN     ← NOTICE GREEN FLASH
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........O
YELLOW CLEAR (0=PARENT.3-25.5 SEC)..O.O
RED CLEAR (0=PARENT.0.1-25.5 SEC)...O.O
OUTPUT AS PHASE # (0=NONE, 1-16)....O
```

PRESS '+' ONCE

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
 PHASE:              123456789101112131415 16
VEH OVL PARENTS:|    XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........O
YELLOW CLEAR (0=PARENT.3-25.5 SEC)..O.O
RED CLEAR (0=PARENT.0.1-25.5 SEC)...O.O
OUTPUT AS PHASE # (0=NONE, 1-16)....O
```

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL
FOR ALTERNATE PHASING

(program controller as shown below)

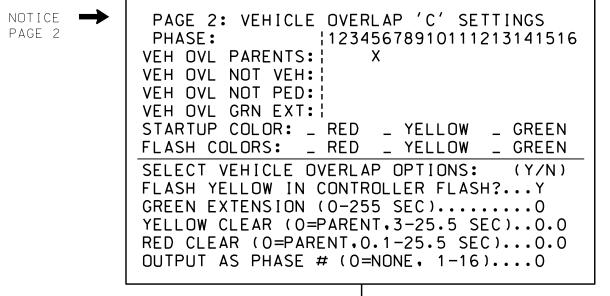FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.
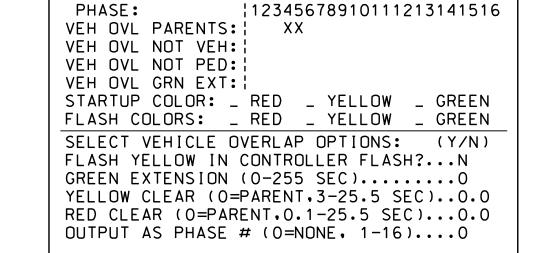
PRESS '+' TWICE

NOTICE PAGE 2 →
```
PAGE 2: VEHICLE OVERLAP 'C' SETTINGS
 PHASE:              123456789101112131415 16
VEH OVL PARENTS:|    X
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........O
YELLOW CLEAR (0=PARENT.3-25.5 SEC)..O.O
RED CLEAR (0=PARENT.0.1-25.5 SEC)...O.O
OUTPUT AS PHASE # (0=NONE, 1-16)....O
```

PRESS '+' ONCE

NOTICE PAGE 2 →
```
PAGE 2: VEHICLE OVERLAP 'D' SETTINGS
 PHASE:              123456789101112131415 16
VEH OVL PARENTS:|    XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........O
YELLOW CLEAR (0=PARENT.3-25.5 SEC)..O.O
RED CLEAR (0=PARENT.0.1-25.5 SEC)...O.O
OUTPUT AS PHASE # (0=NONE, 1-16)....O
```

OVERLAP PROGRAMMING COMPLETE

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 08-1064T1
DESIGNED: March 2022
SEALED: 4/21/2022
REVISED: N/A
```

Temporary Design 1
Electrical Detail - Sheet 2 of 4

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

SEAL
031001
NORTH CAROLINA
PROFESSIONAL
ENGINEER
D. TODD JOYCE

DocuSigned by:
D. Todd Joyce 04/22/2022
A90CADFD8D4241D   DATE

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zzafar