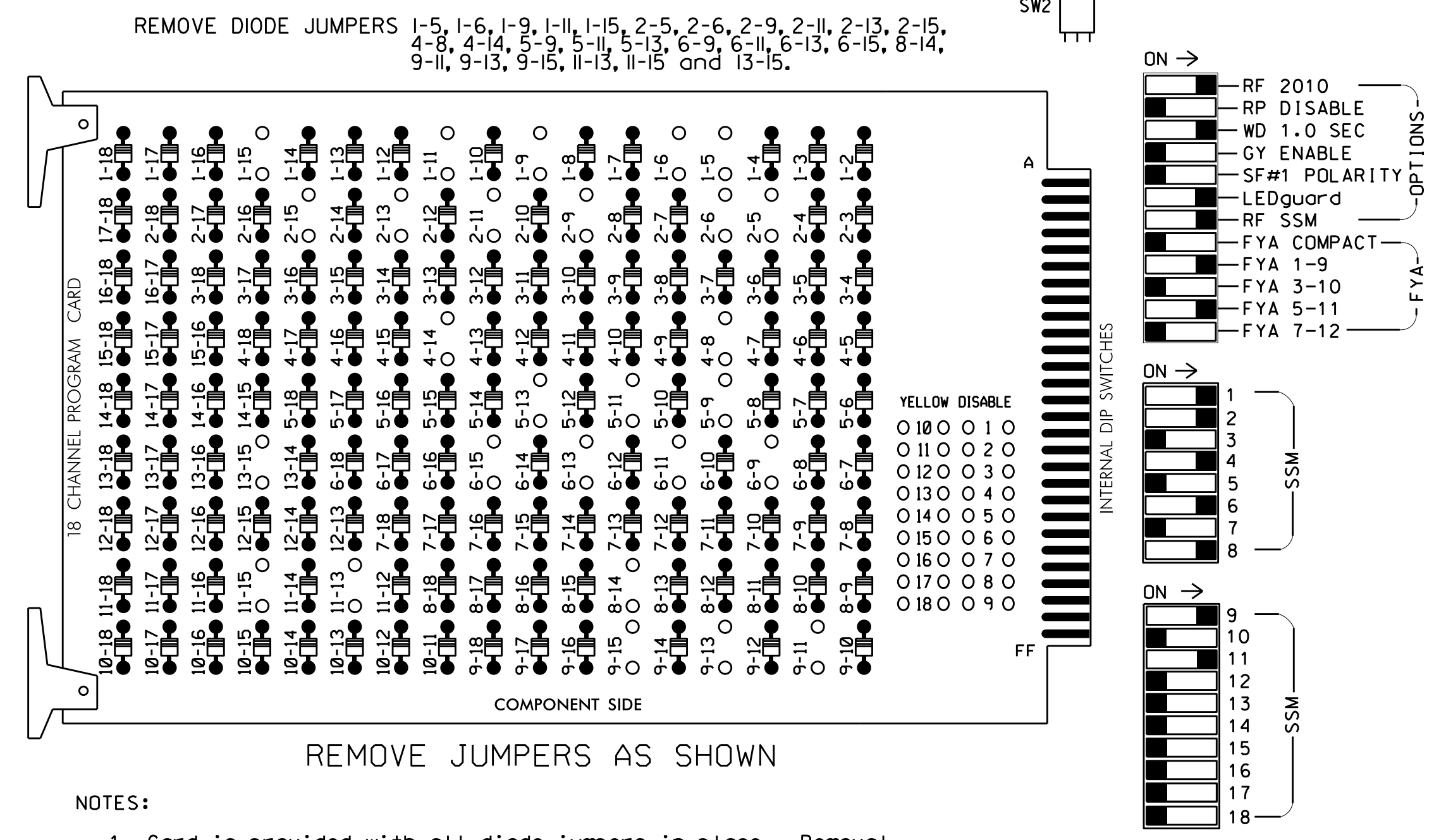


**EDI MODEL 2018ECL-NC CONFLICT MONITOR**  
**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2, 4 and 6 for Startup Ped Call.
7. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
8. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,  
 AUXS1,AUX S4  
 PHASES USED.....1,2,2 PED,4,4 PED,5,6,6 PED,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	P21, P22	NU	41,42	P41, P42	51	61,62	P61, P62	NU	81,82	NU	11	NU	NU	51	NU
RED		*	128			101			134			107						
YELLOW			129			102		*	135			108						
GREEN			130			103			136			109						
RED ARROW																	A121	A114
YELLOW ARROW			126														A122	A115
FLASHING YELLOW ARROW																	A123	A116
GREEN ARROW	127	127							133									
Hand						113			104			119						
Walking						115			106			121						

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

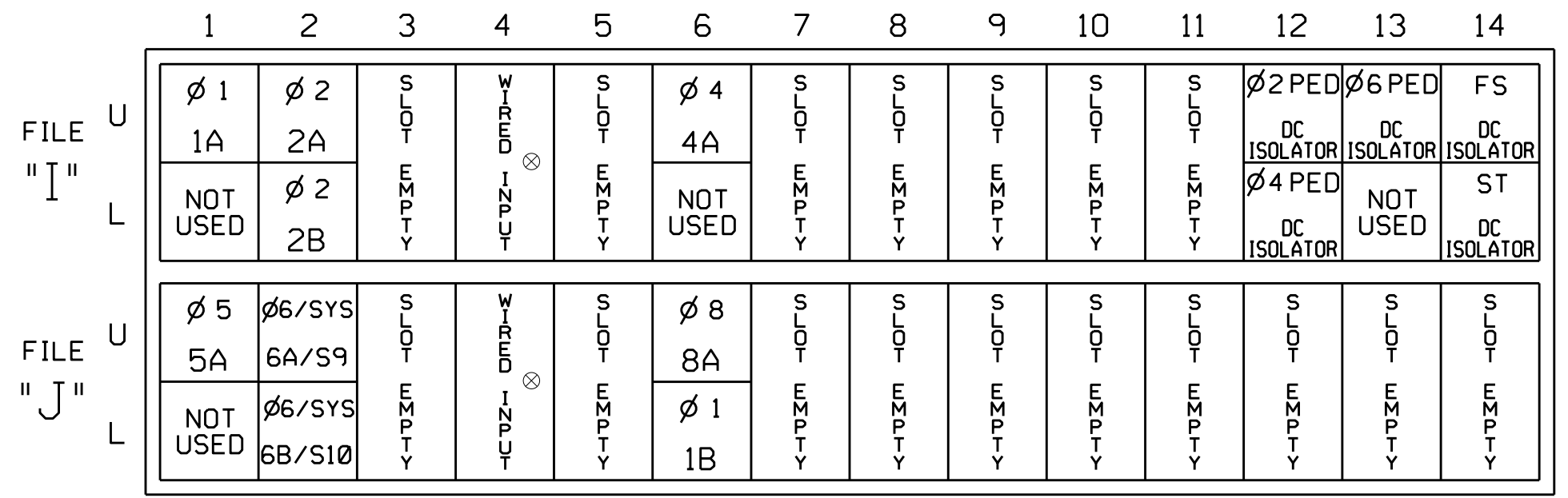
★ See pictorial of head wiring in detail this sheet.

**COUNTDOWN PEDESTRIAN SIGNAL OPERATION**

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
1B	TB5-11,12	J6L	46	8	18	1	Y	Y			15
	2A	TB2-5,6	I2U	39	1	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
6A/S9	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S10	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:  
INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

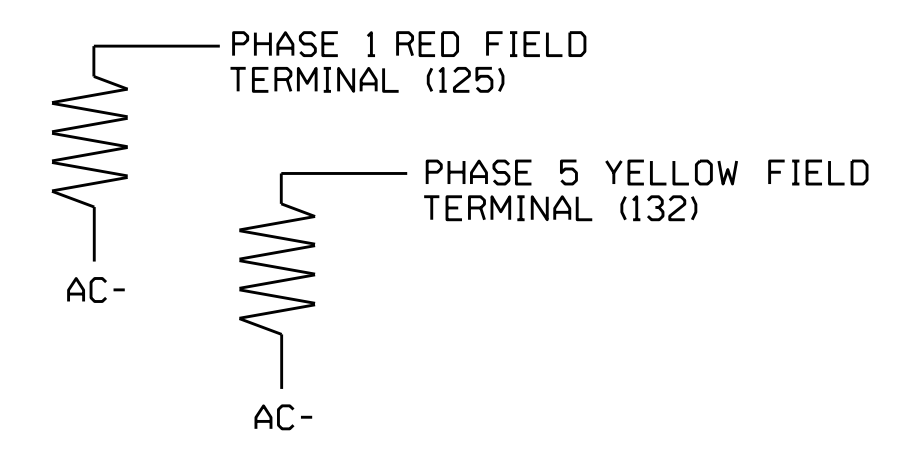
<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

★ See Input Page Assignment programming details on sheets 3 and 4.

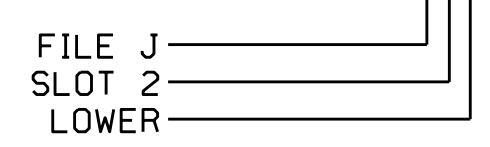
**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

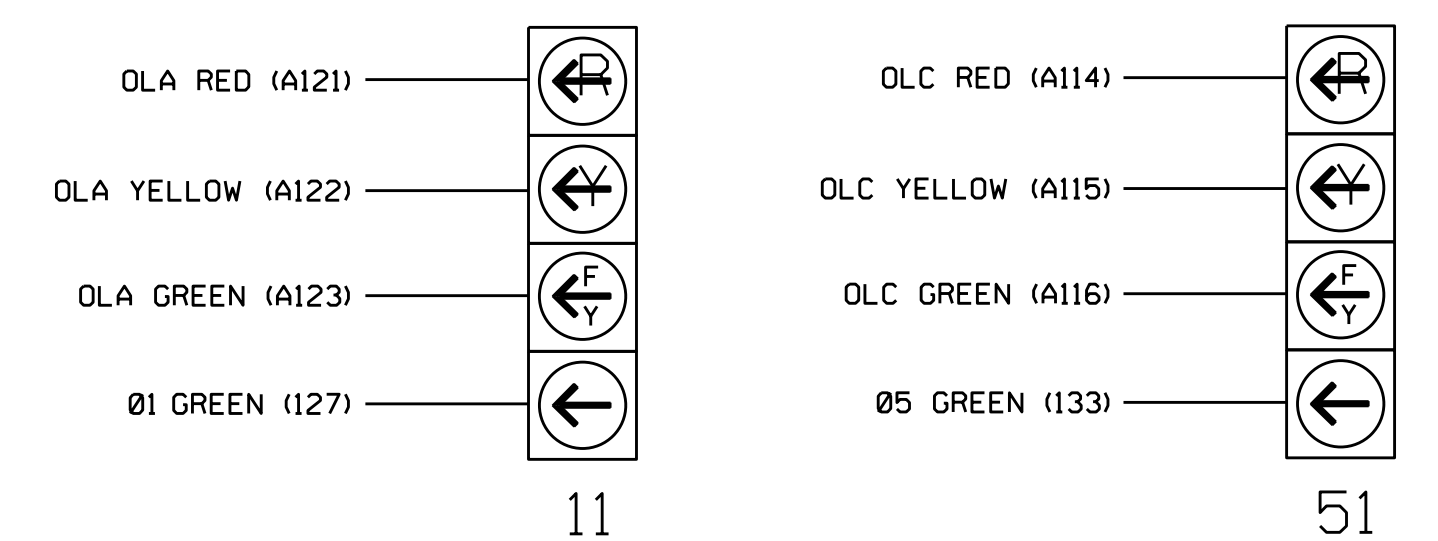


INPUT FILE POSITION LEGEND: J2L



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1018  
 DESIGNED: March 2022  
 SEALED: 4/19/2022  
 REVISED: N/A

Electrical Detail - Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	NC 42 (Main Street) at SR 1520 (Rosser Road) and Magneti Marelli Driveway Lee County Sanford	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL D. Todd Joyce 04/20/2022
	Division 8 PLAN DATE: April 2022 PREPARED BY: Zarrar Zafar	REVIEWED BY: REVIEWED BY: REVISIONS INIT. DATE