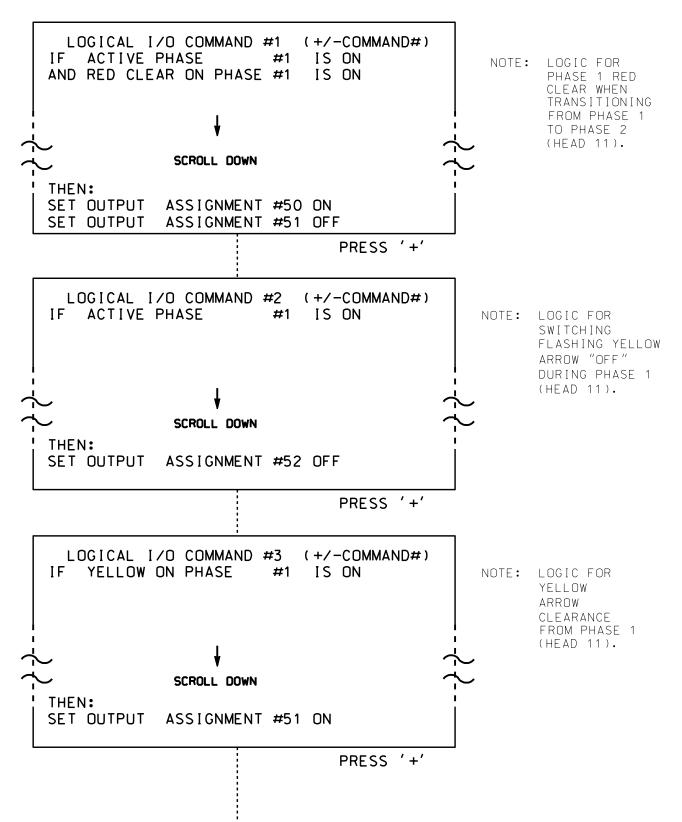
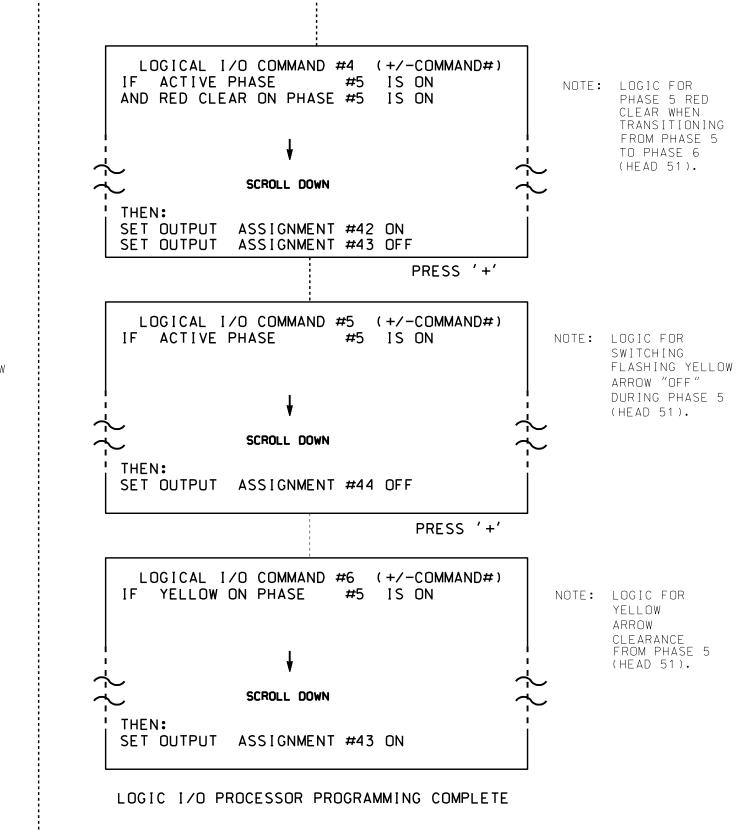
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

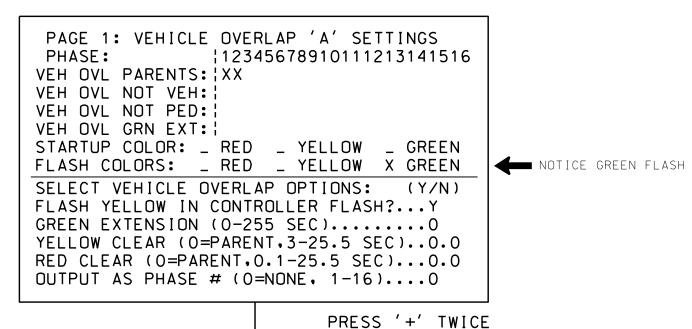


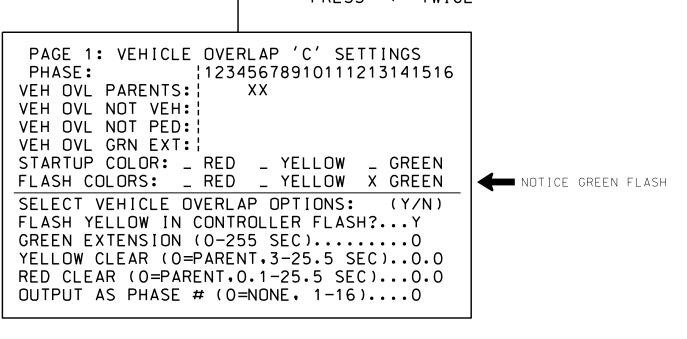


## FOR DEFAULT PHASING (program controller as shown below)

OVERLAP PROGRAMMING DETAIL

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).





OVERLAP PROGRAMMING COMPLETE

## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

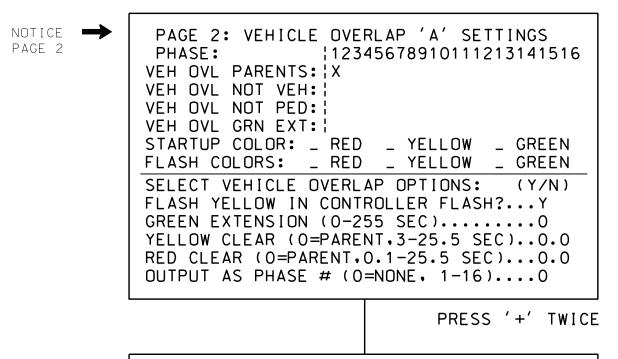
PROJECT REFERENCE NO.

R-3830

Sig 10.2

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



¦12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT PED: : VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)...0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....0

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS

NOTICE

PAGE 2

PHASE:

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-1018t3 DESIGNED: January 2022 SEALED: 4/19/2022 REVISED: N/A

**OUTPUT REFERENCE SCHEDULE** OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

