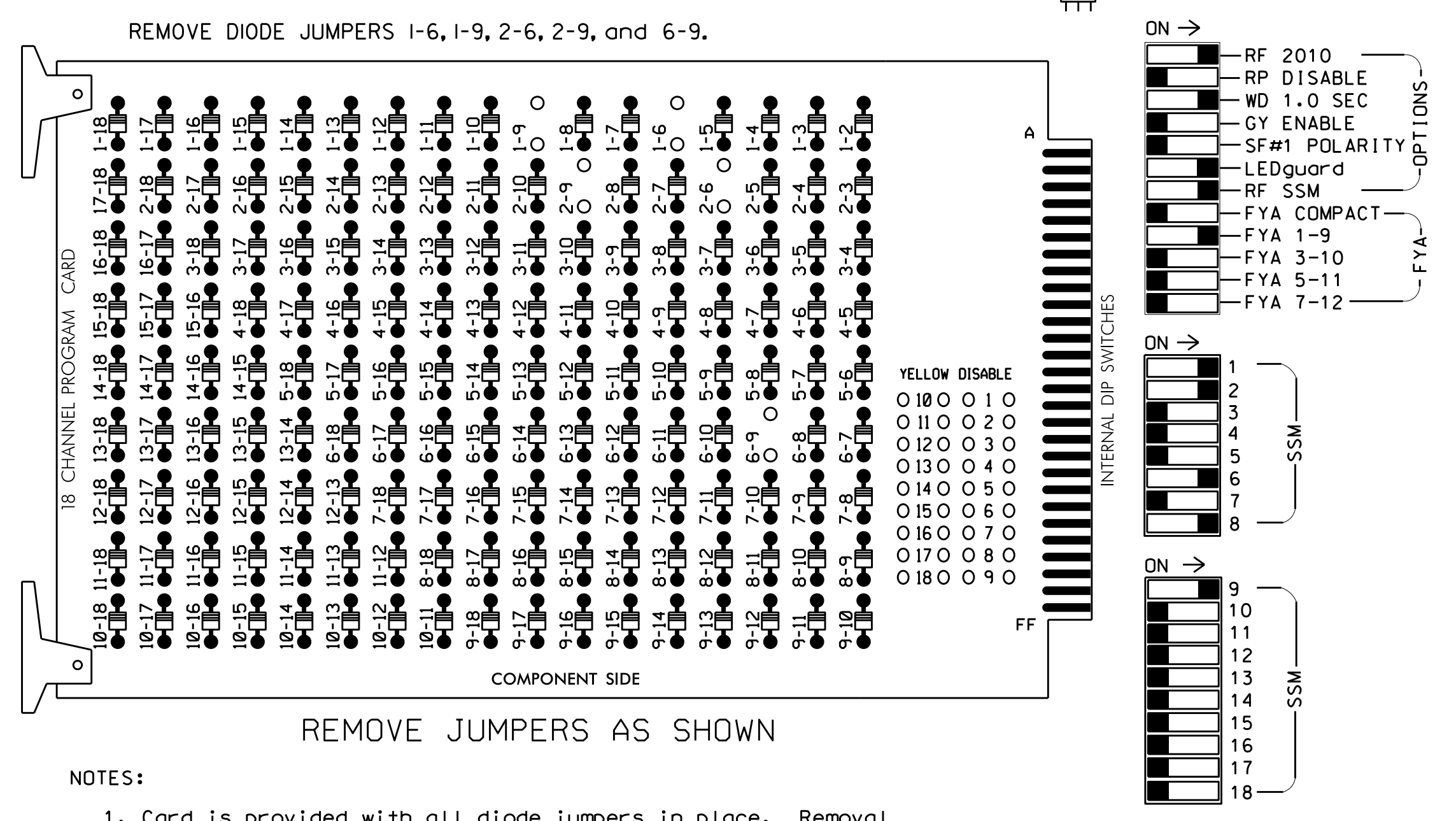


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUXS1
 PHASES USED.....1,2,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "D".....NOT USED

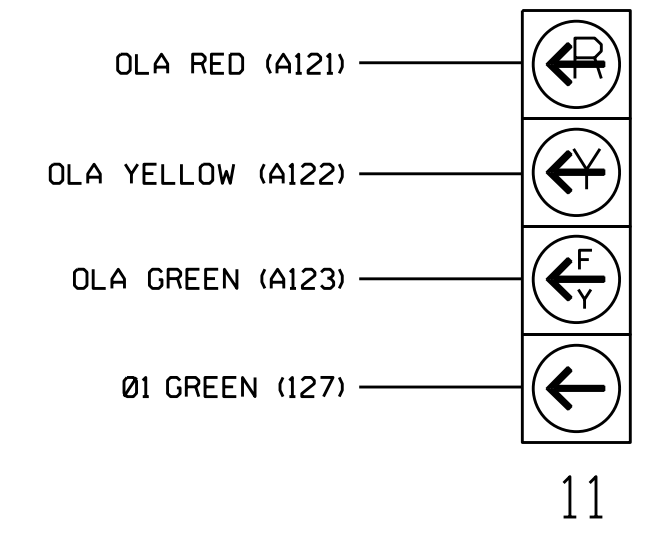
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11★	82	21,22	NU	NU	NU	NU	61,62	NU	NU	81,82	NU	11★	NU	NU	NU	NU	NU	
RED		*	128					134			107								
YELLOW			129					135			108								
GREEN			130					136			109								
RED ARROW																		A121	
YELLOW ARROW			126																A122
FLASHING YELLOW ARROW																			A123
GREEN ARROW	127	127																	

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



NOTE
 The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

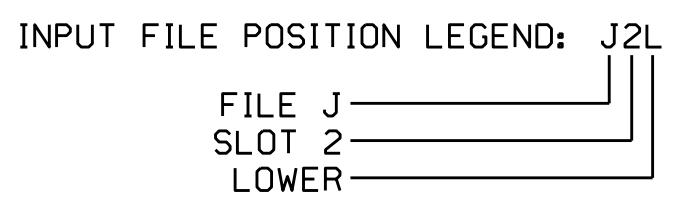
FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A
L	NOT USED	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A
U	S	S	S	S	S	S	S	S	S	S	S	S	S	S
L	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A	1A

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			3

¹Add jumper from I1-W to J4-W, on rear of input file.
 ★ See Input Page Assignment programming details on sheet 3.

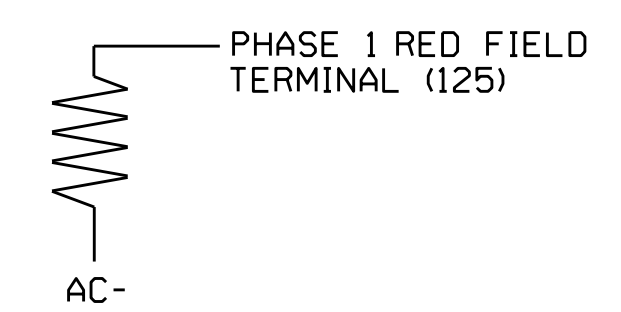


LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



DETECTOR NOTES

- For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loop 1A, detector card placement and slot reserved for wired input are typical for a NCDOT installation. Input associated with this slot are compatible with time of day instructions located on sheet 3 of this electrical detail.

Electrical Detail - Sheet 1 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	NC 42 (Main Street) at SR 1520 (Rosser Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL D. Todd Joyce 04/21/2022
	Division 8 Lee County Sanford	PLAN DATE: April 2022 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS: INIT. DATE	

21-Apr-2022 08:56 S:\IT\5350\115_Signal\work\hous5\g_MarkZafar\plans\081018_temps_and_final\081018-2022\middle-11.dgn ZZZ