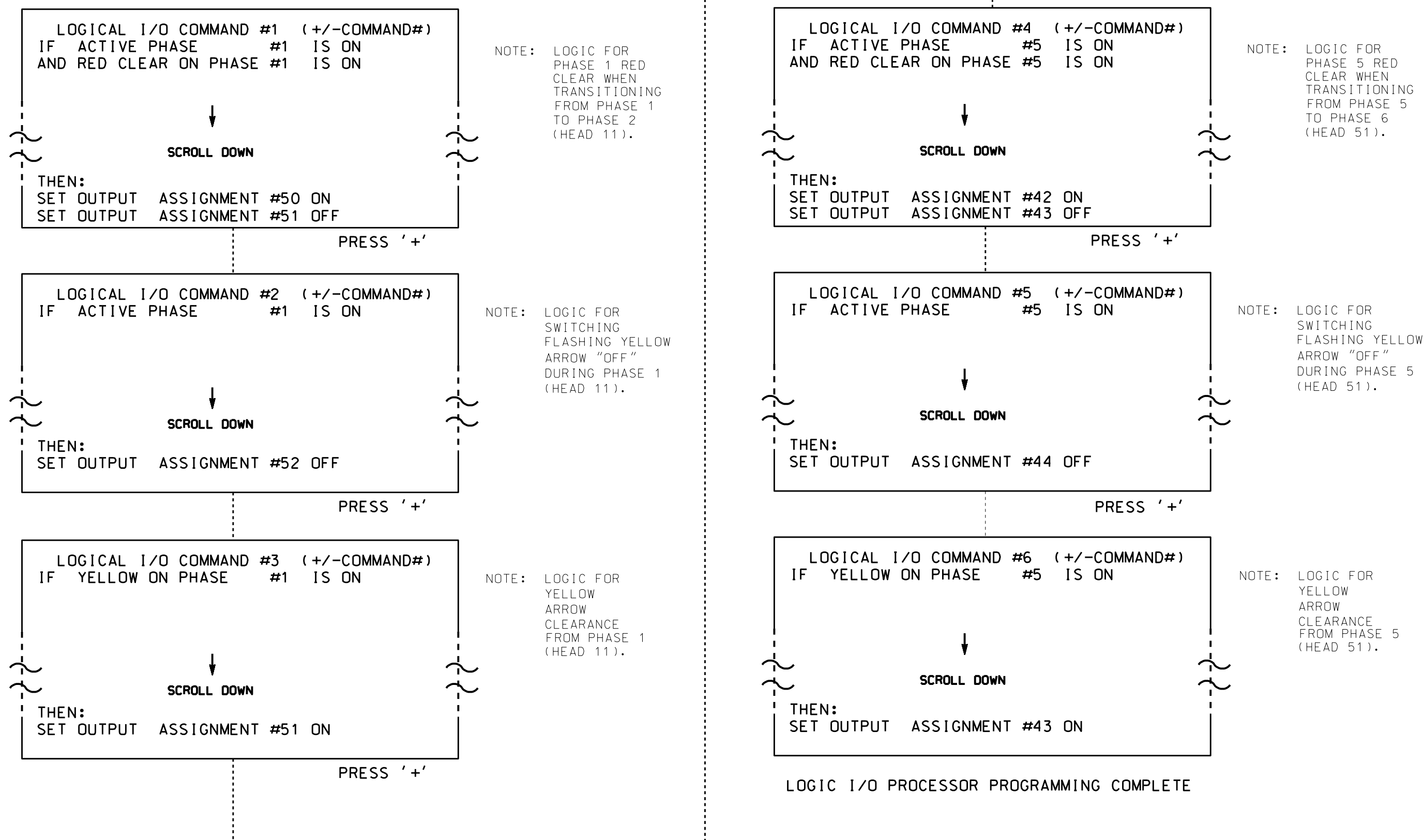


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



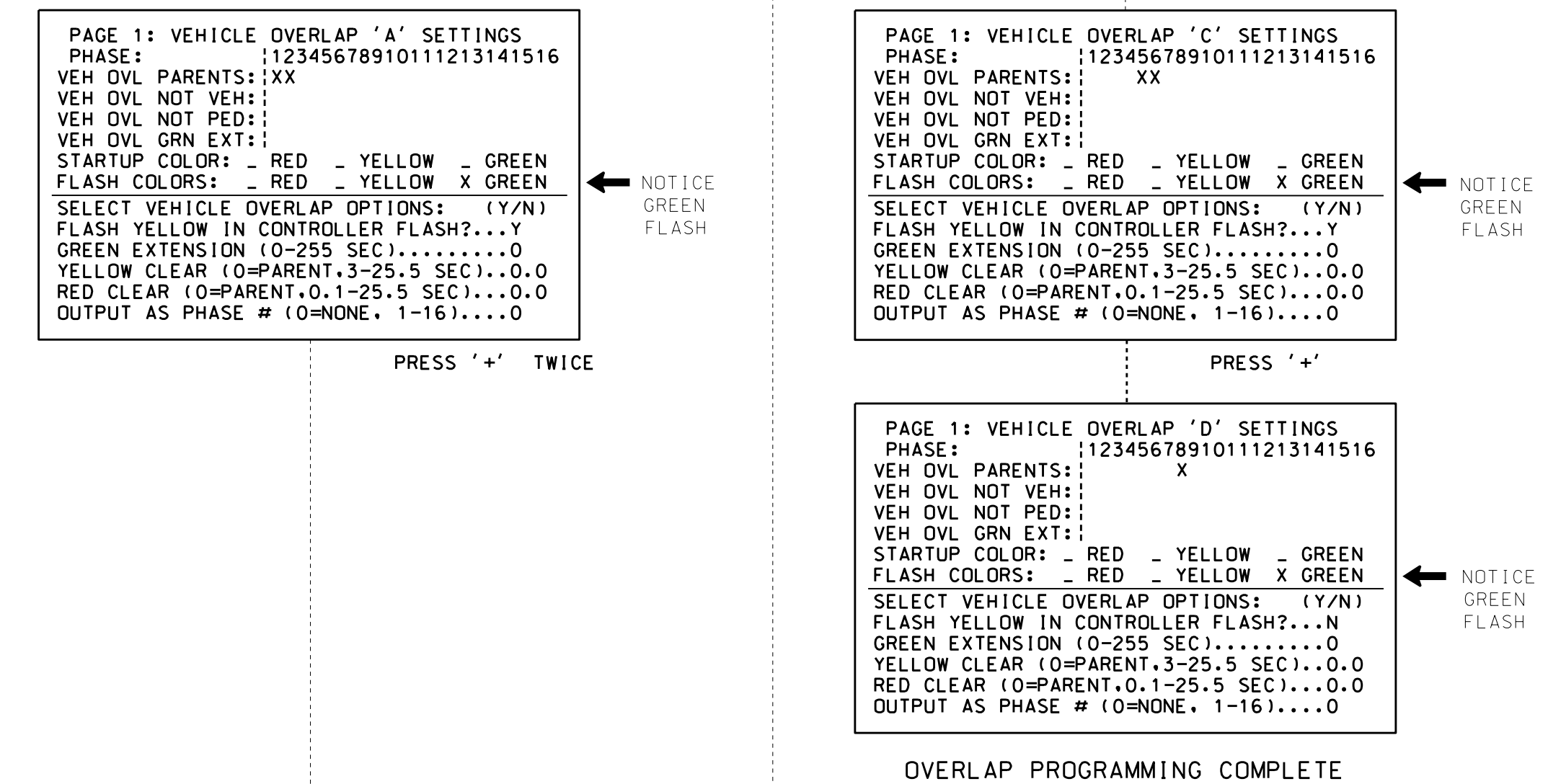
**OUTPUT REFERENCE SCHEDULE**

- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

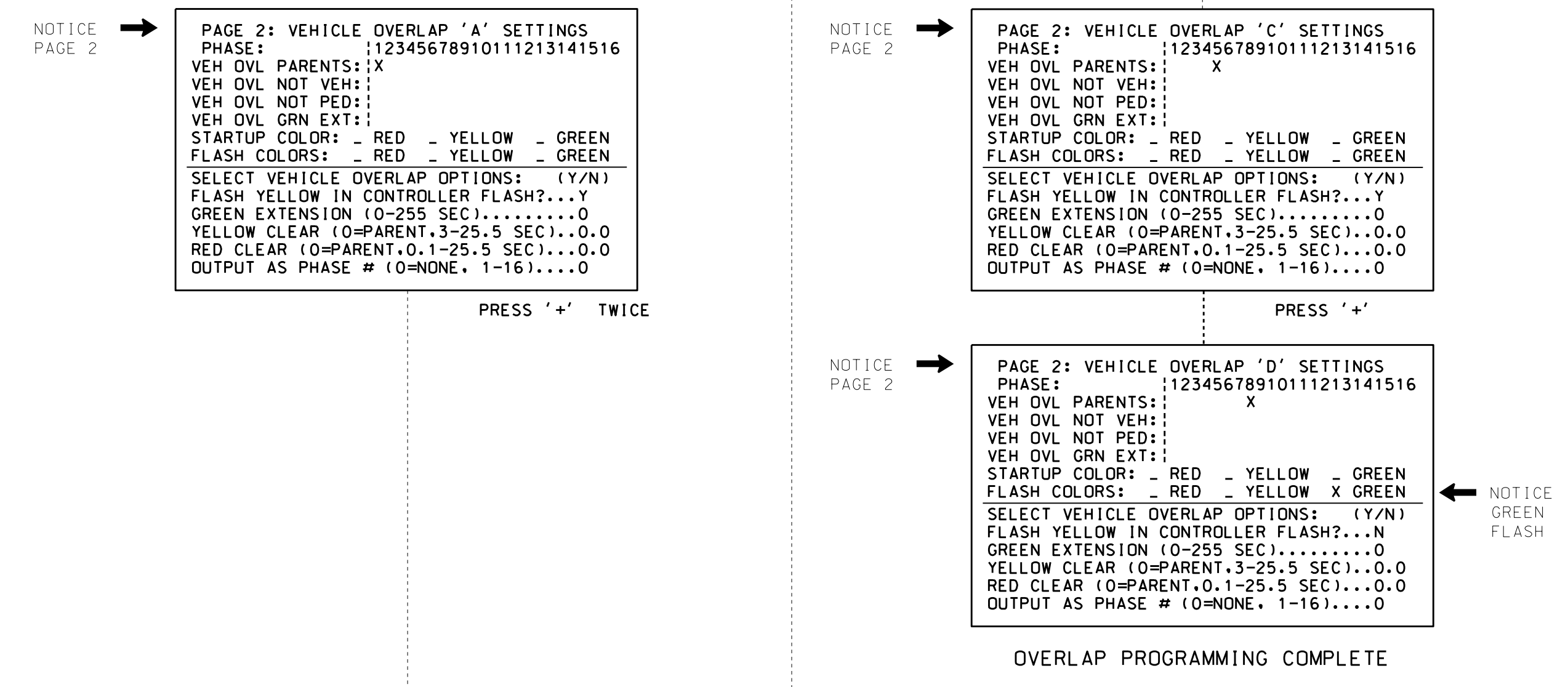
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



**FLASHER CIRCUIT MODIFICATION DETAIL**

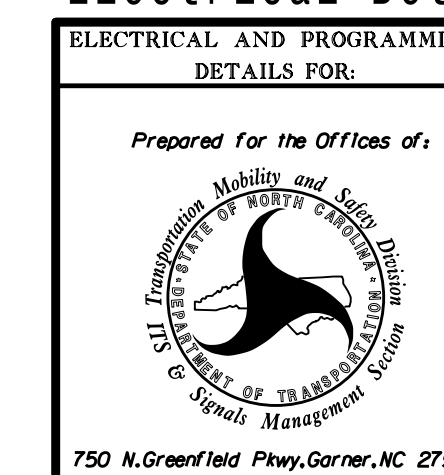
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Temporary Design 2  
Electrical Detail - Sheet 2 of 5

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0236T2  
DESIGNED: March 2022  
SEALED: 4/19/2022  
REVISED: N/A



ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 42 (Main Street) at SR 1519 (Nash Street) / Tyson Foods Driveway	
PLAN DATE:	April 2022	REVIEWED BY:	
PREPARED BY:	Zarrar Zafar	REVIEWED BY:	
REVISIONS	INIT.	DATE	

