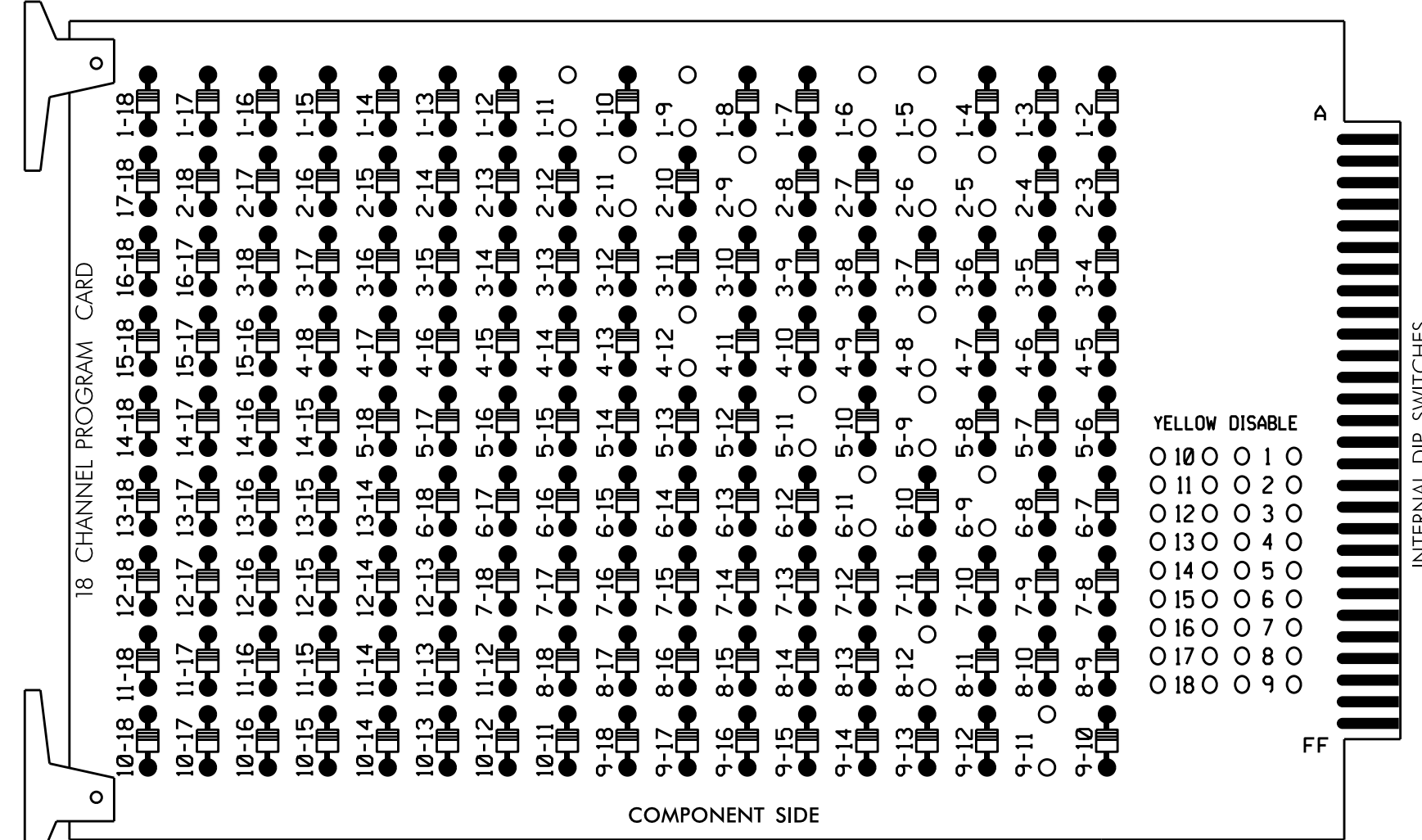


**EDI MODEL 2018ECL-NC CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

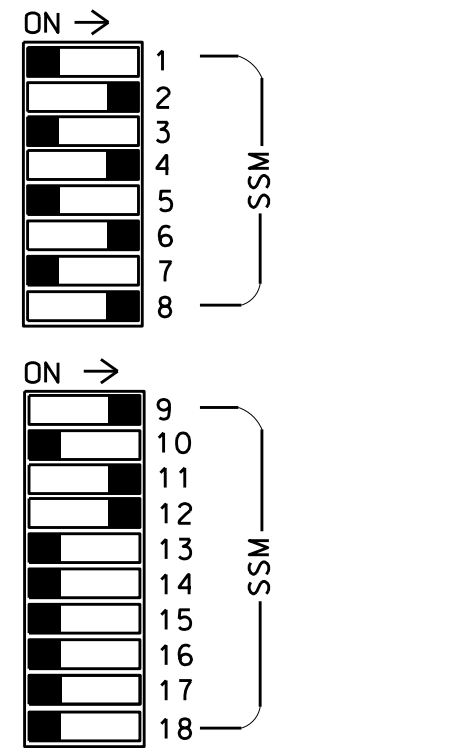
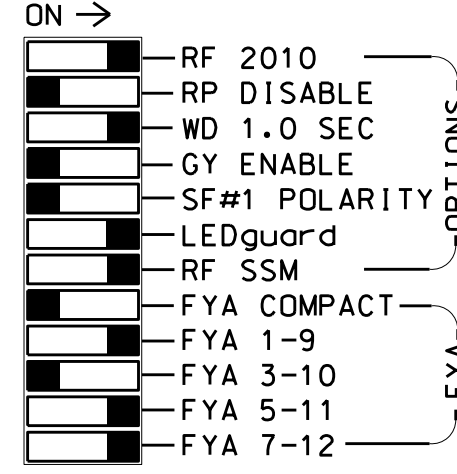
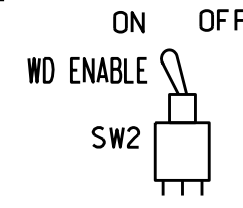
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 4-12, 5-9, 5-11, 6-9, 6-11, 8-12 and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 4 and 8 for Dynamic Max/Max 3.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUXS4,AUXS5  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....8

**SIGNAL HEAD HOOK-UP CHART**

| LOAD SWITCH NO.       | S1  | S2    | S3  | S4 | S5    | S6  | S7 | S8    | S9  | S10 | S11   | S12 | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 |
|-----------------------|-----|-------|-----|----|-------|-----|----|-------|-----|-----|-------|-----|--------|--------|--------|--------|--------|--------|
| CMU CHANNEL NO.       | 1   | 2     | 13  | 3  | 4     | 14  | 5  | 6     | 15  | 7   | 8     | 16  | 9      | 10     | 17     | 11     | 12     | 18     |
| PHASE                 | 1   | 2     | PED | 3  | 4     | PED | 5  | 6     | PED | 7   | 8     | PED | OLA    | OLB    | SPARE  | OLC    | OLD    | SPARE  |
| SIGNAL HEAD NO.       | 11  | 21,22 | NU  | NU | 42,43 | NU  | 51 | 61,62 | NU  | NU  | 81,82 | NU  | 11     | NU     | NU     | 51     | 41     | NU     |
| RED                   |     | 128   |     |    | 101   |     |    | 134   |     |     | 107   |     |        |        |        |        |        |        |
| YELLOW                | *   | 129   |     |    | 102   |     | *  | 135   |     |     | 108   |     |        |        |        |        |        |        |
| GREEN                 |     | 130   |     |    | 103   |     |    | 136   |     |     | 109   |     |        |        |        |        |        |        |
| RED ARROW             |     |       |     |    |       |     |    |       |     |     |       |     | A121   |        |        | A114   | A101   |        |
| YELLOW ARROW          |     |       |     |    |       |     |    |       |     |     |       |     | A122   |        |        | A115   | A102   |        |
| FLASHING YELLOW ARROW |     |       |     |    |       |     |    |       |     |     |       |     | A123   |        |        | A116   | A103   |        |
| GREEN ARROW           | 127 |       |     |    |       |     |    | 133   |     |     |       |     |        |        |        |        |        |        |

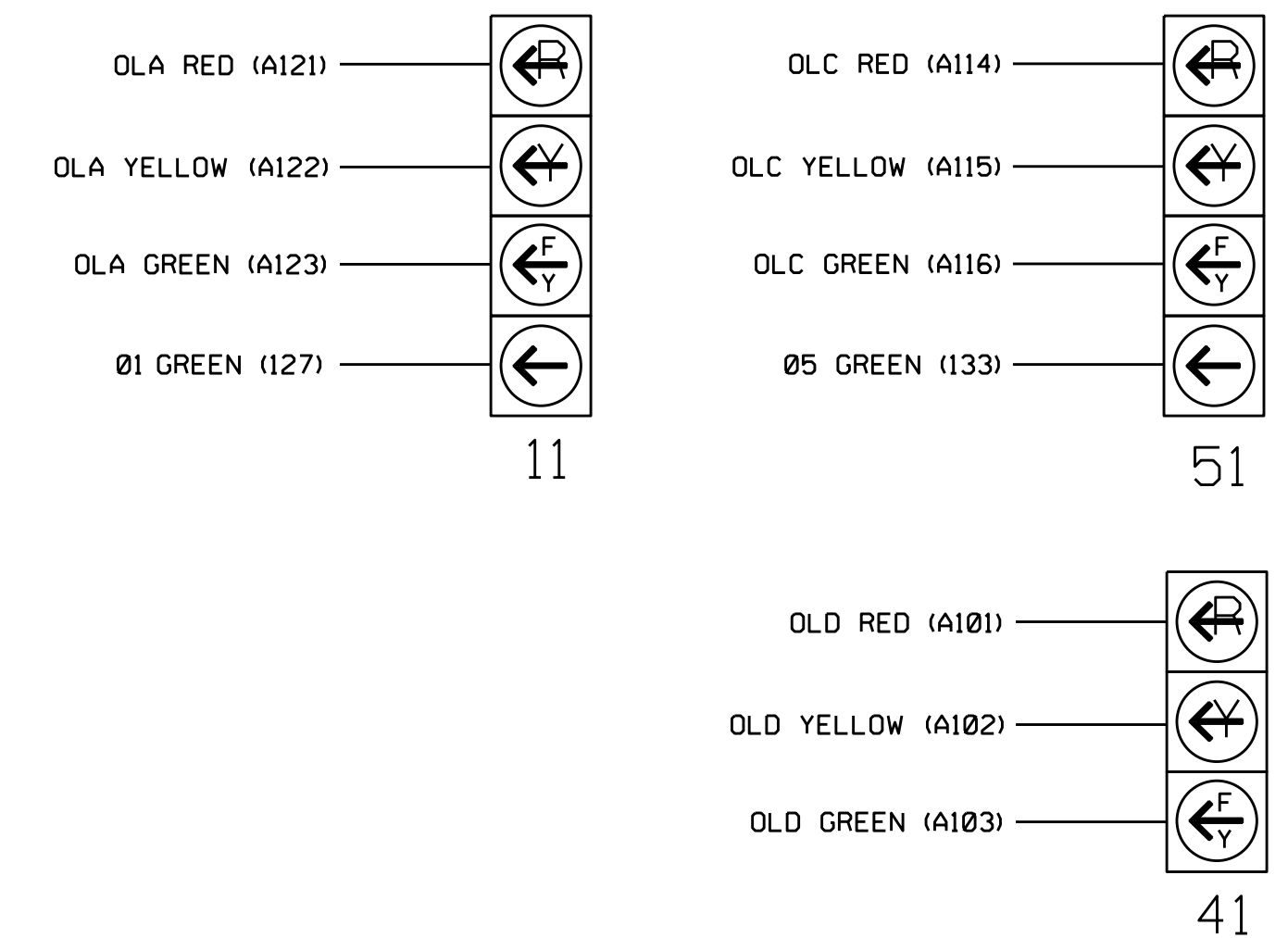
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

The sequence display for signal heads 11 and 51 require special logic programming. See sheet 2 for programming instructions.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

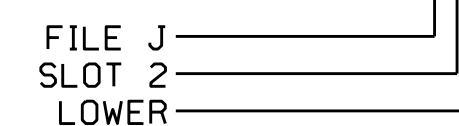
| LOOP NO.        | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|-----------------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 1A <sup>1</sup> | TB2-1,2       | I1U             | 56      | 18                   | 1            | 1          | Y    | Y      |                 |              | 15         |
|                 | -             | J4U             | 48      | 10★                  | 26           | 6          | Y    | Y      |                 |              |            |
| 5A <sup>2</sup> | TB3-1,2       | J1U             | 55      | 17                   | 5            | 5          | Y    | Y      |                 |              | 15         |
|                 | -             | I4U             | 47      | 9★                   | 22           | 2          | Y    | Y      |                 |              |            |
| -               | -             | J1U             | 55      | 17★                  | 55           | 5          | Y    | Y      |                 |              |            |

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

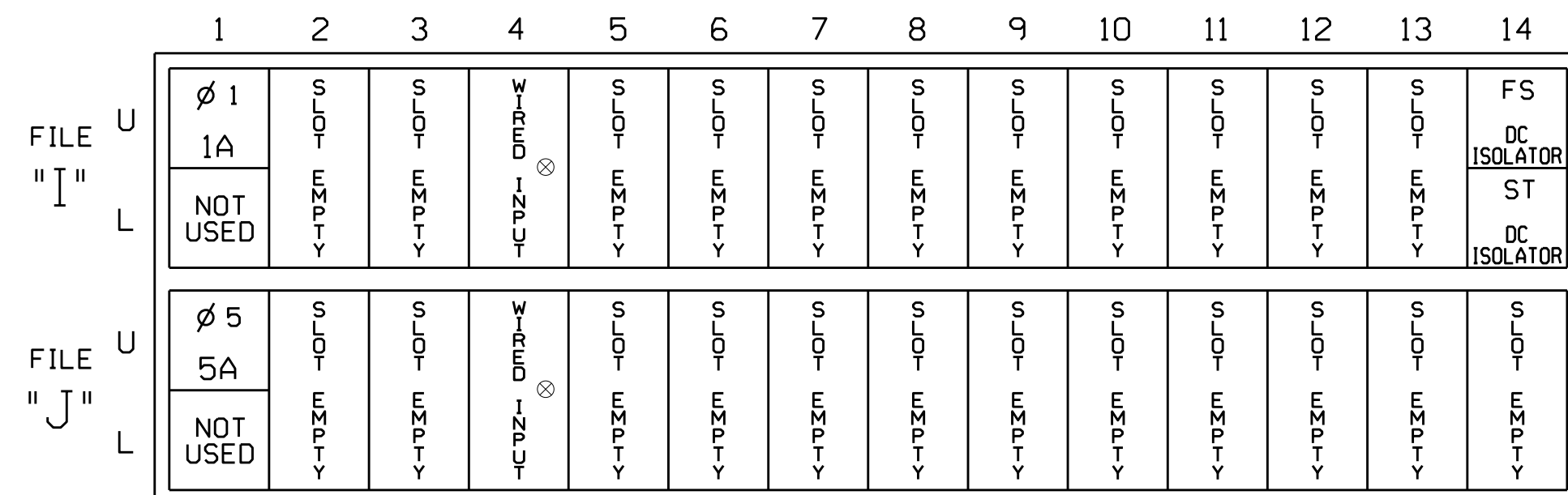
★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND: J2L**



**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

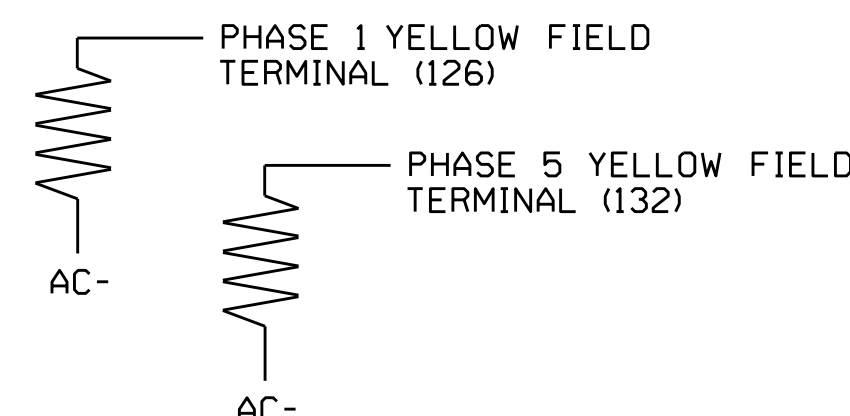
⊗ Wired Input - Do not populate slot with detector card

FS = FLASH SENSE  
 ST = STOP TIME

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

| VALUE (ohms) | WATTAGE   |
|--------------|-----------|
| 1.5K - 1.9K  | 25W (min) |
| 2.0K - 3.0K  | 10W (min) |



**DETECTOR NOTES**

- For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For loops 1A and 5A, detector card placement and slot reserved for wired input are typical for a NCDOT installation. Input associated with these slots are compatible with time of day instructions located on sheet 3 and 4 of this electrical detail.

Temporary Design 2  
 Electrical Detail - Sheet 1 of 5

|   |  |   |  |
|---|--|---|--|
| ELECTRICAL AND PROGRAMMING DETAILS FOR:<br>Prepared for the Offices of:<br>750 N. Greenfield Pkwy, Garner, NC 27529 | NC 42 (Main Street)<br>at<br>SR 1519 (Nash Street) /<br>Tyson Foods Driveway<br>Lee County Sanford |   | DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED<br>SEAL<br>D. Todd Joyce<br>04/20/2022 |
|   | Division 8<br>PLAN DATE: April 2022<br>PREPARED BY: Zarrar Zafar                                   | REVIEWED BY:<br>REVIEWED BY:<br>REVISIONS<br>INIT. DATE |  |