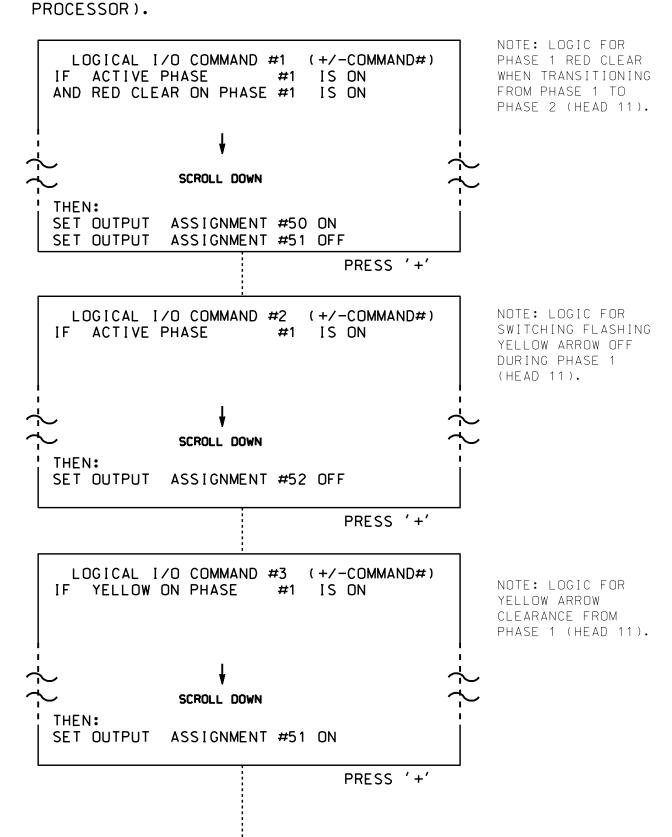
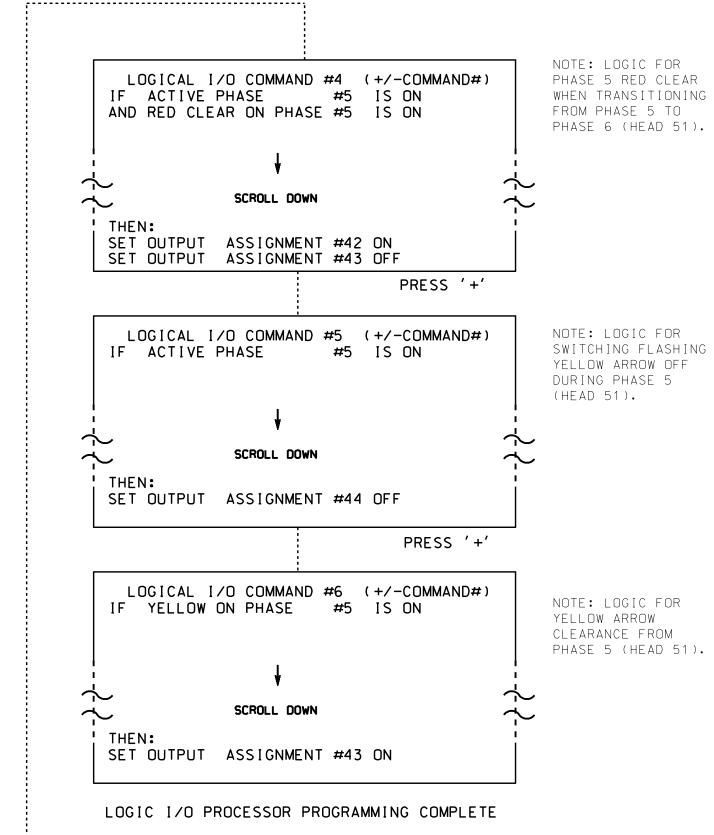
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O





OUTPUT REFERENCE SCHEDULE

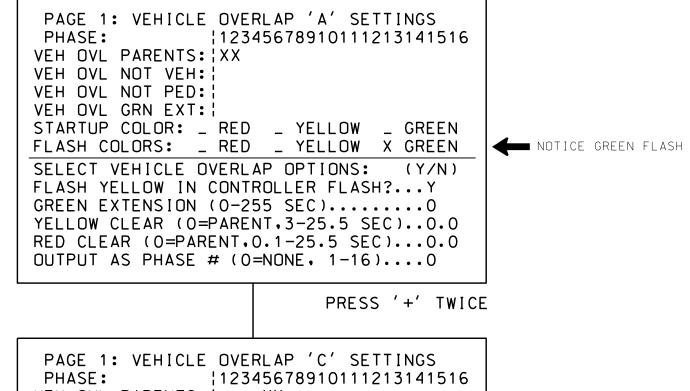
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

R-3830 Sig-3.2

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



PHASE: | 12345678910111213141516

VEH OVL PARENTS: | XX

VEH OVL NOT VEH: |

VEH OVL NOT PED: |

VEH OVL GRN EXT: |

STARTUP COLOR: _ RED _ YELLOW _ GREEN

FLASH COLORS: _ RED _ YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC)......0

YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0

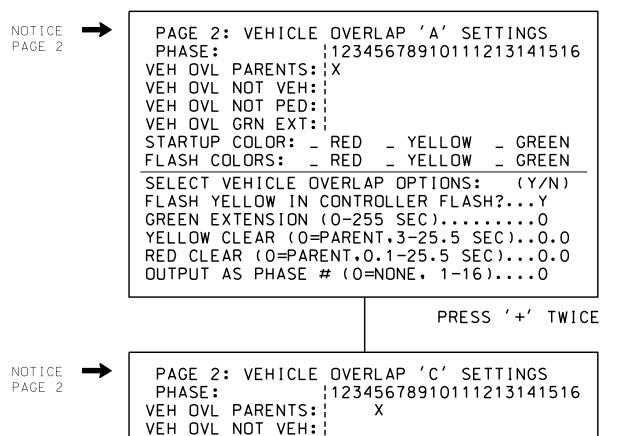
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS).
THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS 'NEXT' TO ADVANCE TO PAGE 2.



VEH OVL NOT PED: :

VEH OVL GRN EXT: :

OVERLAP PROGRAMMING COMPLETE

STARTUP COLOR: _ RED _ YELLOW _ GREEN

FLASH COLORS: _ RED _ YELLOW _ GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

YELLOW CLEAR (O=PARENT,3-25.5 SEC)...0.0

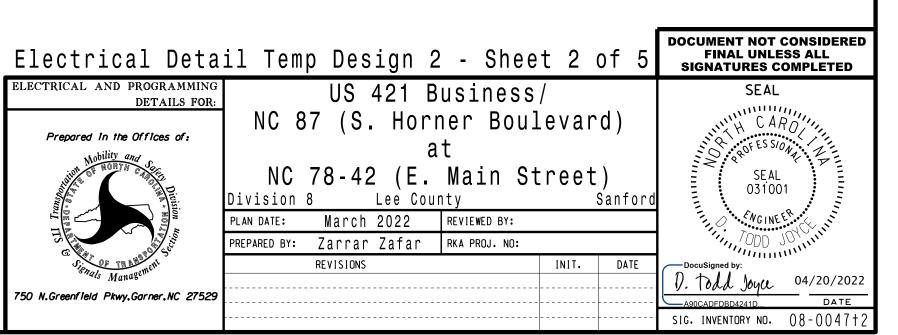
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....

OUTPUT AS PHASE # (O=NONE, 1-16)....0

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 08-0047t2
DESIGNED: February 2022
SEALED: 4/19/2022
REVISED: N/A



20-AFK-2022 10:52 .*080047_sm_ele_2022mmdd-t2.dgn ~~~~~