

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

*OVERLAP A*

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
        
```

Toggle Once

*OVERLAP B*

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[B] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . . . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
        
```

Toggle Once

*OVERLAP C*

Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
        
```

END PROGRAMMING

## FLASHER CIRCUIT MODIFICATION DETAIL

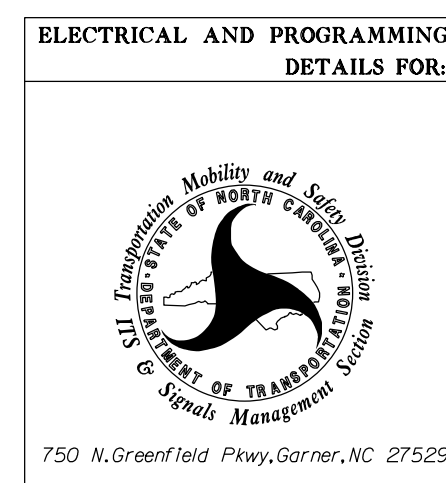
In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 14-0631T1  
DESIGNED: May 2022  
SEALED: 05/10/2022  
REVISED:

Electrical Detail-Temporary Design 1(Phase I)-Sheet 2 of 2



<b>NC 143</b>	
at	
<b>SR 1275 (Five Points Road)/ Robbinsville High School</b>	
Division 14	Graham County Robbinsville
PLAN DATE: May 2022	REVIEWED BY: M. L. Stygles
PREPARED BY: J. Ma	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED

