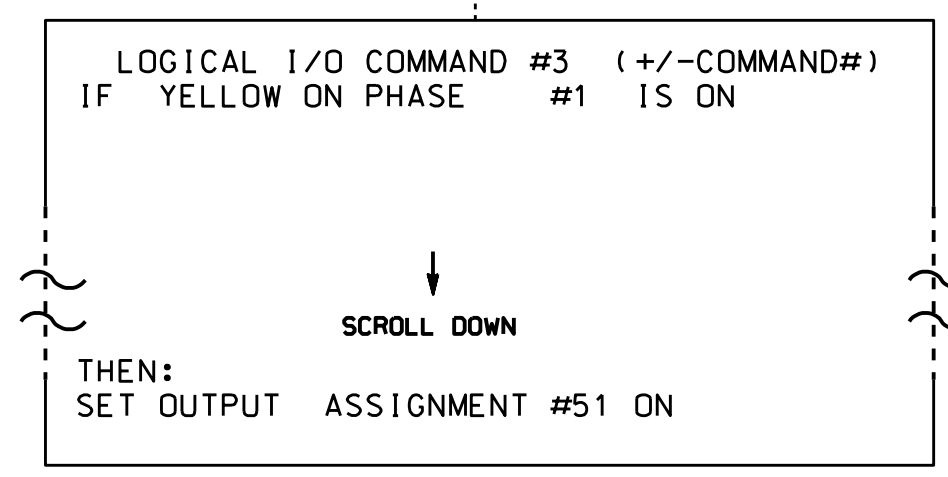
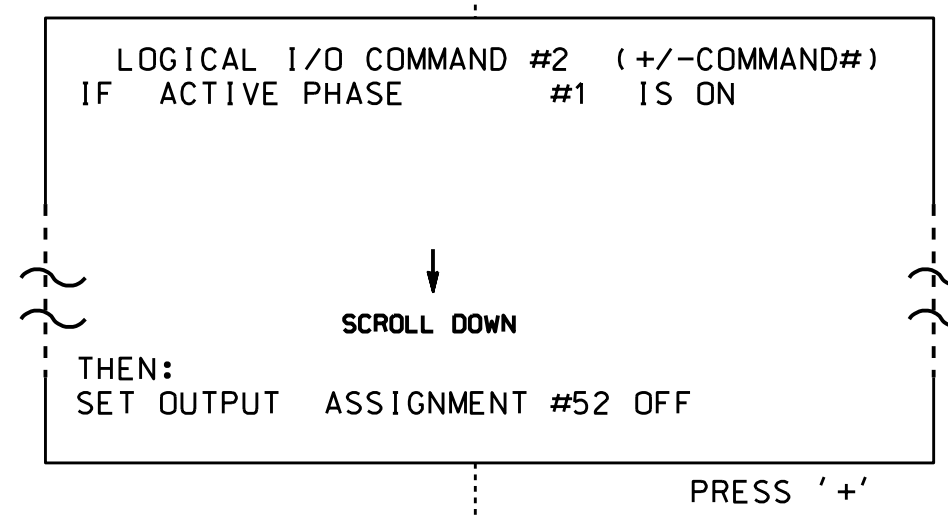
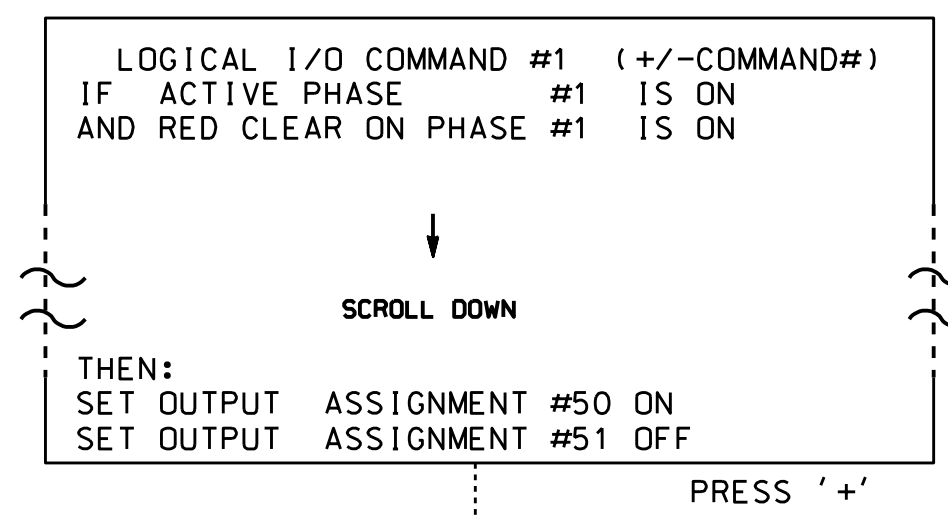


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

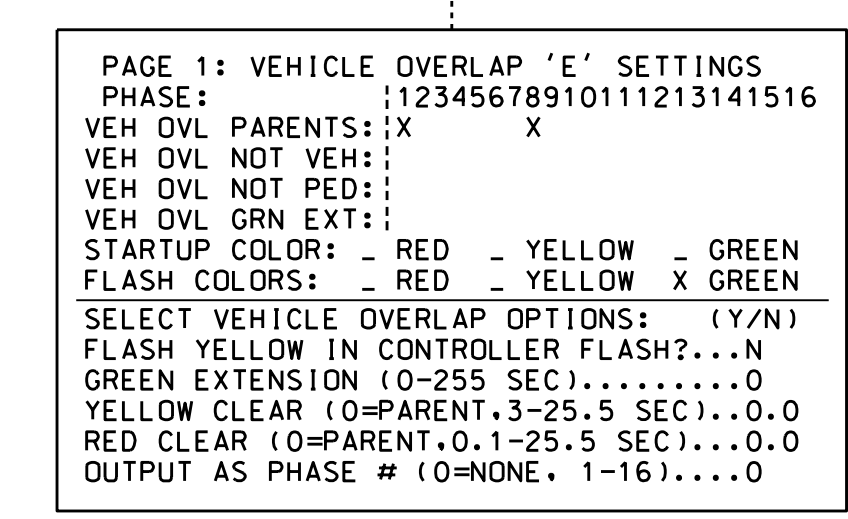
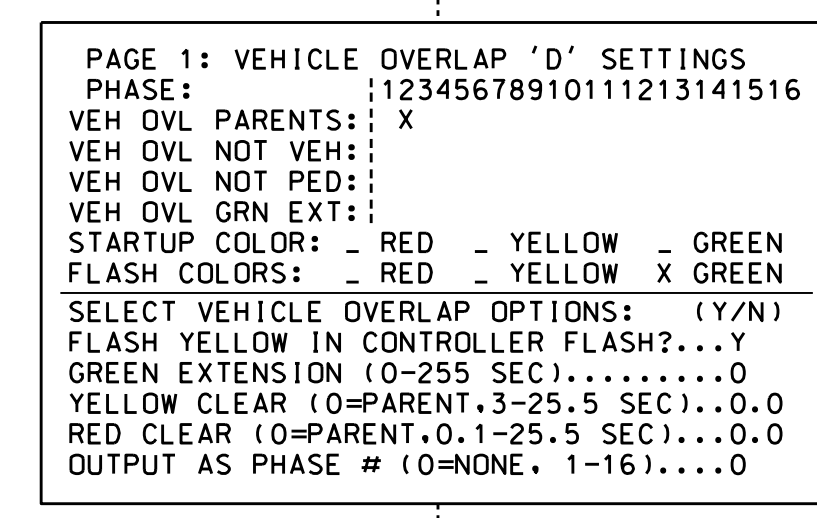
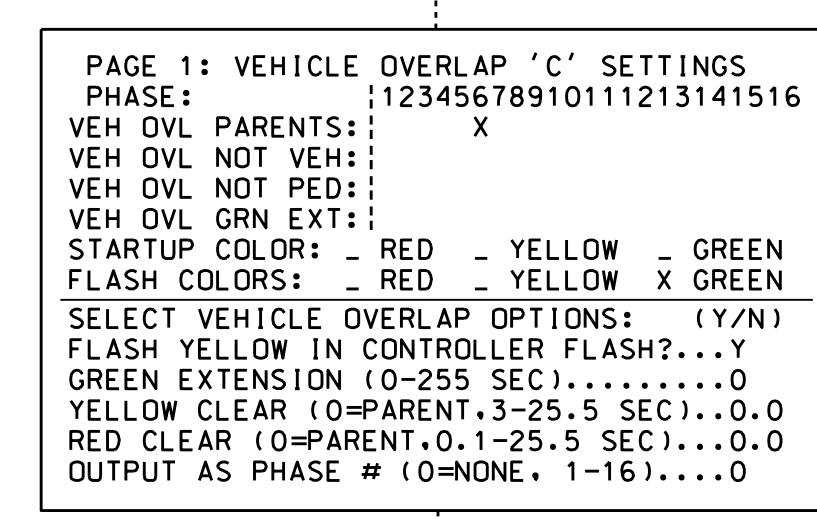
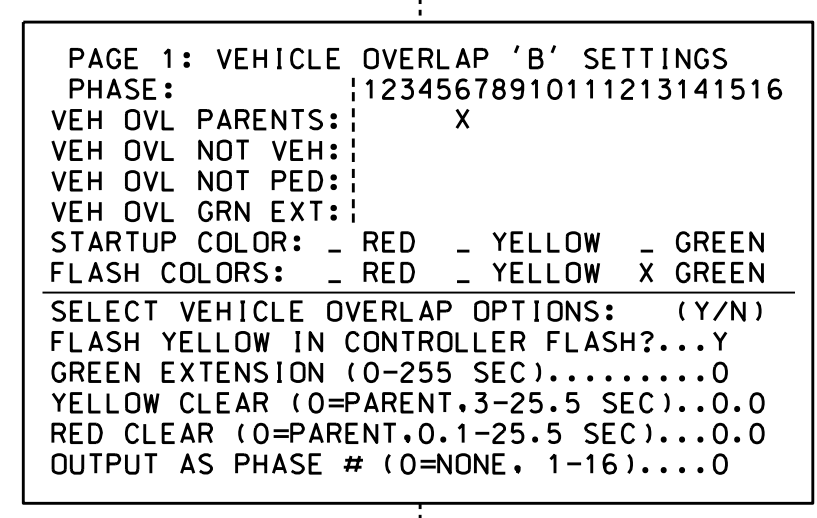
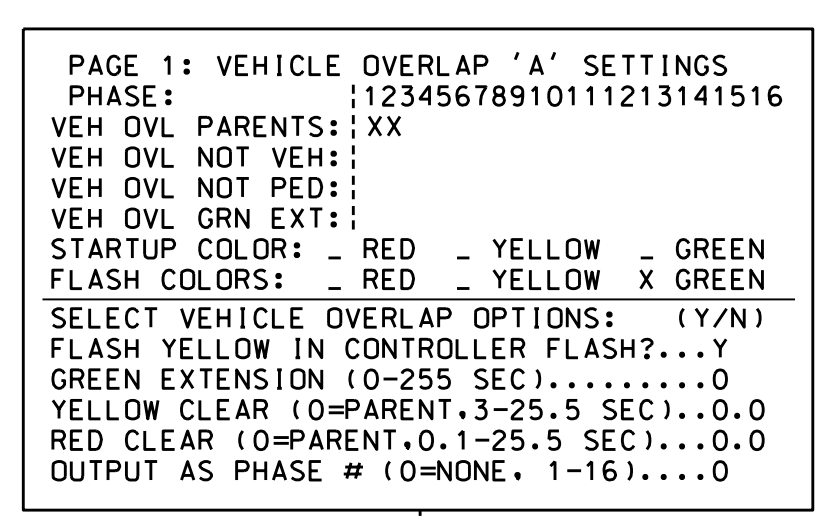
OUTPUT REFERENCE SCHEDULE

- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

3/29/2022 15:43 W:\1449\3-shs-ef-2022\mtds.dgn zzzzgr

Electrical Detail - Sheet 2 of 2

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1449
DESIGNED: January 2022
SEALED: 3/29/2022
REVISED: N/A

Electrical and Programming Details for:
Prepared In the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1560 (-L-) at SR 1563 (-Y1-) / Future Road By Others	
Division 4	Johnston County Clayton
PLAN DATE: March 2022	REVIEWED BY:
PREPARED BY: Zarrar Zafar	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
STATE OF NORTH CAROLINA
PROFESSIONAL ENGINEER
SEAL 031001
TODD JOYCE
DESIGNED BY: D. Todd Joyce
DATE: 03/30/2022
SIC. INVENTORY NO. 04-1449