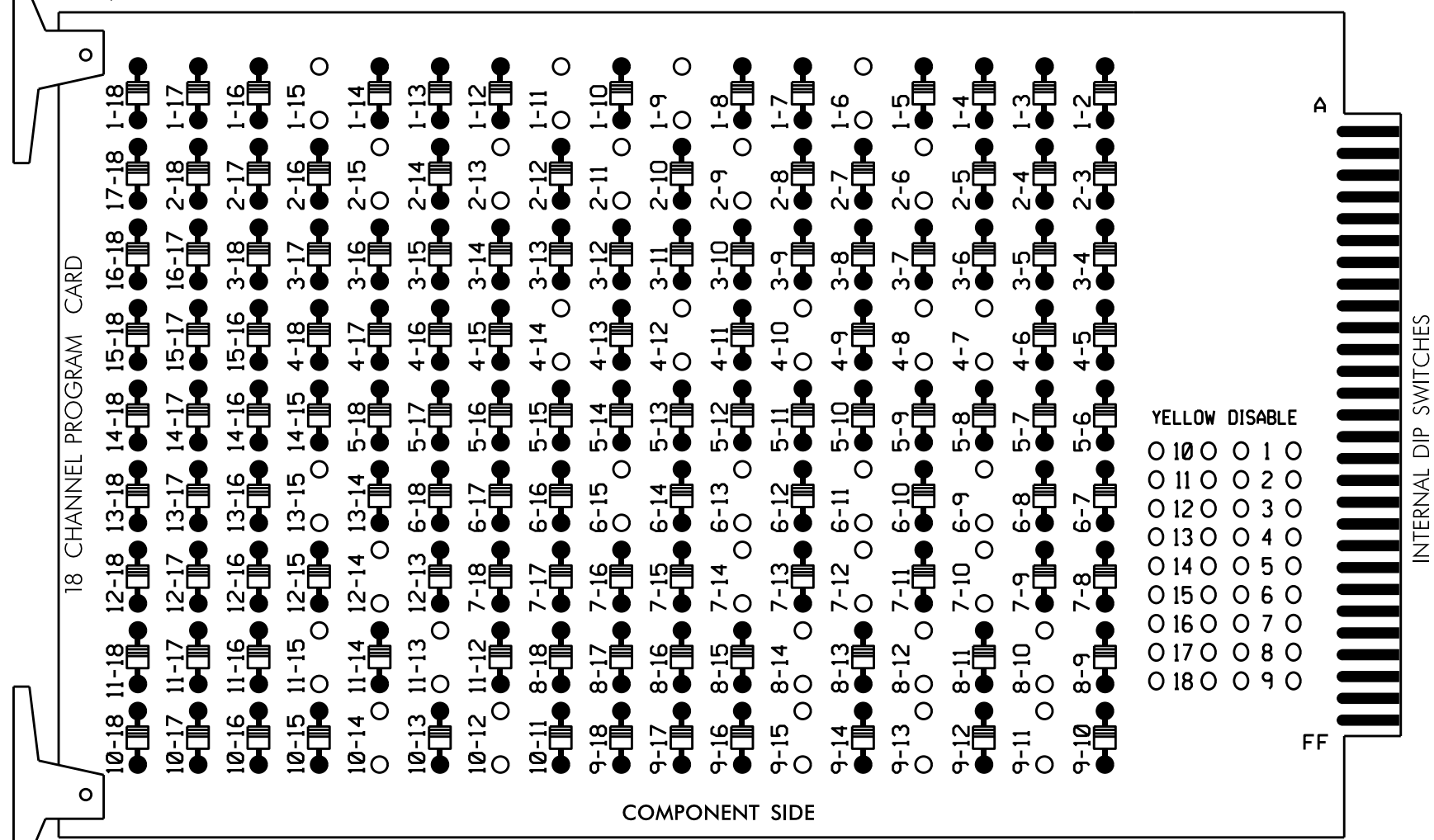


EDI MODEL 2018ECL-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 1-15, 2-6, 2-9, 2-11, 2-13, 2-15, 4-7, 4-8, 4-10, 4-12, 4-14, 6-9, 6-11, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 9-11, 9-13, 9-15, 10-12, 10-14, 11-13, 11-15, 12-14 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4 and 6 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Salisbury Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S8,S9,S10,S11,  
 AUX S1,AUX S2,AUX S4,AUX S5  
 PHASES USED.....1,2,2 PED,4,4 PED,6,6 PED,7,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....4  
 OVERLAP "C".....6  
 OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	22,23	P21, P22	NU	41,42	P41, P42	NU	62,63	P61, P62	63	71	81,82	NU	11	83	NU	21	71
RED		128			101			134	*		107							
YELLOW	*	129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW										123			A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127									124	124							
Hand				113		104			119									
Walking Person				115		106			121									

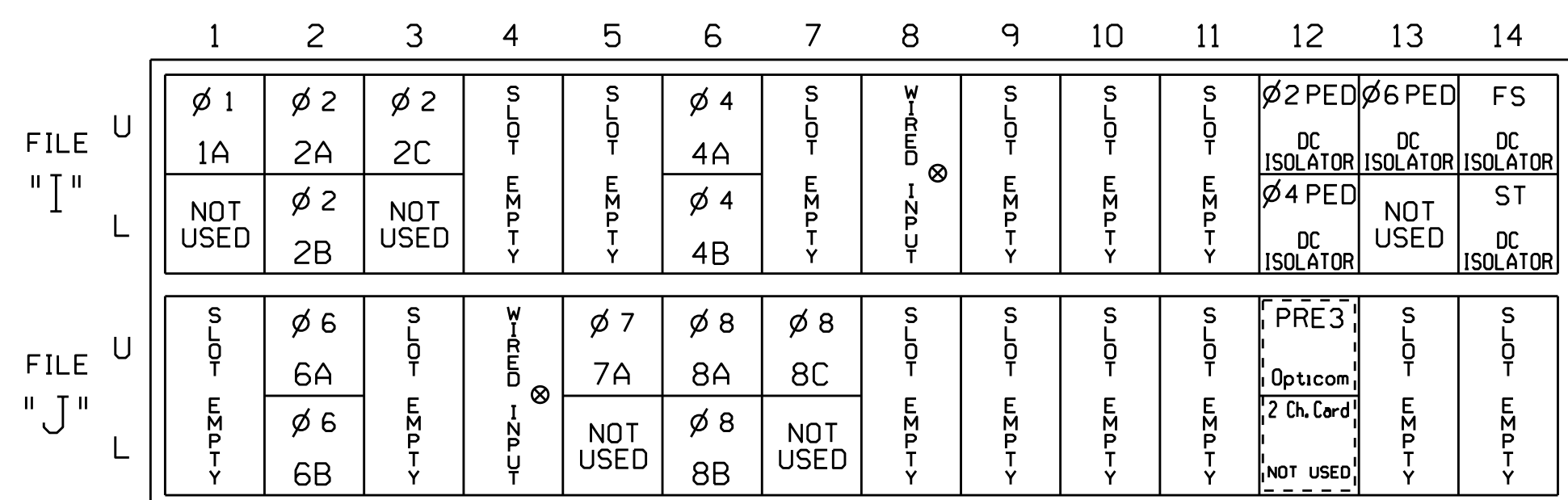
NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail sheet 2.  
 ★ See pictorial of head wiring in detail below.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

INPUT FILE POSITION LAYOUT

(front view)



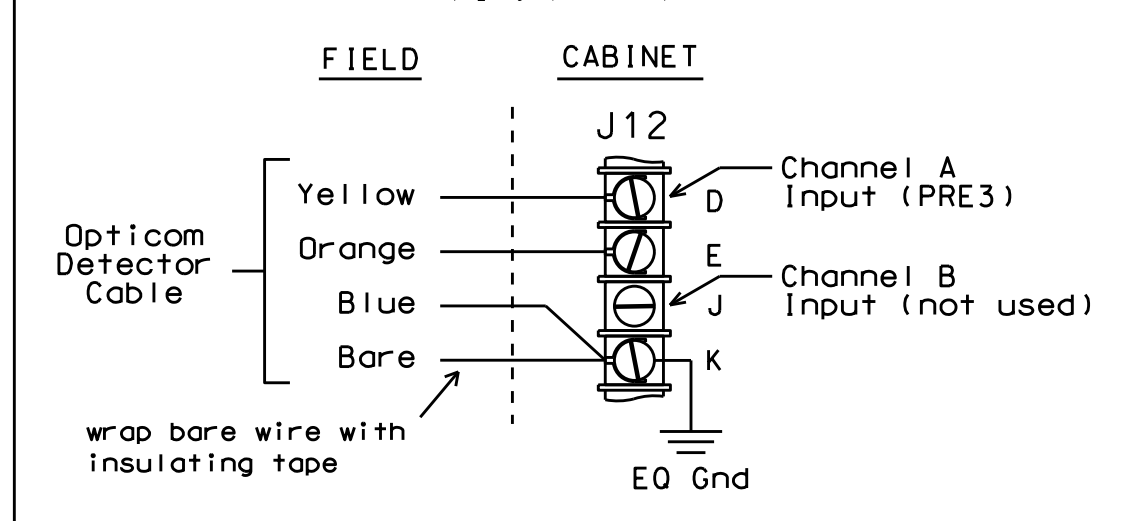
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

⊗ Wired Input - Do not populate slot with detector card

TYPICAL OPTICOM FIELD WIRE DETAIL

(input file, rear view)



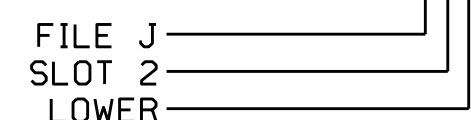
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	Y	Y				
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A <sup>2</sup>	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11★	24	4	Y	Y	Y		3
	-	J5U	57	19★	57	7	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
8C	TB7-1,2	J7U	66	28	38	8	Y	Y			15
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:  
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
  - Add jumper from J5-W to I8-W, on rear of input file.
- ★ See Input Page Assignment programming details on sheets 3 and 4.

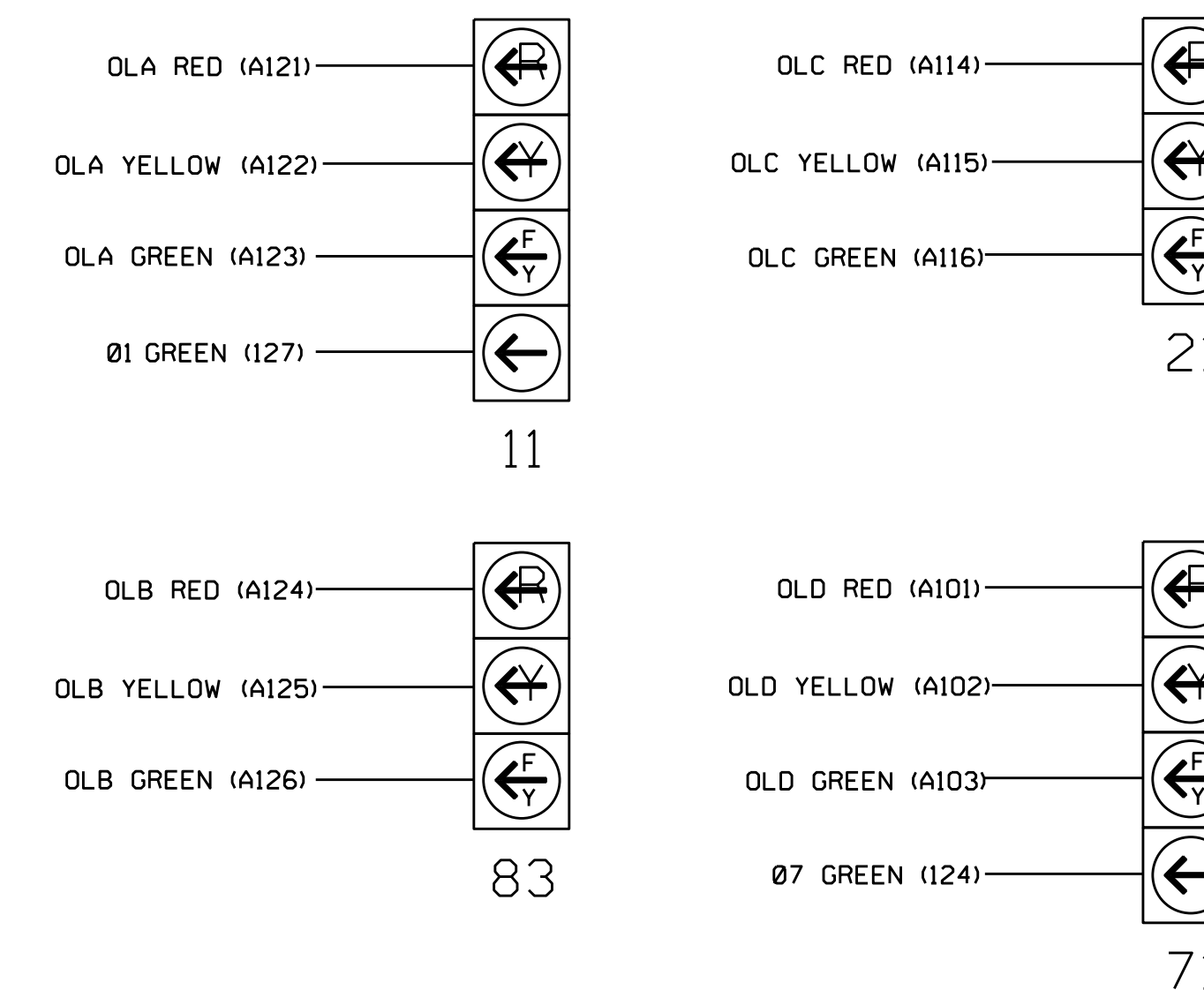
INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1068  
 DESIGNED: September 2021  
 SEALED: 9/30/2021  
 REVISED:

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- The sequence display for signal heads 11 and 71 require special logic programming. See sheet 2 for programming instructions.

Electrical Detail - Sheet 1 of 5

Electrical and Programming Details for: US 70 (Statesville Blvd.) at SR 1722 (Majolica Road) and Ashbrook Road

Prepared In the Offices of: *Transitional Mobility and Safety Solutions*

Division 9 Rowan County Salisbury

PLAN DATE: September 2021 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS: \_\_\_\_\_ INIT. DATE

DocuSigned by: *D. Todd Joyce* 9/30/2021

SIG. INVENTORY NO. 09-1068

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL: *Professional Engineer D. Todd Joyce* SEAL 031001