

)21 -Design#03-1153_sm_ele4.dar

G DETAIL			PROJECT REFERENCE NO. U-5710A	SHEET NO. Sig-3.4
<u>511 511</u>				
	DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS 'VEHICLE OVERLAP' AS SHOWN BELOW.	ALTERNATE PHASING ACTIVATION DETAIL		
	PAGE:1 C1 PIN:21 VEHICLE OVERLAP OUTPUT ASSIGNMENT #19 FREQUENCY (O=DEFAULT) (O-25.5 HZ)0.0 DUTY CYCLE (O=DEFAULT) (O - 100%)0 MODE (O=SOLID.1=FLASH)1 SELECT ASSIGNMENT:	TO RUN ALT. PHASING DURING <u>FREE RUN</u> - PROGRAM CHANGES (SHOWN BELOW) IN SEPARATE T EVENTS. IF PAGE 1 IS USED. NO EVENT PROGRAM FOR THAT PARTICULAR PAGE.	IME OF DAY MING IS NECES	SSARY
LT. GED.	NOT ENABLED. VEHICLE PHASE. PEDESTRIAN PHASE. VEHICLE OVERLAP. VEHICLE OVERLAP. WATCHDOG. DETECTOR RESET.	TO RUN ALT. PHASING DURING <u>COORDINATION</u> - SELECT ALL PAGE CHANGES (AS SHOWN BELOW WITHIN COORDINATION PLAN PROGRAMMING.	1)	
	ADVANCE BEACON OUT OF PHASE FLASHER CONTROLLER FLASH RUN FREE	PHASING OVERLAPS	S PAGE	
ERLAP'	RESERVED.PREEMPT.SOFT PREEMPT.ANY PREEMPT.COORDINATION PLAN.OFFSET.PHASE CHECK.PHASE ON.PHASE NEXT.	ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> 1 ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u> 2	1 2	
LT. GED. .1 ERLAP'	DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS 'VEHICLE OVERLAP' AS SHOWN BELOW. PAGE:1 C1 PIN:22 VEHICLE OVERLAP OUTPUT ASSIGNMENT #20 FREQUENCY (0=DEFAULT) (0-25.5 HZ)0.0 DUTY CYCLE (0=DEFAULT) (0 - 100%)0 MODE (0=SOLID.1=FLASH)1 SELECT ASSIGNMENT: NOT ENABLED	ALTERNATE PHASING CHANGE SUMMARY IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERA EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDUL RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VE ALTERNATE PHASING CHANGE SUMMARY THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BIT 1 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING": DVERLAPS PAGE 2: Modifies overlap parent phase for heads 31 and 32 to run protected turns only. INPUTS PAGE 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.	ATE TIME OF D. ER. (EX. FREE ERSA).	Υ Ξ
_T. GED.	DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS 'VEHICLE OVERLAP' AS SHOWN BELOW. PAGE:1 C1 PIN:23 VEHICLE OVERLAP OUTPUT ASSIGNMENT #21 FREQUENCY (O=DEFAULT) (O-25.5 HZ)0.0 DUTY CYCLE (O=DEFAULT) (O - 100%)0 MODE (O=SOLID,1=FLASH)1 SELECT ASSIGNMENT: NOT ENABLED VEHICLE PHASE VEHICLE OVERLAPY PEDESTRIAN OVERLAP	THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1153 DESIGNED: Nov 2021 SEALED: 11/08/2021 REVISED: N/A		
:7 :2	WATCHDOG DETECTOR RESET ADVANCE BEACON OUT OF PHASE FLASHER CONTROLLER FLASH RUN FREE RESERVED	Electrical Detail Sheet 4 of 4 ELECTRICAL AND PROGRAMMING DETAILS FOR: US 74 (Eastwood Road)	DOCUMENT NOT O FINAL UNLES SIGNATURES CO SEAL	CONSIDERED SS ALL OMPLETED
ERLAP'	PREEMPT SOFT PREEMPT ANY PREEMPT COORDINATION PLAN	Division 3 New Hanover County Wilm:	ington	
)ΑΤΑ.	PHASE CHECK PHASE ON PHASE ON PHASE NEXT OUTPUT PROGRAMMING COMPLETE	RAMEY KEMP ASSOCIATES S808 Faringdon Place Raleigh, North Carolina 27609 Phone: 919-872-5115 www.rameykemp.com NC License No. C-0910	ton do) DATE DATE Docusigned by Docusigned by Docusigned by Docusigned by Docusigned by Docusigned by Docusigned by Docusigned by Docusigned by SIG. INVENTORY NO.	<u>OR</u> <u>11/08/2021</u> 03-1153