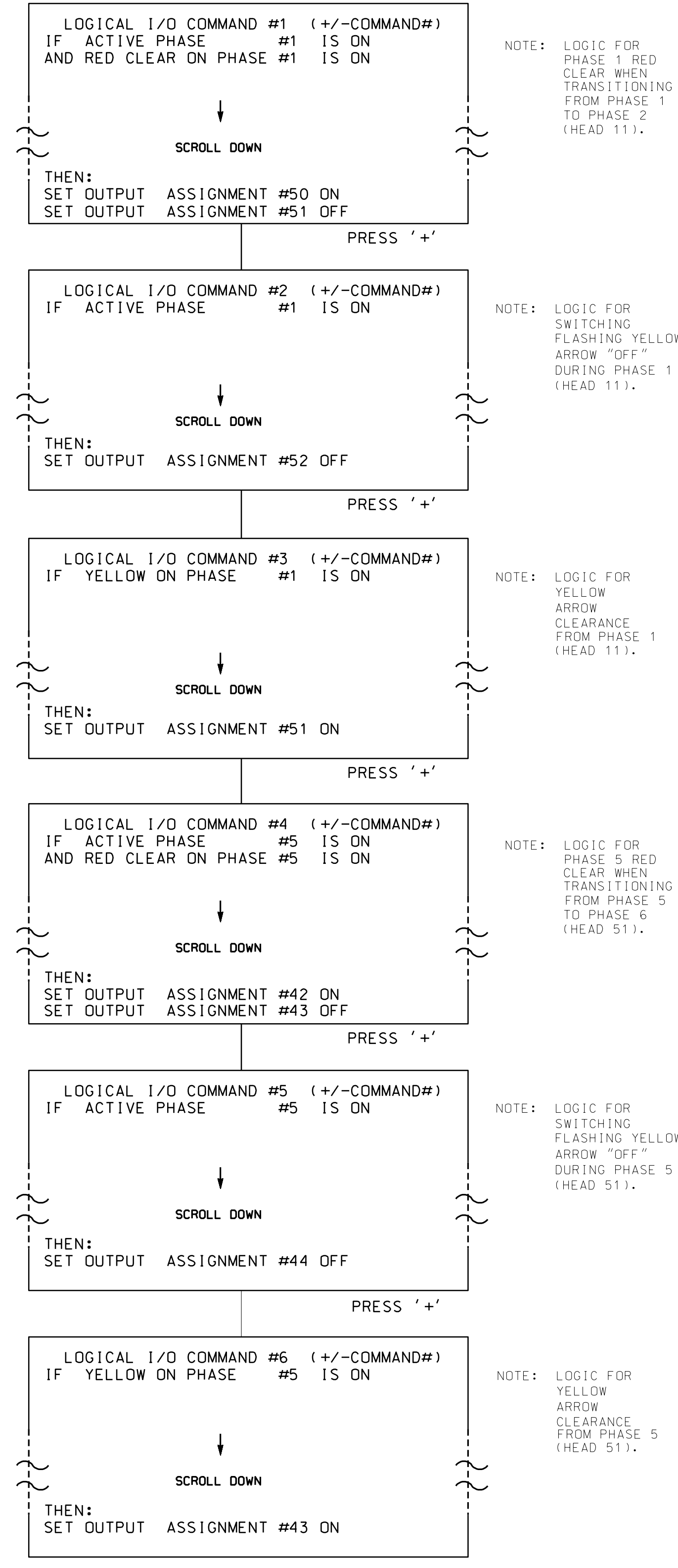


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

| |
|------------------------------|
| OUTPUT 42 = Overlap C Red |
| OUTPUT 43 = Overlap C Yellow |
| OUTPUT 44 = Overlap C Green |
| OUTPUT 50 = Overlap A Red |
| OUTPUT 51 = Overlap A Yellow |
| OUTPUT 52 = Overlap A Green |

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW _ GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW _ GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 03-0752
 DESIGNED: Nov 2021
 SEALED: 11/08/2021
 REVISED: N/A

Electrical Detail - Final Design
Sheet 2 of 5

| <p>RKA RAMEY KEMP ASSOCIATES 8808 Farringdon Place Raleigh, North Carolina 27609 Phone: 919-872-8115 www.rameykemp.com NC License No. C-0910</p> | <p>ELECTRICAL AND PROGRAMMING DETAILS FOR: US 17 (Military Cutoff Road) at Drysdale Drive</p> <p>Division 3 New Hanover County Wilmington</p> <p>PLAN DATE: November 2021 REVIEWED BY: WJ Hamilton</p> <p>PREPARED BY: A. Andrews RKA PROJ. NO.: 19258 (040)</p> | <p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p>SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 32396 WILLIAM J. HAMILTON</p> <p>DocuSigned by: <i>William J. Hamilton</i> 11/08/2021</p> <p>SIGNATURE DATE</p> | | | | | | | | | | | |
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