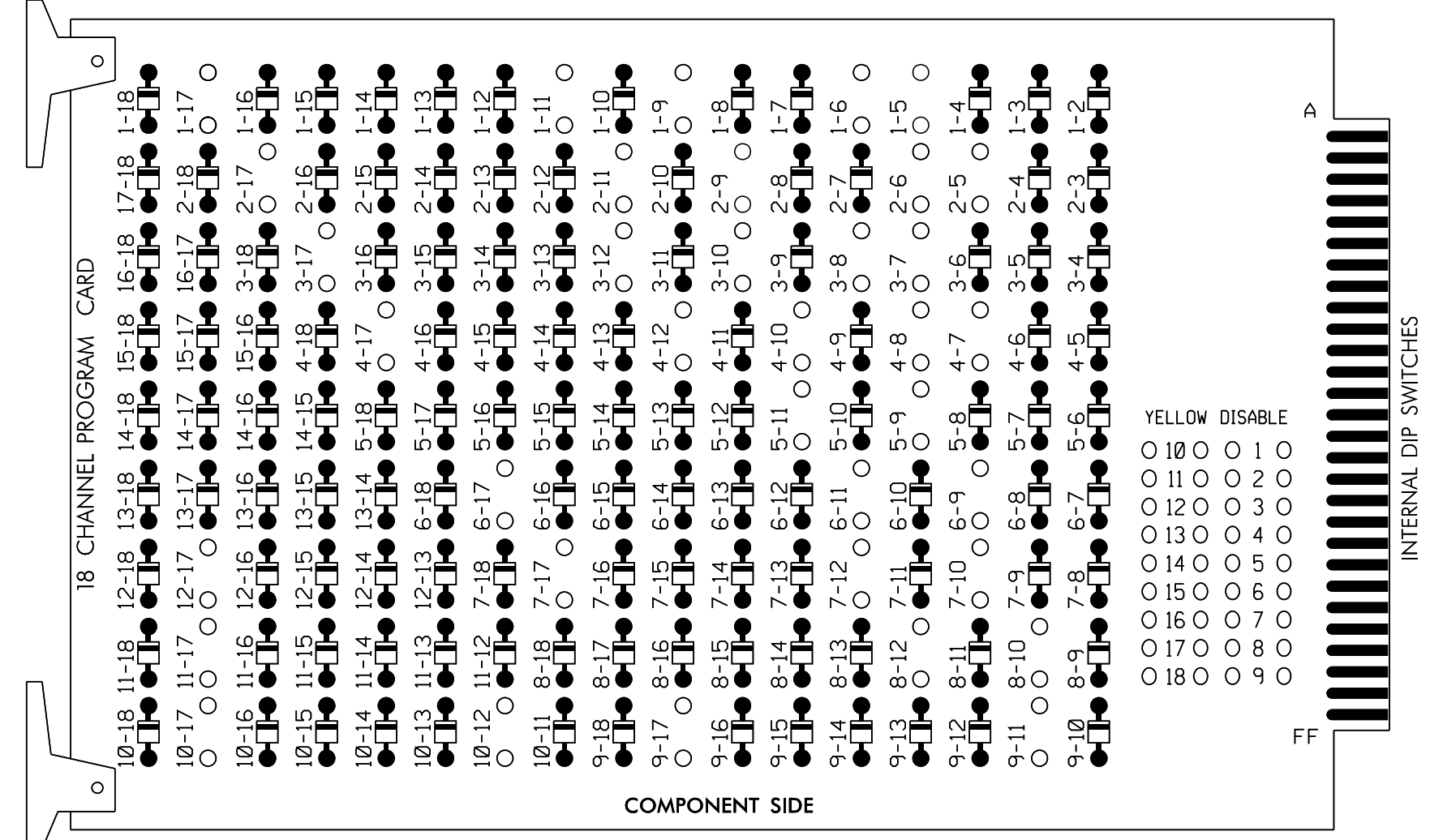


### EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)  
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-17, 2-5, 2-6, 2-9, 2-11, 2-17, 3-7, 3-8, 3-10, 3-12, 3-17, 4-7, 4-8, 4-10, 4-12, 4-17, 5-9, 5-11, 6-9, 6-11, 6-17, 7-10, 7-12, 7-17, 8-10, 8-12, 9-11, 9-17, 10-12, 10-17, 11-17, and 12-17.



- NOTES:
- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
  - 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
  - 3. Ensure that Red Enable is active at all times during normal operation.
  - 4. Integrate monitor with Ethernet network in cabinet.

### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green and 6 Green.
- 4. The cabinet and controller are part of the Fayetteville Signal System.

### EQUIPMENT INFORMATION

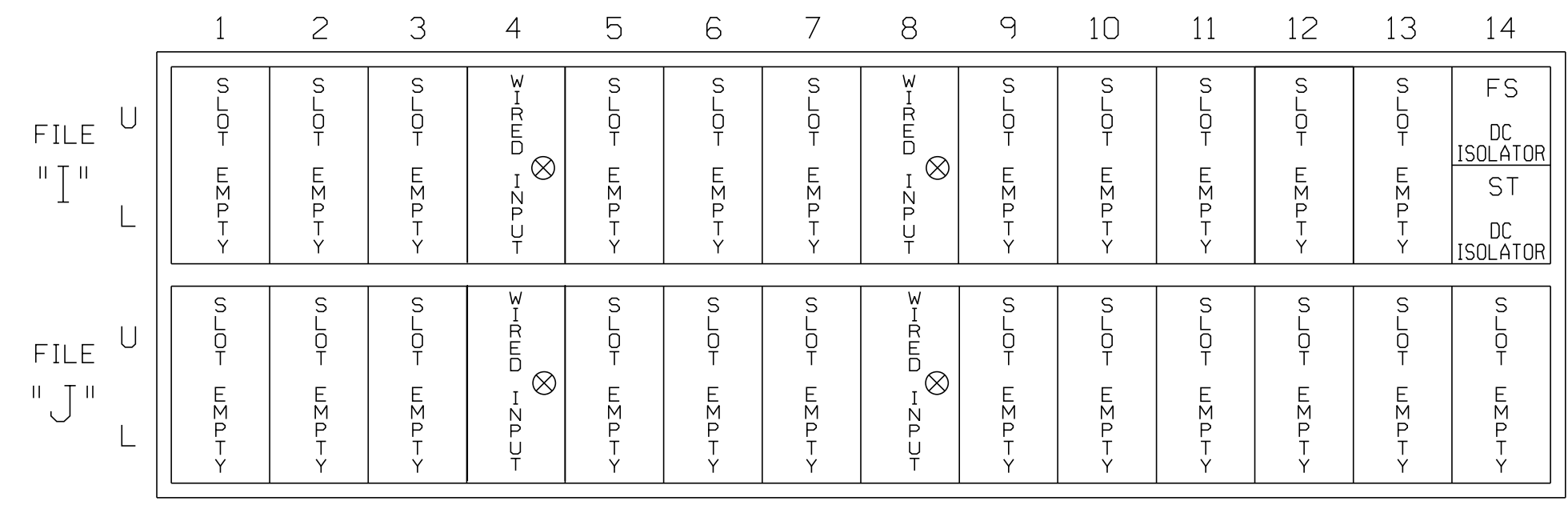
CONTROLLER.....2070LX  
CABINET.....332 W/AUX  
SOFTWARE.....ECONOLITE ASC/3-2070  
CABINET MOUNT.....BASE  
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,  
S10,S11,AUX S1,AUX S2,AUX S3,AUX S4,AUX S5  
PHASES USED.....1,2,3,4,5,6,7,8  
OVERLAP "A".....\*  
OVERLAP "B".....\*  
OVERLAP "C".....\*  
OVERLAP "D".....\*  
OVERLAP "E".....\*  
\* See overlap programming detail on sheet 2

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6						
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18						
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	OLE	OLC	OLD	SPARE						
SIGNAL HEAD NO.	11★	82	21,22	NU	22	31★	41,42	NU	42	51★	61,62	NU	71★	81,82	NU	11★	31★	63★	51★	71★	NU			
RED	*	128		*		101		*		134									A111					
YELLOW		129				102				135		*		108										
GREEN		130				103				136				109										
RED ARROW																			A121	A124	A114	A101		
YELLOW ARROW		126				117				132									A122	A125	A112	A115	A102	
FLASHING YELLOW ARROW																			A123	A126	A113	A116	A103	
GREEN ARROW	127	127								118	118			133	133									124

NU = Not Used  
\* Denotes install load resistor. See load resistor installation detail this sheet.  
★ See pictorial of head wiring in detail on sheet 2.

### INPUT FILE POSITION LAYOUT (front view)



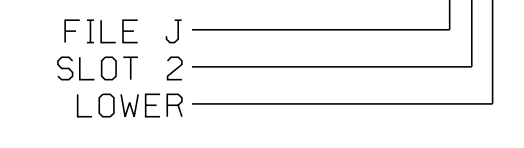
EX. : 1A, 2A, ETC. = LOOP NO.'S  
FS = FLASH SENSE  
ST = STOP TIME  
⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A <sup>1</sup>	TB2-1,2	I1U	56	1★	1	YES		15	---	N
	-	J4U	48	26★	6	YES		3	---	G
3A <sup>2</sup>	TB4-5,6	I5U	58	3★	3	YES		15	---	N
	-	J8U	50	28★	8	YES			---	N
5A <sup>3</sup>	TB3-1,2	J1U	55	5★	5	YES		15	---	N
	-	I4U	47	22★	2	YES		3	---	G
7A <sup>4</sup>	TB5-5,6	J5U	57	7★	7	YES		15	---	N
	-	I8U	49	24★	4	YES			---	N

- <sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.
- <sup>2</sup>Add jumper from I5-W to J8-W, on rear of input file.
- <sup>3</sup>Add jumper from J1-W to I4-W, on rear of input file.
- <sup>4</sup>Add jumper from J5-W to I8-W, on rear of input file.

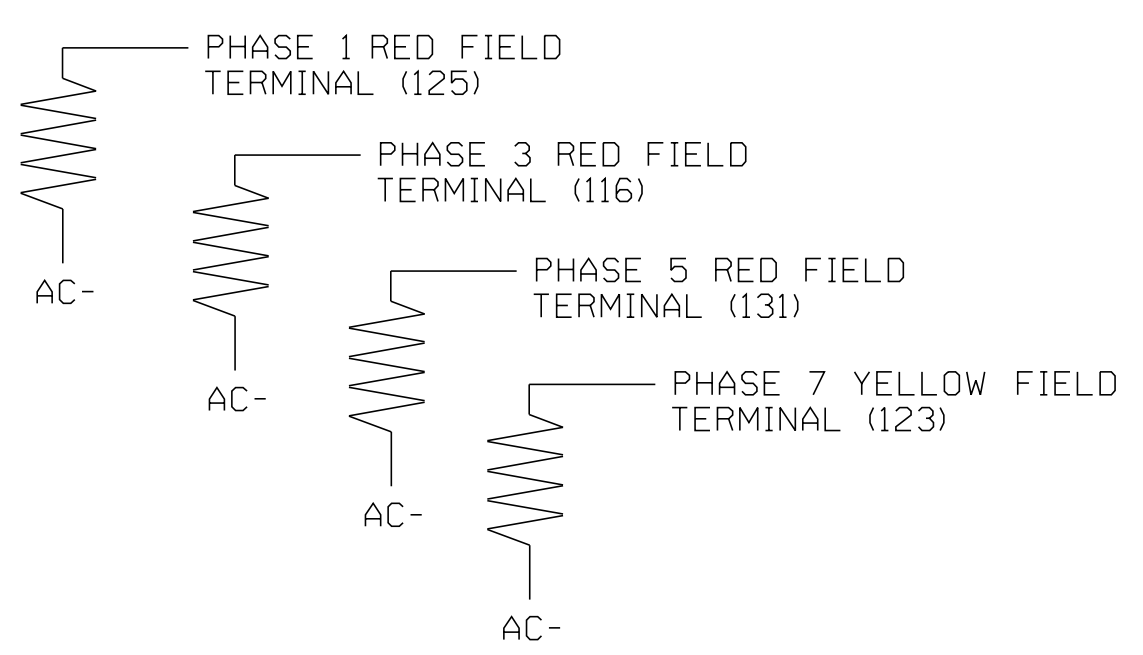
★ See vehicle detector setup programming detail for alternate phasing on sheet 3. INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.  
For Detection Zones 1A, 3A, 5A and 7A, the equipment placement and slots reserved for wired inputs are typical for a NCDOT installation.

### FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

- 1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
- 2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
- 3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1131T2  
DESIGNED: January 2022  
SEALED: 1/5/2022  
REVISED: N/A

Temporary Signal 2 - TCP Phase III  
Electrical Detail - Sheet 1 of 4

Document control and signature block including project details (SR 1102, SR 1112), dates, signatures, and a professional seal for Steven G. Haynie, Engineer.