ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to "Overlap G"

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```
TMG VEH OVLP...[G] TYPE: ......NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . X . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle once

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```
TMG VEH OVLP...[H] TYPE: ......NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . . . X . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle to "Overlap B"

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```
TMG VEH OVLP...[B] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....    OVERLAP  G
OPPOSING THROUGH.......    PHASE    6

FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE...........O
```

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....    PHASE    5
OPPOSING THROUGH.......    PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE...........O
```

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```
TMG VEH OVLP...[D] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....    OVERLAP  H
OPPOSING THROUGH.......    PHASE    4

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE...........O
```
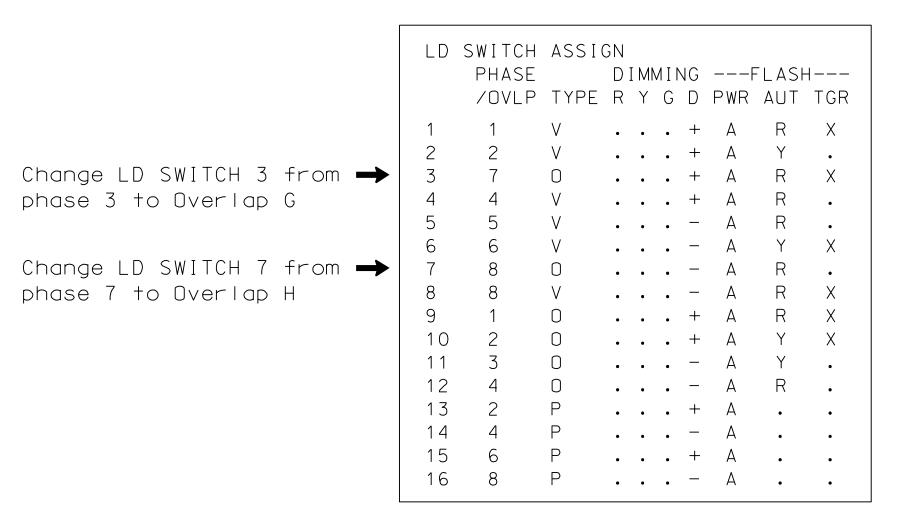
ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL
(program controller as shown)

To assign load switches S4 and S10 as OLG and OLH,
program LD SWITCH 3 as OVLP '7' TYPE 'O' and
LD SWITCH 7 as OVLP '8' TYPE 'O' as shown below.

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN

	PHASE /OVLP	TYPE	R	Y	G	DIMMING D	PWR	FLASH AUT	TGR
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	Y	.
3	7	O	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	−	A	R	.
6	6	V	.	.	.	−	A	Y	X
7	8	O	.	.	.	−	A	R	.
8	8	V	.	.	.	−	A	R	X
9	1	V	.	.	.	+	A	R	X
10	2	O	.	.	.	+	A	Y	X
11	3	O	.	.	.	−	A	Y	.
12	4	O	.	.	.	−	A	R	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	+	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	−	A	.	.

Change LD SWITCH 3 from phase 3 to Overlap G ➡

Change LD SWITCH 7 from phase 7 to Overlap H ➡

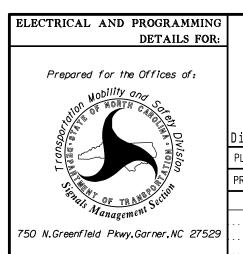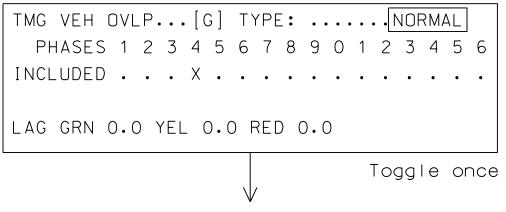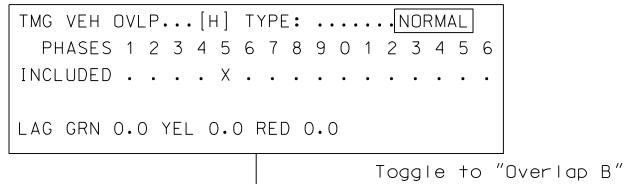# FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA — REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA — REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-1243
DESIGNED: JANUARY 2022
SEALED: 1/5/2022
REVISED: N/A
```

Final Signal Upgrade
Electrical Detail - Sheet 2 of 2

SEAL
NORTH CAROLINA
PROFESSIONAL
SEAL
027930
ENGINEER
JONATHAN D. REID

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

2022.01.05
14:58:58-05'00'
DATE

SIG. INVENTORY NO. 06-1243