ECONOLITE ASC/3-2070 OVERLA

(program controller

1. From Main Menu select 2. CON

2. From CONTROLLER Submenu selec

	overlap g
S	Select TMG VEH OVLP [G] and
	TMG VEH OVLP[G] TYPE: PHASES 1 2 3 4 5 6 7 8 9
	INCLUDED · · · X · · · ·
	LAG GRN 0.0 YEL 0.0 RED 0.0
_	
	overlap b
S	Select TMG VEH OVLP [B] and
	TMG VEH OVLP[B] TYPE: .
	PROTECTED LEFT TURN OPPOSING THROUGH
	FLASHING ARROW OUTPUT
	DELAY START OF: FYAO.O C ACTION PLAN SF BIT DISABLE
	\bigvee
	OVERLAP C
	Select TMG VEH OVLP [C] and
	TMG VEH OVLP[C] TYPE: .
	PROTECTED LEFT TURN OPPOSING THROUGH
	FLASHING ARROW OUTPUT
	DELAY START OF: FYAO.O C Action plan sf bit disable
	OVERLAP D
S	Select TMG VEH OVLP [D] and
ſ	TMG VEH OVLP[D] TYPE:
	PHASES 1 2 3 4 5 6 7 8 9 INCLUDED X
	LAG GRN 0.0 YEL 0.0 RED 0.0

LAP PROGRAMMING DETAIL		
r as shown)	LOAD	<u>EC(</u>
DNTROLLER		000
ect 2. VEHICLE OVERLAPS		
Toggle to "Overlap G"	To assign LD SWITCH	
nd 'NORMAL'		
NORMAL 9 0 1 2 3 4 5 6	1. From Mai 2. From CON	
).0		LD
Toggle to "Overlap B"		
nd 'PPLT FYA'	Change LD SWITCH 3 from 🛶	1 2 3
····PPLT FYA	phase 3 to Overlap G	4 5
OVERLAP G PHASE 6		6 7 8
CH10 ISOLATE		9 10
CLEARANCEO.O		11
LE0 Toggle Once		12 13
		14 15 16
nd 'PPLT FYA'		10
····PPLT FYA		
PHASE 5 PHASE 6		
CH11 ISOLATE		
CLEARANCEO.O LEO		
Toggle Once		
nd 'NORMAL'		
NORMAL 9 0 1 2 3 4 5 6		
		Т
).0		E
	ARCAD	<u>ام </u>
	5420 Wade Park Boulev Suite 350 Raleigh, NC 27607	
	NGIEIGII, NC 21001	

PROJECT REFERENCE NO.	SHEET NO.
U - 5798A	Sig. 7.2

CONOLITE ASC/3-2070 WITCH ASSIGNMENT DETAIL

(program controller as shown)

ad switch S4 as OLG, program as OVLP '7' TYPE '0' as shown below.

Menu select 1. CONFIGURATION

GURATION Submenu select 3. LOAD SW ASSIGN

)	SWITCH PHASE /OVLP	ASSI (TYPE		[MN Y	ЛІР G	NG D	F PWR	- L A S F A U T	H TGR
	1	V			•	+	А	R	Х
	2	V	•	•	•	+	A	Y	•
	7	0	•	•	•	+	А	R	Х
	4	V	•	•	•	+	А	R	•
	5	V	•	•	•	—	А	R	•
	6	V	•	•	•	—	А	Y	Х
	7	V	•	•	•	—	А	R	•
	8	V	•	•	•	—	А	R	Х
	1	0	•	•	•	+	А	R	Х
)	2	0	•	•	•	+	А	Y	Х
	3	0	•	•	•	—	А	Y	•
) -	4	0	•	•	•	—	А	R	•
5	2	Ρ	•	•	•	+	А	•	•
ł	4	Ρ	•	•	•	—	А	•	•
)	6	Ρ	•	•	•	+	А	•	•
>	8	Ρ	•	•	•	—	А	•	•

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1243T2 DESIGNED: JANUARY 2022 SEALED: 1/5/2022 REVISED: N/A

Temporary Signal 2 - TCP Phase III Electrical Detail - Sheet 2 of 2

Electrical Detai					DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of:	SR 1102 (Gil) a Shopping Cer Division 6 Cumberla PLAN DATE: January 2022 PREPARED BY: C. Evans REVISIONS	at	/eway	/ :teville	THAN D.
750 N.Greenfield Pkwy,Garner,NC 27529		· · · · · · · · · · · · · · · · · · ·			Jonathur D Reid 2022.01.05 14:58:57-05'00' DATE
					SIG. INVENTORY NO. 06-1243T2