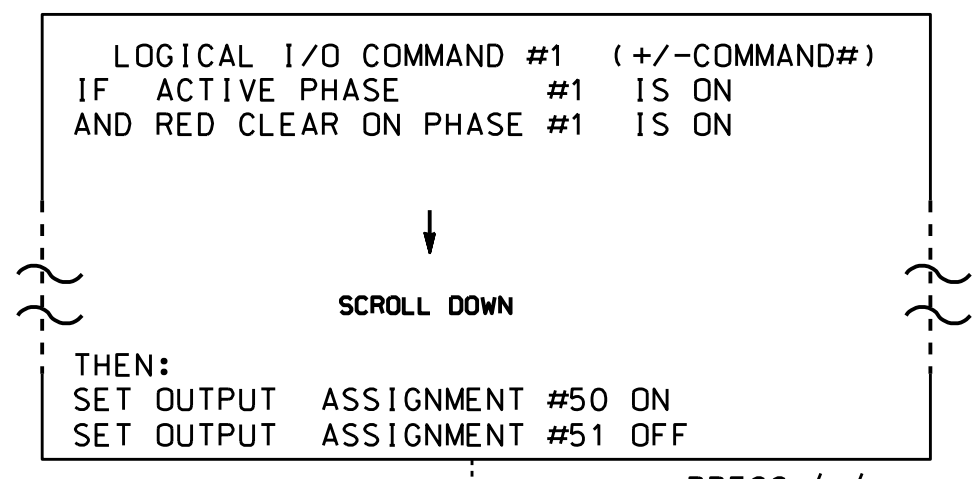


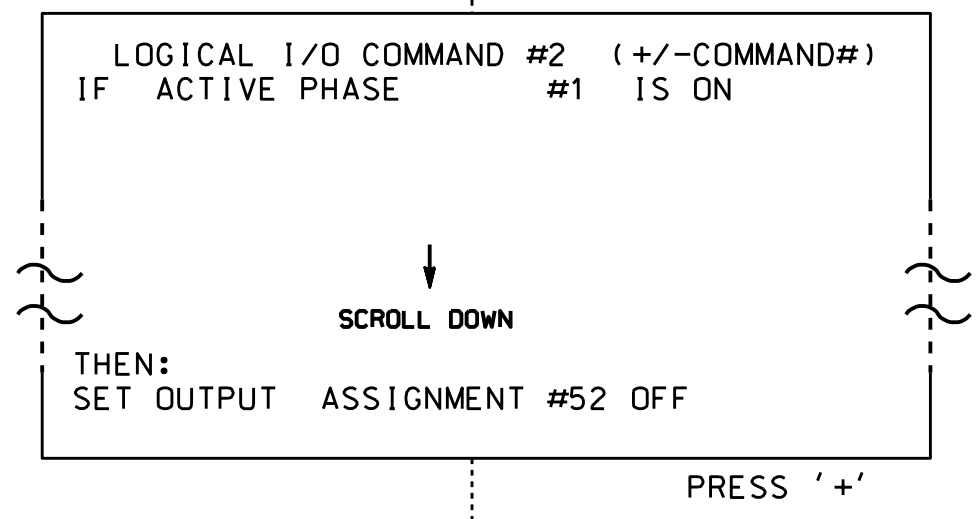
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

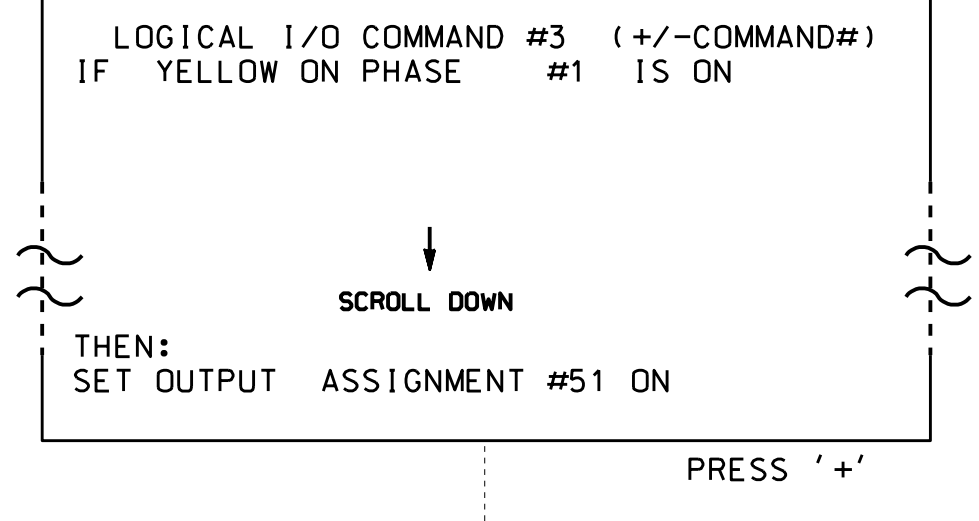
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8 AND 9.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



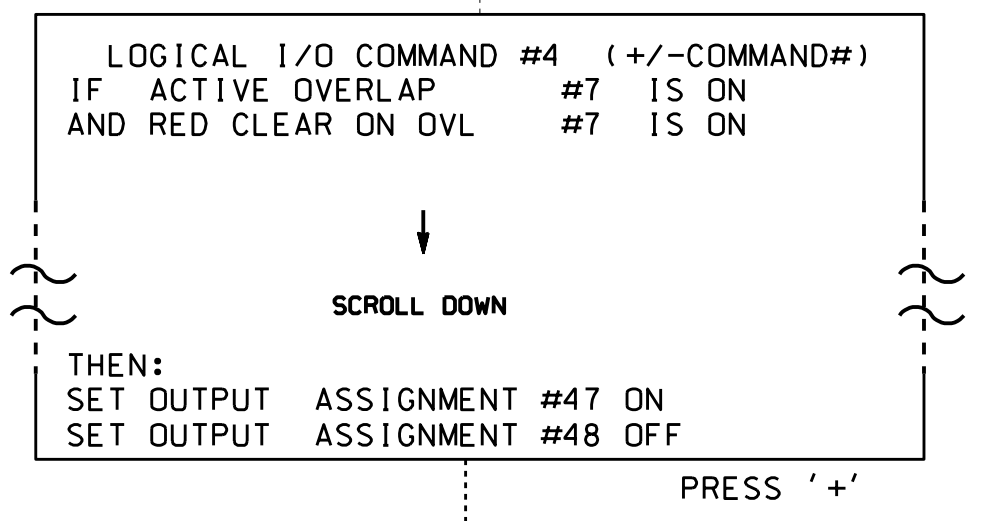
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



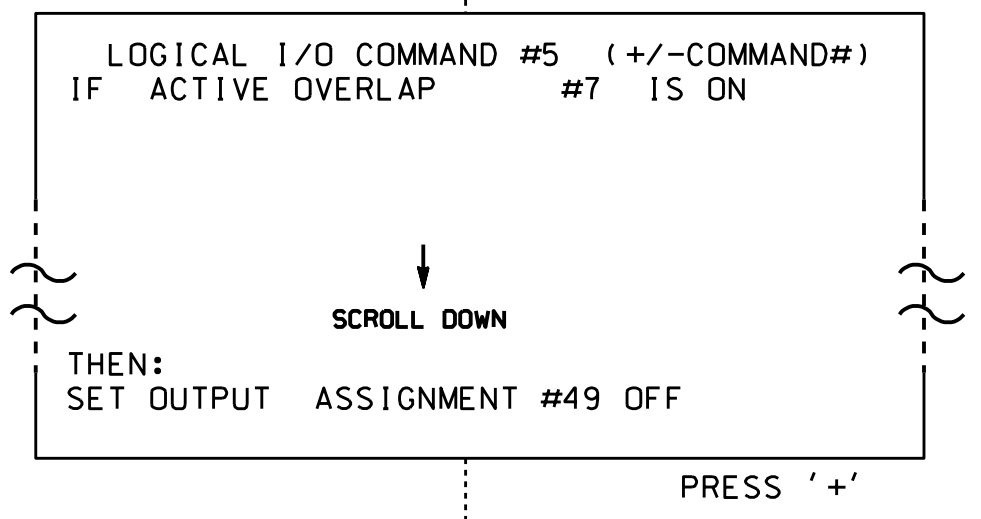
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).



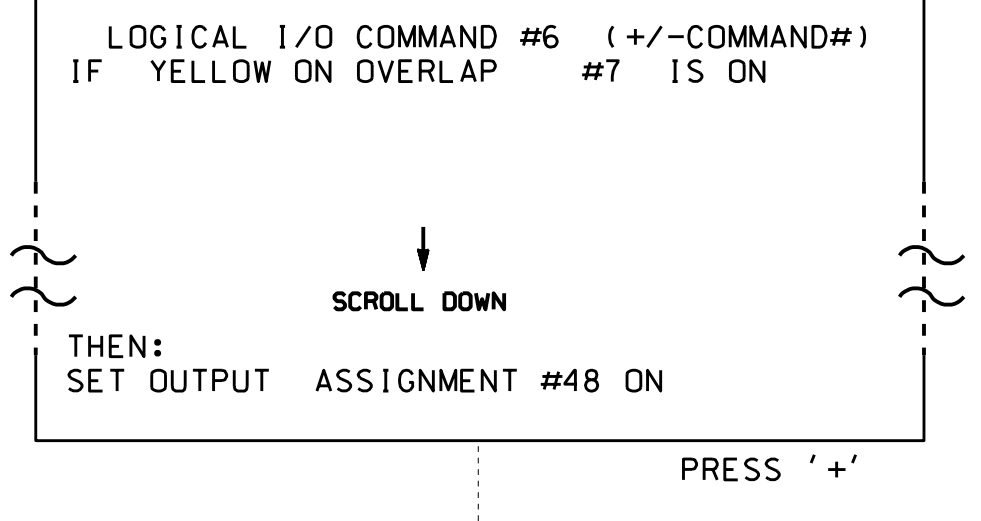
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



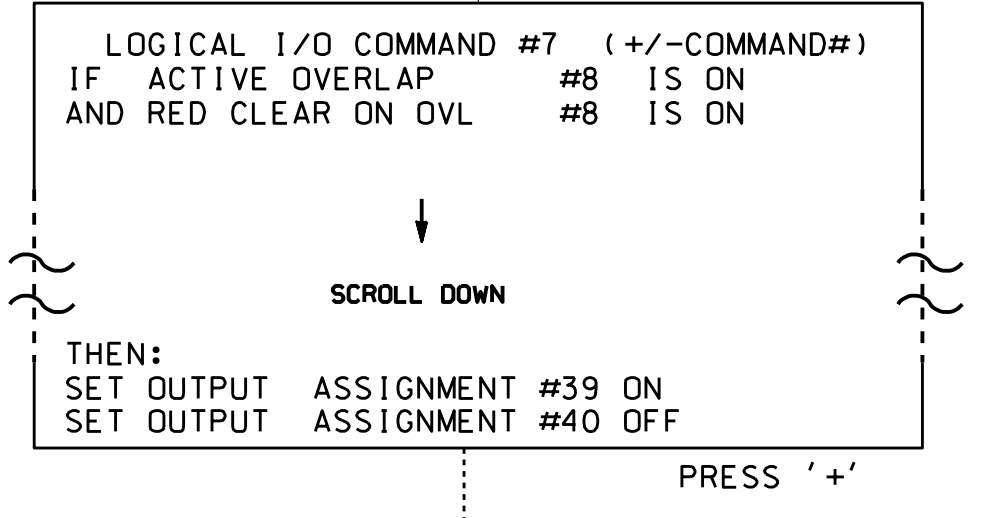
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 (HEAD 43).



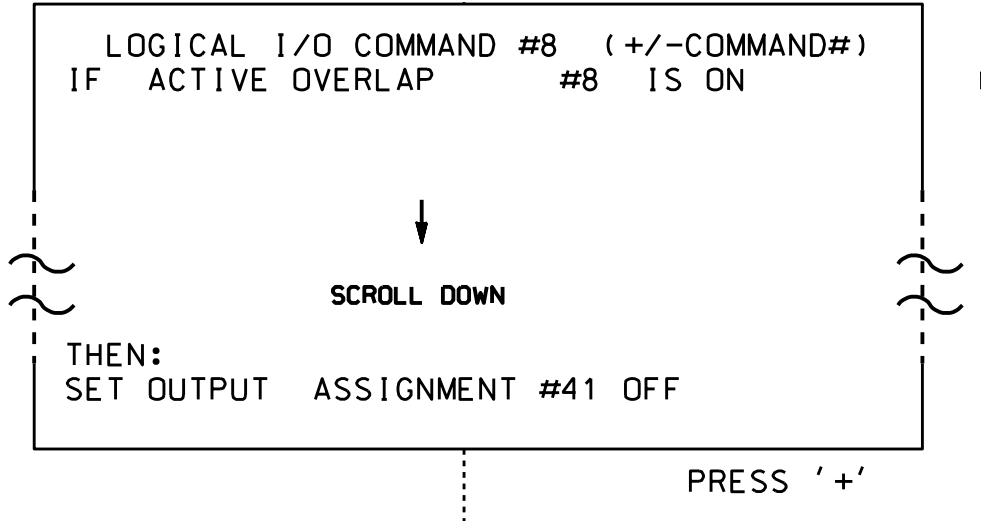
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 43).



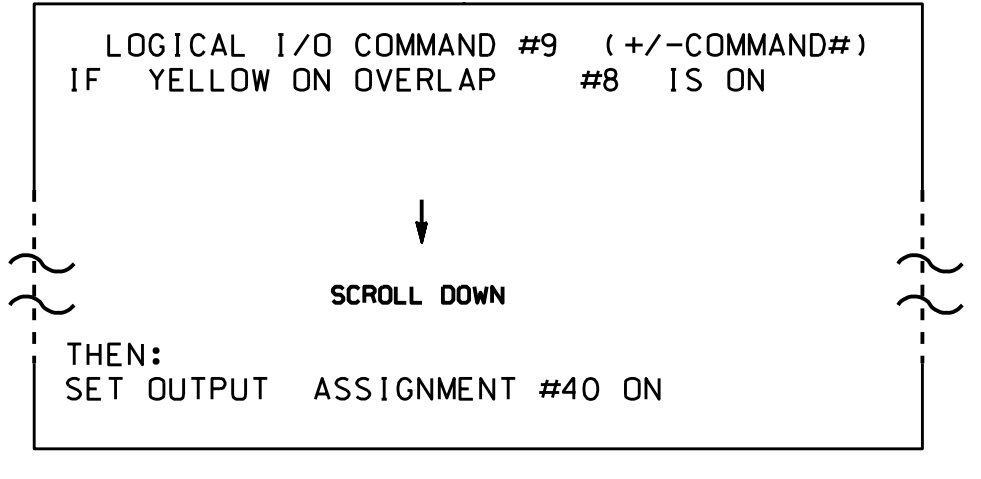
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 43).



NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 (HEAD 23).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 23).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 23).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

COUNTDOWN PED SIGNALS ARE REQUIRED TO DISPLAY TIMING ONLY DURING PED CLEARANCE INTERVAL. CONSULT PED SIGNAL MODULE USER'S MANUAL FOR INSTRUCTIONS ON SELECTING THIS FEATURE.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

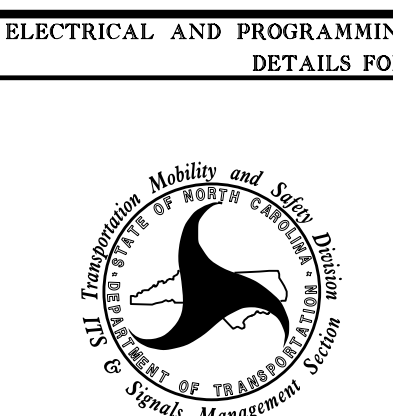
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-3.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-2.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 39	= Overlap D Red
OUTPUT 40	= Overlap D Yellow
OUTPUT 41	= Overlap D Green
OUTPUT 47	= Overlap B Red
OUTPUT 48	= Overlap B Yellow
OUTPUT 49	= Overlap B Green
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0640
DESIGNED: January 2022
SEALED: 1/27/2022
REVISED: N/A

Electrical Detail - Sheet 5 of 7

 <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>US 601 (Jake Alexander Blvd S) at SR 2528 (Julian Rd) and Martin Luther King Jr Ave</p> <p>Division 9 Rowan County Salisbury</p> <p>PLAN DATE: January 2022 REVIEWED BY: T. Joyce</p> <p>PREPARED BY: C. Strickland REVIEWED BY:</p>	<p>SEAL</p> <p>STATE OF NORTH CAROLINA PROFESSIONAL ENGINEER D. TODD JOYCE 031001</p> <p>DocuSigned by: D. Todd Joyce 01/28/2022</p> <p>SIG. INVENTORY NO. 09-0640</p>
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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED