

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS)... 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#) IF ACTIVE PHASE #1 IS ON AND RED CLEAR ON PHASE #1 IS ON

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#) IF ACTIVE PHASE #1 IS ON

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#) IF YELLOW ON PHASE #1 IS ON

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#) IF ACTIVE PHASE #5 IS ON AND RED CLEAR ON PHASE #5 IS ON

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#) IF ACTIVE PHASE #5 IS ON

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#) IF YELLOW ON PHASE #5 IS ON

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE table with 2 columns: Output Number, Output Description (e.g., OUTPUT 42 = Overlap C Red)

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS PHASE: 12345678910111213141516

NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS PHASE: 12345678910111213141516

NOTICE PAGE 2

PRESS '+' TWICE

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516

NOTICE PAGE 2

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0987 DESIGNED: NOVEMBER 2021

Electrical Detail - Sheet 2 of 5

Professional Engineer Seal for Zhaolong Teng, State of North Carolina, License No. 032179. Includes project details and signature area.

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